



SPEC® CINT2006 Result

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Itautec

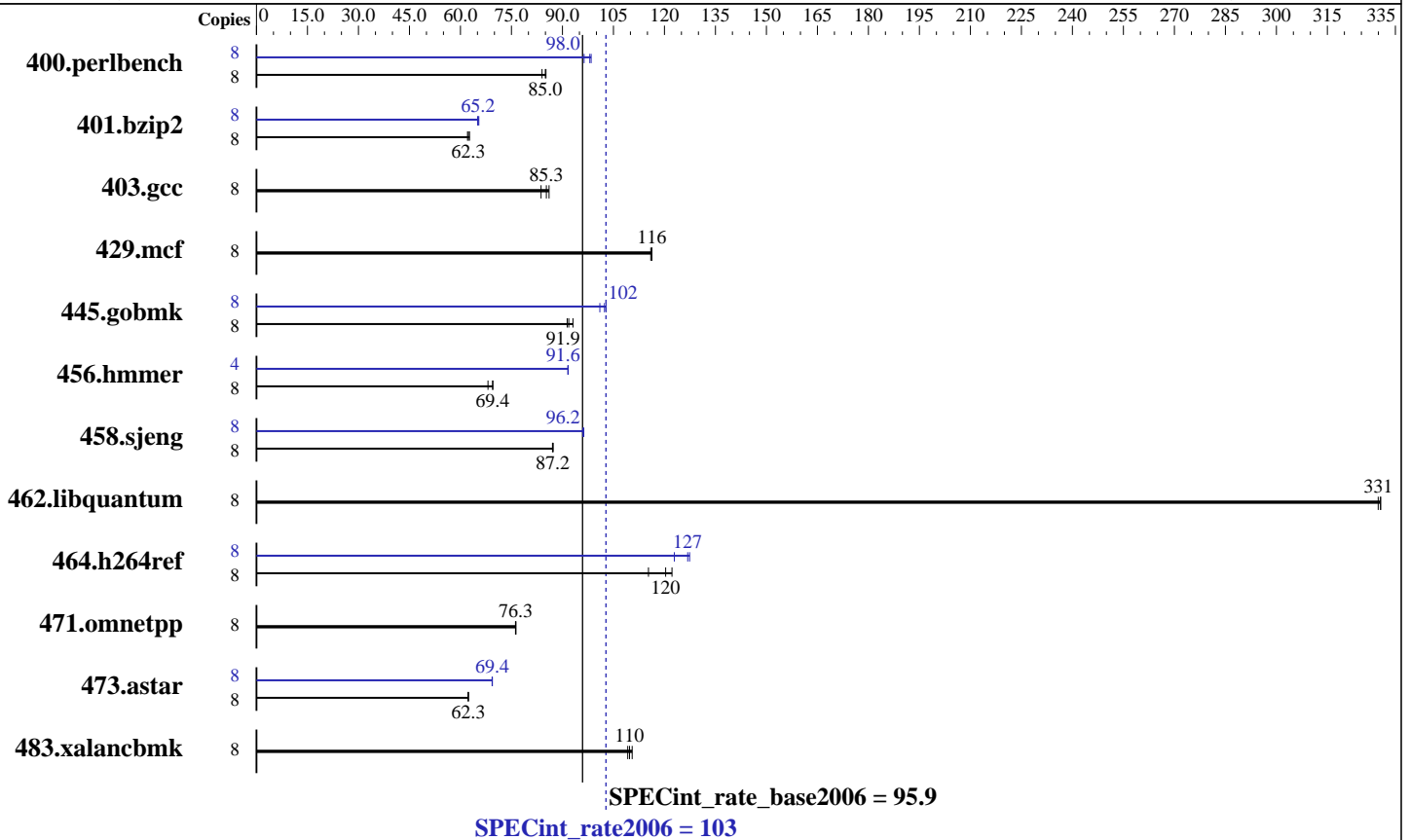
SPECint®_rate2006 = 103

Servidor Itautec MX223 (Intel Xeon E5520)

SPECint_rate_base2006 = 95.9

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Dec-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5520
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 12 GB (3 x 4GB DDR3-1066, CL 7, ECC)
 Disk Subsystem: 1 x 160 GB SATA-2, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	920	85.0	931	84.0	918	85.2	8	797	98.0	794	98.4	811	96.4
401.bzip2	8	1244	62.1	1238	62.3	1232	62.7	8	1184	65.2	1187	65.1	1180	65.4
403.gcc	8	748	86.1	769	83.7	755	85.3	8	748	86.1	769	83.7	755	85.3
429.mcf	8	629	116	627	116	627	116	8	629	116	627	116	627	116
445.gobmk	8	901	93.1	914	91.9	918	91.4	8	816	103	820	102	831	101
456.hammer	8	1073	69.5	1096	68.1	1075	69.4	4	407	91.6	407	91.6	407	91.7
458.sjeng	8	1112	87.1	1109	87.3	1110	87.2	8	1006	96.2	1005	96.3	1008	96.0
462.libquantum	8	502	330	501	331	501	331	8	502	330	501	331	501	331
464.h264ref	8	1449	122	1471	120	1535	115	8	1390	127	1396	127	1440	123
471.omnetpp	8	655	76.3	656	76.2	655	76.3	8	655	76.3	656	76.2	655	76.3
473.astar	8	901	62.3	902	62.3	901	62.3	8	810	69.3	810	69.4	809	69.4
483.xalanbmk	8	500	111	503	110	505	109	8	500	111	503	110	505	109

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

General Notes

This result was measured on the Servidor Itaotec MX223.
The Servidor Itaotec MX223 and the Servidor Itaotec MX203 are electronically equivalent.
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LINUX
```



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs
-L/home/richard/sh/SmartHeap_8.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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