



SPEC® CINT2006 Result

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ACTION S.A.

SPECint®_rate2006 = 135

ACTINA SOLAR 200 S4+ (Intel Xeon E5504)

SPECint_rate_base2006 = 126

CPU2006 license: 9008

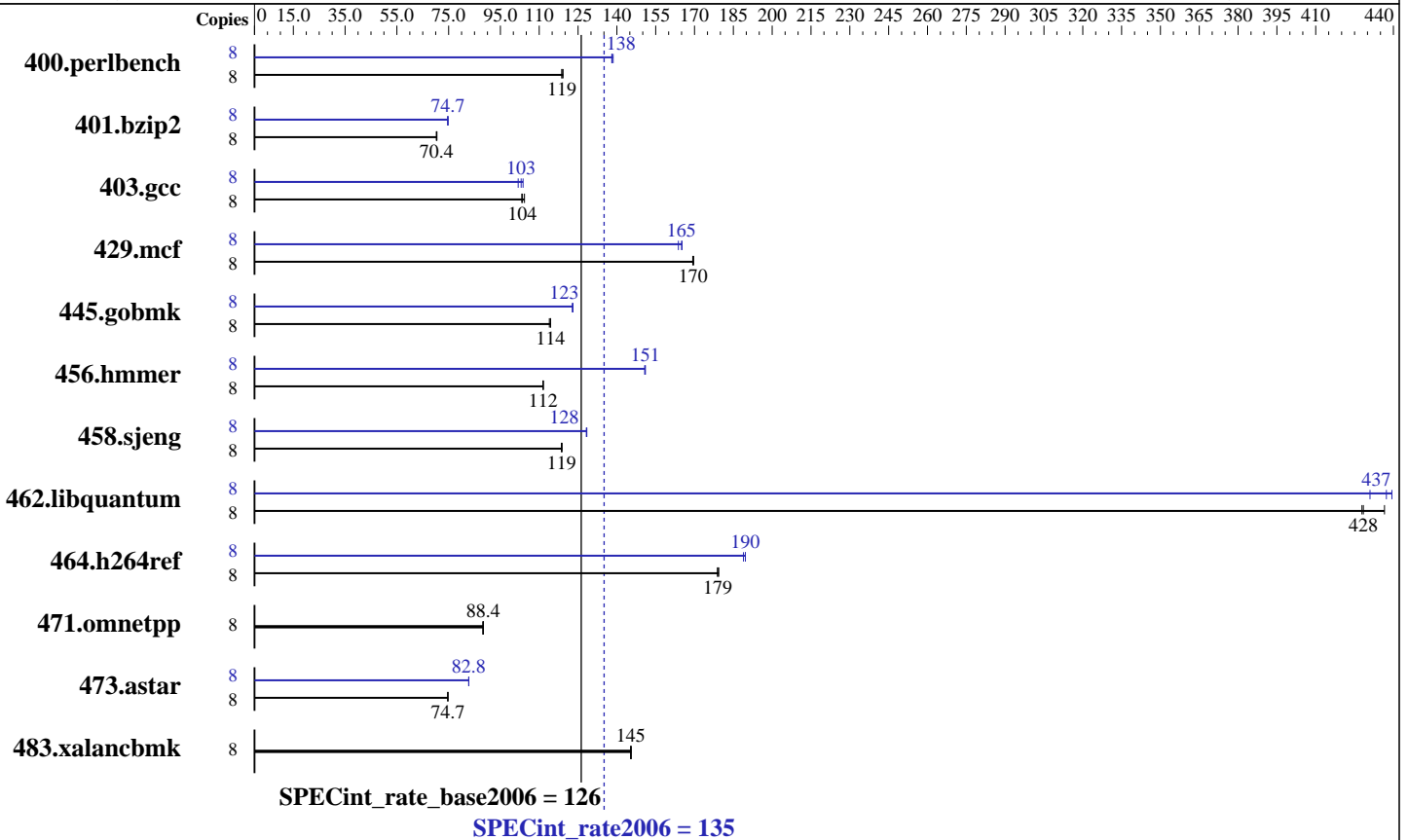
Test date: Nov-2009

Test sponsor: ACTION S.A.

Hardware Availability: Apr-2009

Tested by: ACTION S.A.

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5504
 CPU Characteristics:
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB PC3-8500, 1066 MHz, DDR3, ECC, downclocked to 800 MHz)
 Disk Subsystem: 160 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502
 Microquill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|------------|-------------|------------|------------|-------------|-------------|--------|-------------|-------------|------------|------------|------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 656 | 119 | 658 | 119 | 658 | 119 | 8 | 566 | 138 | 564 | 139 | 566 | 138 |
| 401.bzip2 | 8 | 1099 | 70.3 | 1095 | 70.5 | 1097 | 70.4 | 8 | 1033 | 74.7 | 1035 | 74.6 | 1033 | 74.8 |
| 403.gcc | 8 | 617 | 104 | 624 | 103 | 622 | 104 | 8 | 621 | 104 | 625 | 103 | 632 | 102 |
| 429.mcf | 8 | 430 | 170 | 430 | 170 | 431 | 169 | 8 | 446 | 164 | 442 | 165 | 442 | 165 |
| 445.gobmk | 8 | 734 | 114 | 737 | 114 | 734 | 114 | 8 | 682 | 123 | 683 | 123 | 683 | 123 |
| 456.hammer | 8 | 669 | 112 | 670 | 111 | 669 | 112 | 8 | 494 | 151 | 495 | 151 | 494 | 151 |
| 458.sjeng | 8 | 817 | 119 | 815 | 119 | 815 | 119 | 8 | 755 | 128 | 755 | 128 | 754 | 128 |
| 462.libquantum | 8 | 387 | 428 | 387 | 428 | 380 | 437 | 8 | 385 | 431 | 377 | 439 | 379 | 437 |
| 464.h264ref | 8 | 989 | 179 | 987 | 179 | 990 | 179 | 8 | 937 | 189 | 934 | 190 | 933 | 190 |
| 471.omnetpp | 8 | 565 | 88.4 | 565 | 88.5 | 567 | 88.2 | 8 | 565 | 88.4 | 565 | 88.5 | 567 | 88.2 |
| 473.astar | 8 | 752 | 74.7 | 751 | 74.8 | 752 | 74.6 | 8 | 678 | 82.8 | 679 | 82.8 | 678 | 82.8 |
| 483.xalancbmk | 8 | 380 | 145 | 379 | 146 | 380 | 145 | 8 | 380 | 145 | 379 | 146 | 380 | 145 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

'numactl' was used to bind copies to the cores
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

```
icpc
```

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
 -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
 -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap64

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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