



SPEC® CINT2006 Result

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Bull SAS

SPECint®_rate2006 = 106

NovaScale T810 F2 (Intel Xeon X3440, 2.53 GHz)

SPECint_rate_base2006 = 97.4

CPU2006 license: 20

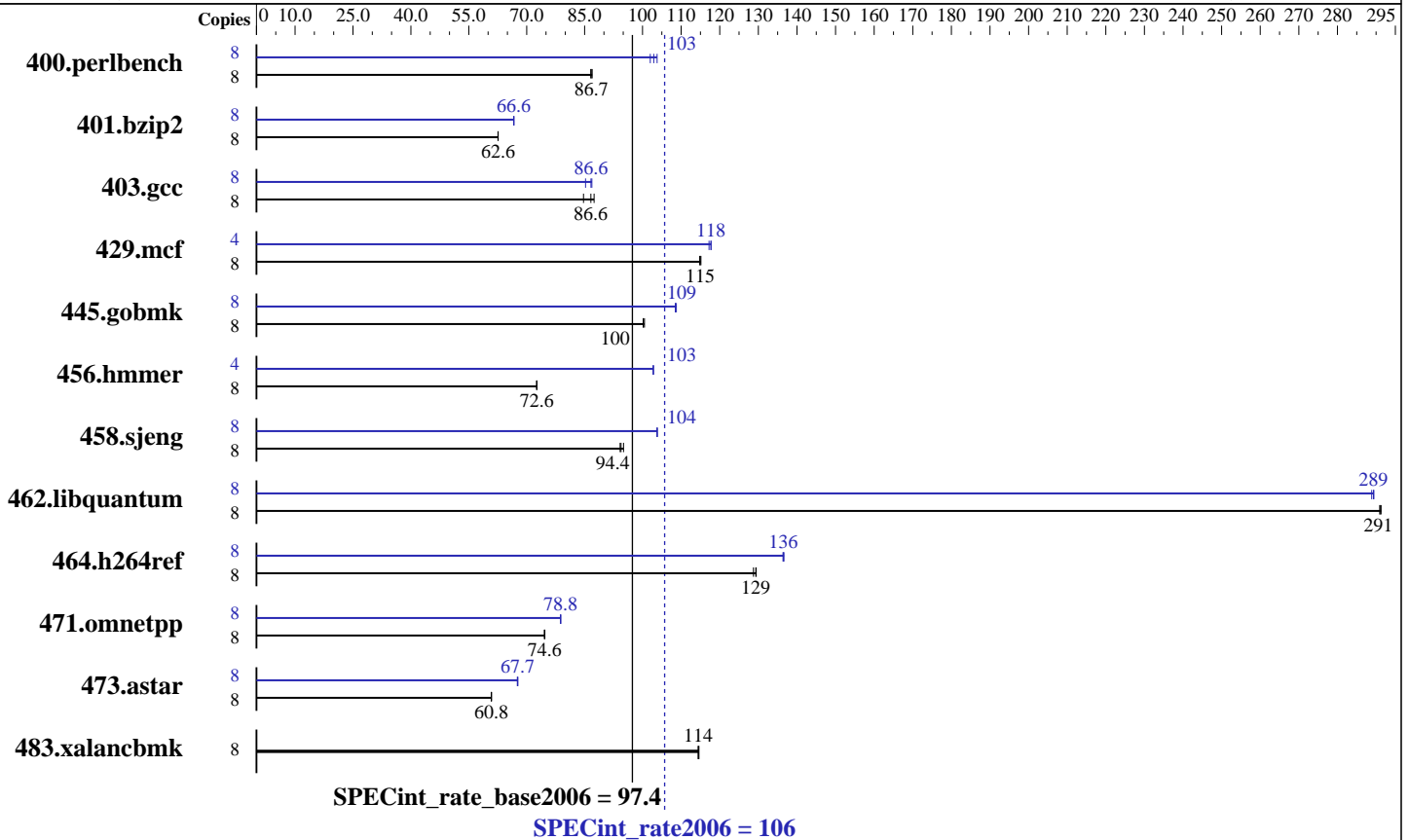
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Oct-2009

Hardware Availability: Dec-2009

Software Availability: Jul-2009



Hardware

CPU Name: Intel Xeon X3440
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz
 CPU MHz: 2533
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (4 x 2 GB DDR3-1333 DR UDIMM)
 Disk Subsystem: 1 x 160 GB 7200 RPM SATA
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5
 Compiler: Intel C++ Compiler Professional Edition 11.1 for Linux Build 20090511 Package ID: I_cproc_p_11.1.040
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	903	86.6	899	87.0	<u>901</u>	<u>86.7</u>	8	<u>759</u>	<u>103</u>	753	104	766	102
401.bzip2	8	<u>1233</u>	<u>62.6</u>	1233	62.6	1234	62.6	8	<u>1158</u>	<u>66.6</u>	1159	66.6	1157	66.7
403.gcc	8	736	87.5	<u>743</u>	<u>86.6</u>	760	84.7	8	<u>743</u>	<u>86.6</u>	741	86.9	755	85.2
429.mcf	8	634	115	<u>635</u>	<u>115</u>	636	115	4	<u>310</u>	<u>118</u>	310	118	311	117
445.gobmk	8	838	100	835	101	<u>837</u>	<u>100</u>	8	773	109	772	109	<u>773</u>	<u>109</u>
456.hammer	8	1028	72.6	<u>1028</u>	<u>72.6</u>	1027	72.7	4	363	103	363	103	<u>363</u>	<u>103</u>
458.sjeng	8	<u>1025</u>	<u>94.4</u>	1028	94.2	1018	95.1	8	932	104	933	104	<u>933</u>	<u>104</u>
462.libquantum	8	<u>570</u>	<u>291</u>	569	291	570	291	8	<u>573</u>	<u>289</u>	574	289	573	289
464.h264ref	8	1375	129	1368	129	<u>1369</u>	<u>129</u>	8	<u>1297</u>	<u>136</u>	1298	136	1296	137
471.omnetpp	8	<u>670</u>	<u>74.6</u>	670	74.6	669	74.7	8	634	78.8	<u>635</u>	<u>78.8</u>	635	78.8
473.astar	8	924	60.8	922	60.9	<u>923</u>	<u>60.8</u>	8	831	67.6	830	67.7	<u>830</u>	<u>67.7</u>
483.xalancbmk	8	482	115	482	114	<u>482</u>	<u>114</u>	8	482	115	482	114	<u>482</u>	<u>114</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

The Dell PowerEdge T110 (Intel Xeon X3440, 2.53 GHz) and the Bull NovaScale T810 F2 (Intel Xeon X3440, 2.53 GHz) models are electronically equivalent. The results have been measured on a Dell PowerEdge T110 (Intel Xeon X3440, 2.53 GHz) model.

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
 -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
 -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

471.omnetpp (continued):

`-L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`

`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`

`-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32`

`-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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