



SPEC[®] CFP2006 Result

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Bull SAS

SPECfp[®]_rate2006 = 78.2

NovaScale R410 F2 (Intel Xeon X3440, 2.53 GHz)

SPECfp_rate_base2006 = 74.6

CPU2006 license: 20

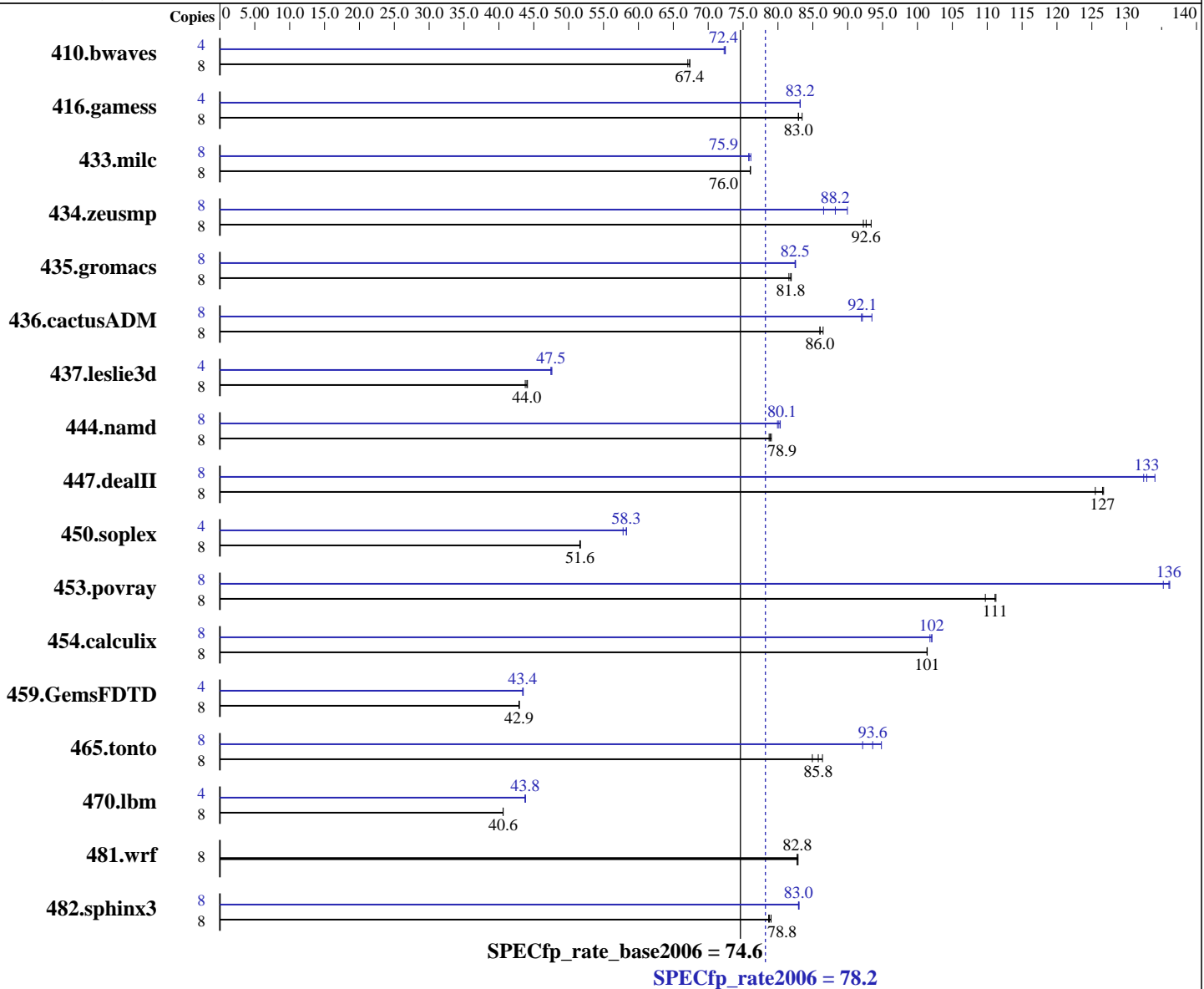
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: Sep-2009

Hardware Availability: Dec-2009

Software Availability: Jul-2009



Hardware

CPU Name: Intel Xeon X3440
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz
 CPU MHz: 2533
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5
 Compiler: Intel Fortran Compiler and Intel C++ Compiler Professional Edition 11.1 For Linux Build 20090511 Package ID: l_cproc_p_11.1.040, l_cprof_p_11.1.040
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (4 x 2 GB DDR3-1333 DR UDIMM)
Disk Subsystem: 1 x 160 GB 7200 RPM SATA
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 8 | 1621 | 67.1 | 1614 | 67.4 | 1614 | 67.4 | 4 | 750 | 72.4 | 752 | 72.3 | 750 | 72.5 |
| 416.gamess | 8 | 1889 | 82.9 | 1888 | 83.0 | 1877 | 83.5 | 4 | 942 | 83.2 | 942 | 83.2 | 941 | 83.2 |
| 433.milc | 8 | 966 | 76.1 | 966 | 76.0 | 966 | 76.0 | 8 | 965 | 76.1 | 968 | 75.9 | 969 | 75.8 |
| 434.zeusmp | 8 | 786 | 92.6 | 789 | 92.2 | 780 | 93.4 | 8 | 841 | 86.5 | 809 | 90.0 | 825 | 88.2 |
| 435.gromacs | 8 | 700 | 81.6 | 697 | 81.9 | 698 | 81.8 | 8 | 693 | 82.5 | 693 | 82.5 | 692 | 82.5 |
| 436.cactusADM | 8 | 1111 | 86.0 | 1111 | 86.0 | 1106 | 86.5 | 8 | 1039 | 92.0 | 1023 | 93.5 | 1038 | 92.1 |
| 437.leslie3d | 8 | 1718 | 43.8 | 1709 | 44.0 | 1705 | 44.1 | 4 | 792 | 47.5 | 790 | 47.6 | 792 | 47.5 |
| 444.namd | 8 | 815 | 78.7 | 813 | 78.9 | 812 | 79.1 | 8 | 799 | 80.3 | 801 | 80.1 | 803 | 79.9 |
| 447.dealII | 8 | 723 | 127 | 729 | 125 | 722 | 127 | 8 | 683 | 134 | 689 | 133 | 691 | 132 |
| 450.soplex | 8 | 1291 | 51.7 | 1292 | 51.6 | 1293 | 51.6 | 4 | 577 | 57.8 | 572 | 58.3 | 573 | 58.3 |
| 453.povray | 8 | 383 | 111 | 388 | 110 | 383 | 111 | 8 | 315 | 135 | 313 | 136 | 313 | 136 |
| 454.calculix | 8 | 651 | 101 | 651 | 101 | 651 | 101 | 8 | 647 | 102 | 648 | 102 | 647 | 102 |
| 459.GemsFDTD | 8 | 1979 | 42.9 | 1978 | 42.9 | 1979 | 42.9 | 4 | 977 | 43.4 | 976 | 43.5 | 977 | 43.4 |
| 465.tonto | 8 | 911 | 86.4 | 918 | 85.8 | 927 | 84.9 | 8 | 830 | 94.8 | 854 | 92.1 | 841 | 93.6 |
| 470.lbm | 8 | 2707 | 40.6 | 2708 | 40.6 | 2707 | 40.6 | 4 | 1256 | 43.8 | 1255 | 43.8 | 1255 | 43.8 |
| 481.wrf | 8 | 1080 | 82.8 | 1078 | 82.9 | 1080 | 82.8 | 8 | 1080 | 82.8 | 1078 | 82.9 | 1080 | 82.8 |
| 482.sphinx3 | 8 | 1982 | 78.7 | 1973 | 79.0 | 1979 | 78.8 | 8 | 1880 | 82.9 | 1878 | 83.0 | 1880 | 83.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)



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General Notes

The Dell PowerEdge R210 (Intel Xeon X3440, 2.53 GHz) and the Bull NovaScale R410 F2 (Intel Xeon X3440, 2.53 GHz) models are electronically equivalent. The results have been measured on a Dell PowerEdge R210 (Intel Xeon X3440, 2.53 GHz) model.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks (except as noted below):

`ifort -m64`

437.leslie3d: `ifort -m32`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
 416.gamess: `-DSPEC_CPU_LP64`
 433.milc: `-DSPEC_CPU_LP64`
 434.zeusmp: `-DSPEC_CPU_LP64`
 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
 444.namd: `-DSPEC_CPU_LP64`
 447.dealII: `-DSPEC_CPU_LP64`
 453.povray: `-DSPEC_CPU_LP64`
 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
 459.GemsFDTD: `-DSPEC_CPU_LP64`
 465.tonto: `-DSPEC_CPU_LP64`
 470.lbm: `-DSPEC_CPU_LP64`
 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`



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Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -opt-malloc-options=3 -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch

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Peak Optimization Flags (Continued)

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-fp-linux64-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 03:53:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 22 December 2009.