# SPEC® CINT2006 Result

## Dell Inc.

$SPECint^®\_rate2006 = $ Not Run

PowerEdge T410 (Intel Xeon E5506, 2.13GHz)

$SPECint\_rate\_base2006 = $ 67.6

**CPU2006 license:** 55
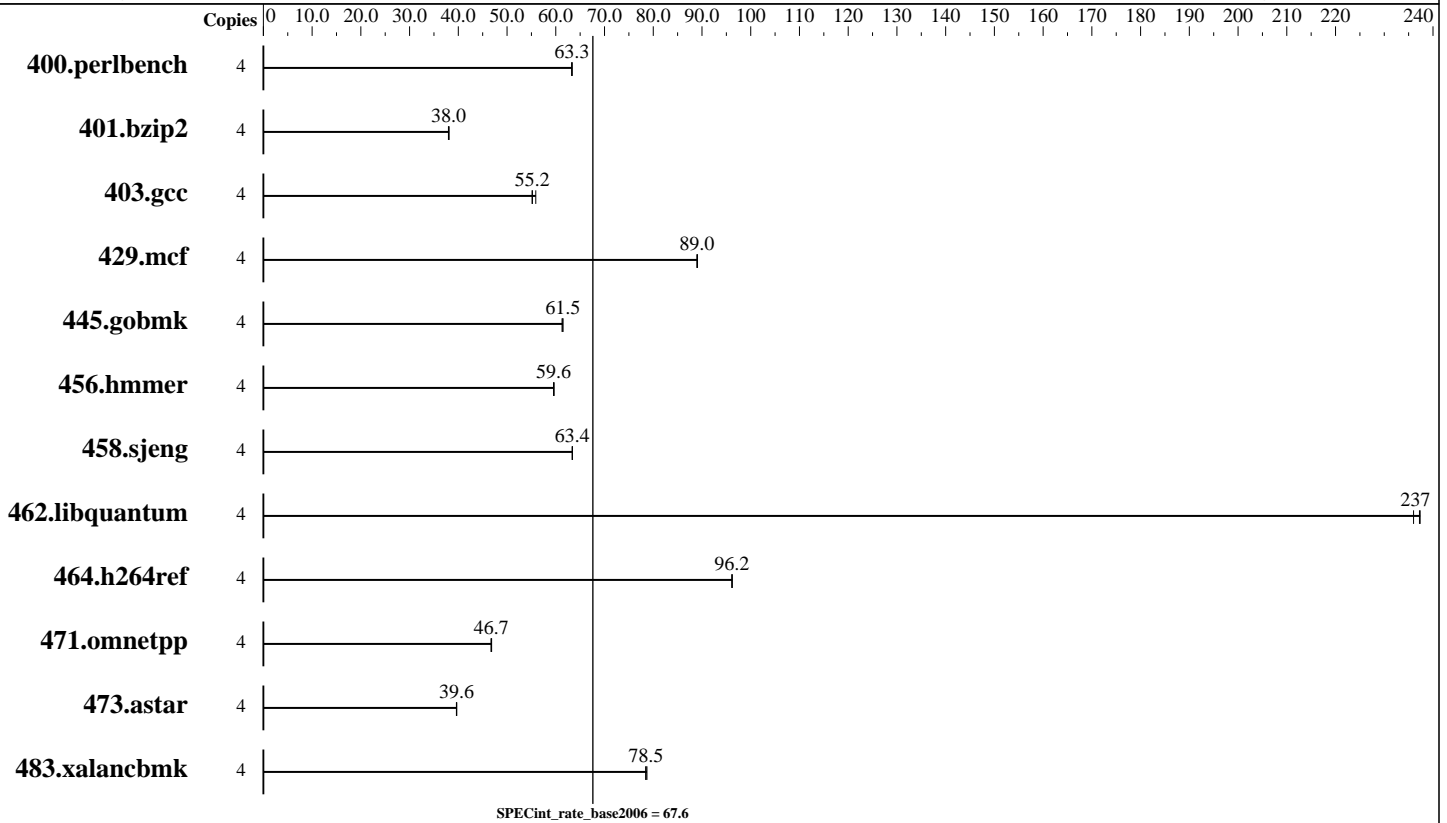**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Nov-2009
**Hardware Availability:** May-2009
**Software Availability:** Feb-2009



| Benchmark | Copies | Result |
|-----------|--------|--------|
| 400.perlbench | 4 | 63.3 |
| 401.bzip2 | 4 | 38.0 |
| 403.gcc | 4 | 55.2 |
| 429.mcf | 4 | 89.0 |
| 445.gobmk | 4 | 61.5 |
| 456.hmmer | 4 | 59.6 |
| 458.sjeng | 4 | 63.4 |
| 462.libquantum | 4 | 237 |
| 464.h264ref | 4 | 96.2 |
| 471.omnetpp | 4 | 46.7 |
| 473.astar | 4 | 39.6 |
| 483.xalancbmk | 4 | 78.5 |

SPECint_rate_base2006 = 67.6

## Hardware

CPU Name: Intel Xeon E5506
CPU Characteristics:
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 12 GB (3 x 4 GB DDR3-1066 DR RDIMM downclocked to 800MHz)
Disk Subsystem: 1 x 160 GB 7200 RPM SATA
Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502

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## Results Table

| Benchmark | Copies | Base Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Peak Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 400.perlbench | 4 | 618 | 63.3 | 617 | 63.3 | **617** | **63.3** | | | | | | | |
| 401.bzip2 | 4 | 1017 | 38.0 | **1015** | **38.0** | 1013 | 38.1 | | | | | | | |
| 403.gcc | 4 | **583** | **55.2** | 576 | 55.9 | 584 | 55.1 | | | | | | | |
| 429.mcf | 4 | **410** | **89.0** | 410 | 89.0 | 410 | 89.1 | | | | | | | |
| 445.gobmk | 4 | **683** | **61.5** | 685 | 61.3 | 683 | 61.5 | | | | | | | |
| 456.hmmer | 4 | 627 | 59.5 | **626** | **59.6** | 626 | 59.7 | | | | | | | |
| 458.sjeng | 4 | 763 | 63.4 | 764 | 63.4 | **763** | **63.4** | | | | | | | |
| 462.libquantum | 4 | 351 | 236 | **349** | **237** | 349 | 237 | | | | | | | |
| 464.h264ref | 4 | **921** | **96.2** | 921 | 96.2 | 920 | 96.2 | | | | | | | |
| 471.omnetpp | 4 | 534 | 46.8 | **535** | **46.7** | 535 | 46.7 | | | | | | | |
| 473.astar | 4 | **708** | **39.6** | 709 | 39.6 | 708 | 39.7 | | | | | | | |
| 483.xalancbmk | 4 | 352 | 78.4 | 351 | 78.7 | **351** | **78.5** | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

## Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

## Base Compiler Invocation

C benchmarks:
```
     icc
```

C++ benchmarks:
```
     icpc
```

## Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

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| | |
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## Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

> 403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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