



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint®2006 = 29.6

IBM System x3250 M3 (Intel Xeon X3440)

SPECint_base2006 = 26.0

CPU2006 license: 11

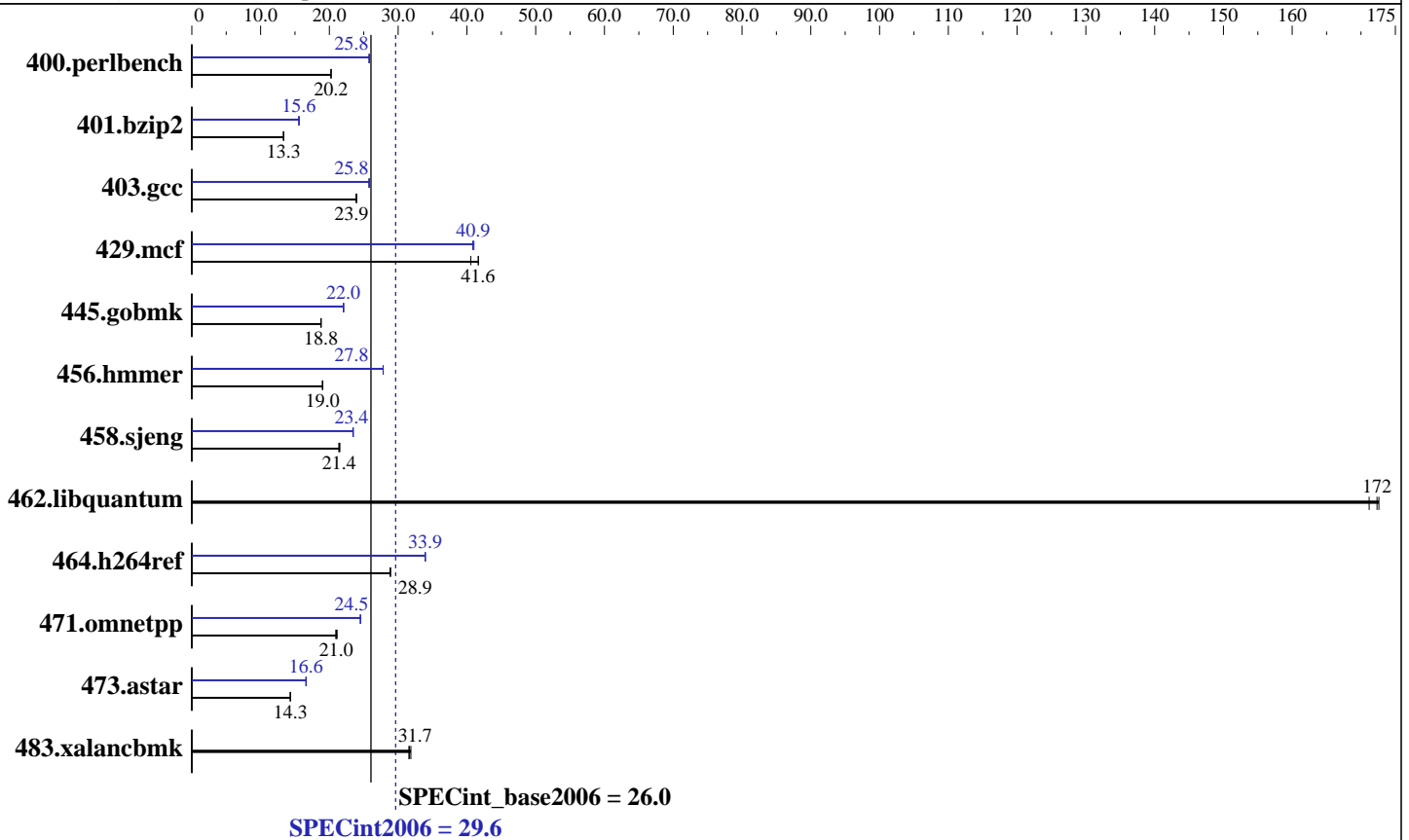
Test date: Oct-2009

Test sponsor: IBM Corporation

Hardware Availability: Oct-2009

Tested by: IBM Corporation

Software Availability: Mar-2009



Hardware

CPU Name: Intel Xeon X3440
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz
 CPU MHz: 2533
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB PC3-10600R)
 Disk Subsystem: 1 x 73 GB SAS, 15000RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 29.6

IBM System x3250 M3 (Intel Xeon X3440)

SPECint_base2006 = 26.0

CPU2006 license: 11

Test date: Oct-2009

Test sponsor: IBM Corporation

Hardware Availability: Oct-2009

Tested by: IBM Corporation

Software Availability: Mar-2009

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	483	20.2	482	20.3	483	20.2	379	25.8	379	25.8	379	25.8
401.bzip2	724	13.3	723	13.3	723	13.3	619	15.6	618	15.6	621	15.5
403.gcc	337	23.9	336	24.0	337	23.9	312	25.8	313	25.7	312	25.8
429.mcf	225	40.6	219	41.6	219	41.7	223	40.9	223	40.9	222	41.0
445.gobmk	558	18.8	558	18.8	559	18.8	475	22.1	476	22.0	476	22.0
456.hammer	491	19.0	490	19.0	491	19.0	336	27.8	335	27.8	335	27.8
458.sjeng	562	21.5	566	21.4	565	21.4	515	23.5	517	23.4	516	23.4
462.libquantum	121	171	120	172	120	173	121	171	120	172	120	173
464.h264ref	767	28.9	767	28.9	766	28.9	651	34.0	653	33.9	652	33.9
471.omnetpp	297	21.0	299	20.9	296	21.1	255	24.5	255	24.5	255	24.5
473.astar	490	14.3	490	14.3	490	14.3	423	16.6	422	16.6	423	16.6
483.xalancbmk	219	31.6	218	31.7	217	31.9	219	31.6	218	31.7	217	31.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 CPU C-States Enable and Adjacent Sector Prefetch Enable
 Turbo Mode Enable
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 29.6

IBM System x3250 M3 (Intel Xeon X3440)

SPECint_base2006 = 26.0

CPU2006 license: 11

Test date: Oct-2009

Test sponsor: IBM Corporation

Hardware Availability: Oct-2009

Tested by: IBM Corporation

Software Availability: Mar-2009

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 29.6

IBM System x3250 M3 (Intel Xeon X3440)

SPECint_base2006 = 26.0

CPU2006 license: 11

Test date: Oct-2009

Test sponsor: IBM Corporation

Hardware Availability: Oct-2009

Tested by: IBM Corporation

Software Availability: Mar-2009

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
 -opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 29.6

IBM System x3250 M3 (Intel Xeon X3440)

SPECint_base2006 = 26.0

CPU2006 license: 11

Test date: Oct-2009

Test sponsor: IBM Corporation

Hardware Availability: Oct-2009

Tested by: IBM Corporation

Software Availability: Mar-2009

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091028.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091028.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:58:27 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 23 November 2009.