



SPEC® CINT2006 Result

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Hewlett-Packard Company

SPECint®_rate2006 = 259

ProLiant SL170z G6
(2.93 GHz, Intel Xeon X5570)

SPECint_rate_base2006 = 246

CPU2006 license: 3

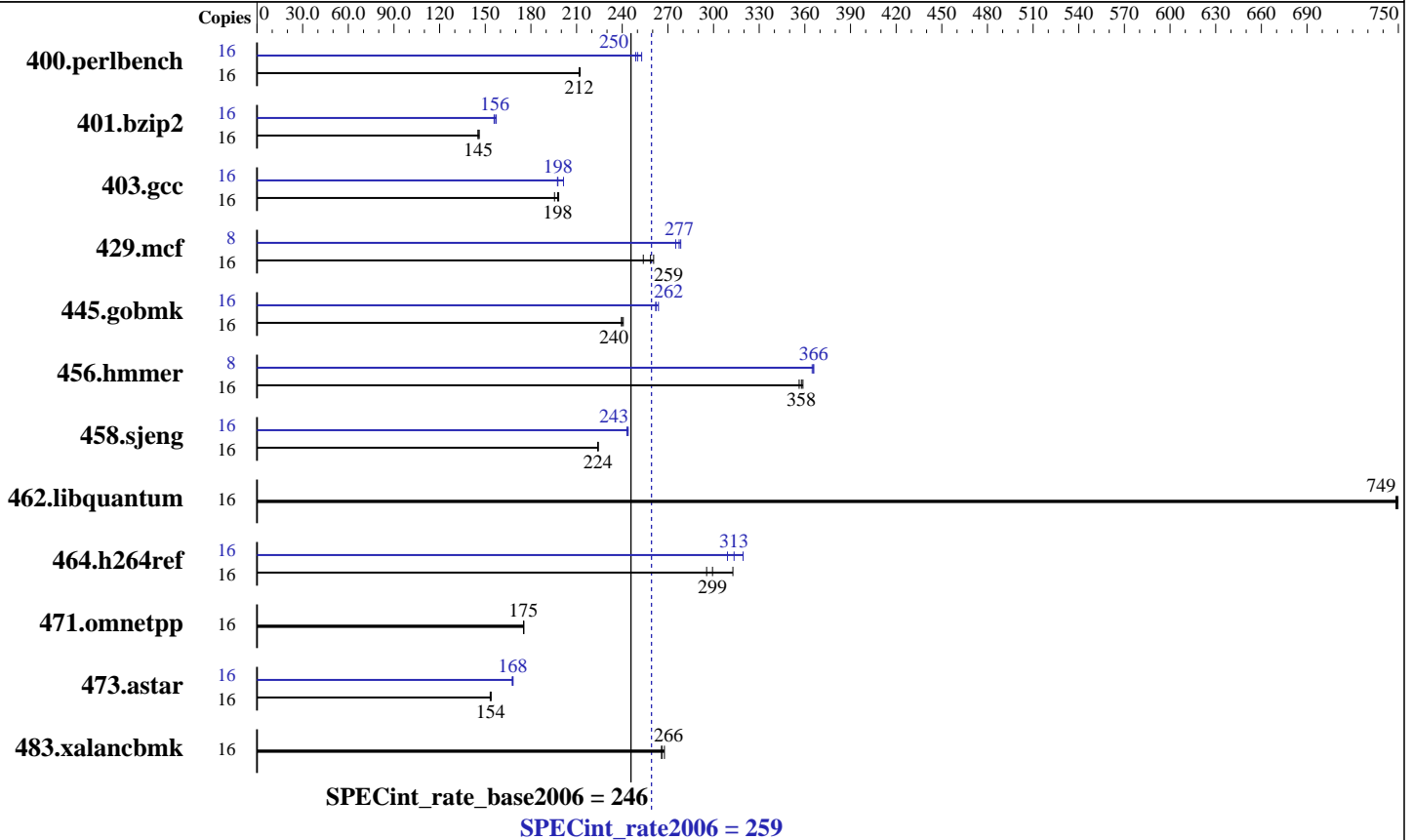
Test date: Oct-2009

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2009

Tested by: Hewlett-Packard Company

Software Availability: Sep-2009



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6x4 GB DDR3-10600R CL9)
 Disk Subsystem: 1x160 GB 7.2 K 3.5" SATA
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.3
 Kernel 2.6.18-128.el5
 Compiler: Intel C++ Compiler 11.1 for Linux
 Build 20090827 Package ID: l_cproc_p_11.1.056
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.17.50.0.18



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	739	212	736	212	<u>738</u>	<u>212</u>	16	618	253	628	249	<u>625</u>	<u>250</u>
401.bzip2	16	1063	145	1057	146	<u>1062</u>	<u>145</u>	16	991	156	982	157	<u>988</u>	<u>156</u>
403.gcc	16	<u>652</u>	<u>198</u>	650	198	659	196	16	639	201	652	197	<u>651</u>	<u>198</u>
429.mcf	16	575	254	<u>564</u>	<u>259</u>	560	261	8	265	275	262	278	<u>263</u>	<u>277</u>
445.gobmk	16	701	240	697	241	<u>701</u>	<u>240</u>	16	<u>640</u>	<u>262</u>	636	264	641	262
456.hammer	16	419	356	416	359	<u>417</u>	<u>358</u>	8	205	365	204	366	<u>204</u>	<u>366</u>
458.sjeng	16	863	224	865	224	<u>864</u>	<u>224</u>	16	794	244	<u>795</u>	<u>243</u>	796	243
462.libquantum	16	442	750	<u>443</u>	<u>749</u>	443	748	16	442	750	<u>443</u>	<u>749</u>	443	748
464.h264ref	16	<u>1183</u>	<u>299</u>	1132	313	1198	295	16	<u>1129</u>	<u>313</u>	1108	319	1145	309
471.omnetpp	16	571	175	570	175	<u>571</u>	<u>175</u>	16	571	175	570	175	<u>571</u>	<u>175</u>
473.astar	16	733	153	<u>731</u>	<u>154</u>	730	154	16	668	168	670	168	<u>668</u>	<u>168</u>
483.xalancbmk	16	<u>415</u>	<u>266</u>	412	268	415	266	16	<u>415</u>	<u>266</u>	412	268	415	266

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Power Efficiency Mode set to Performance

General Notes

The ProLiant SL2x170z G6 and ProLiant SL170z G6 models are electronically equivalent.
The results have been measured on the ProLiant SL2x170z G6 model.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.1/056/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.1/056/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.1/056/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/cpu2006/SmartHeap_8.1/lib -lsmarheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20091110.html>
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091110.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20091110.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091110.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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