



SPEC[®] CINT2006 Result

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Itaotec

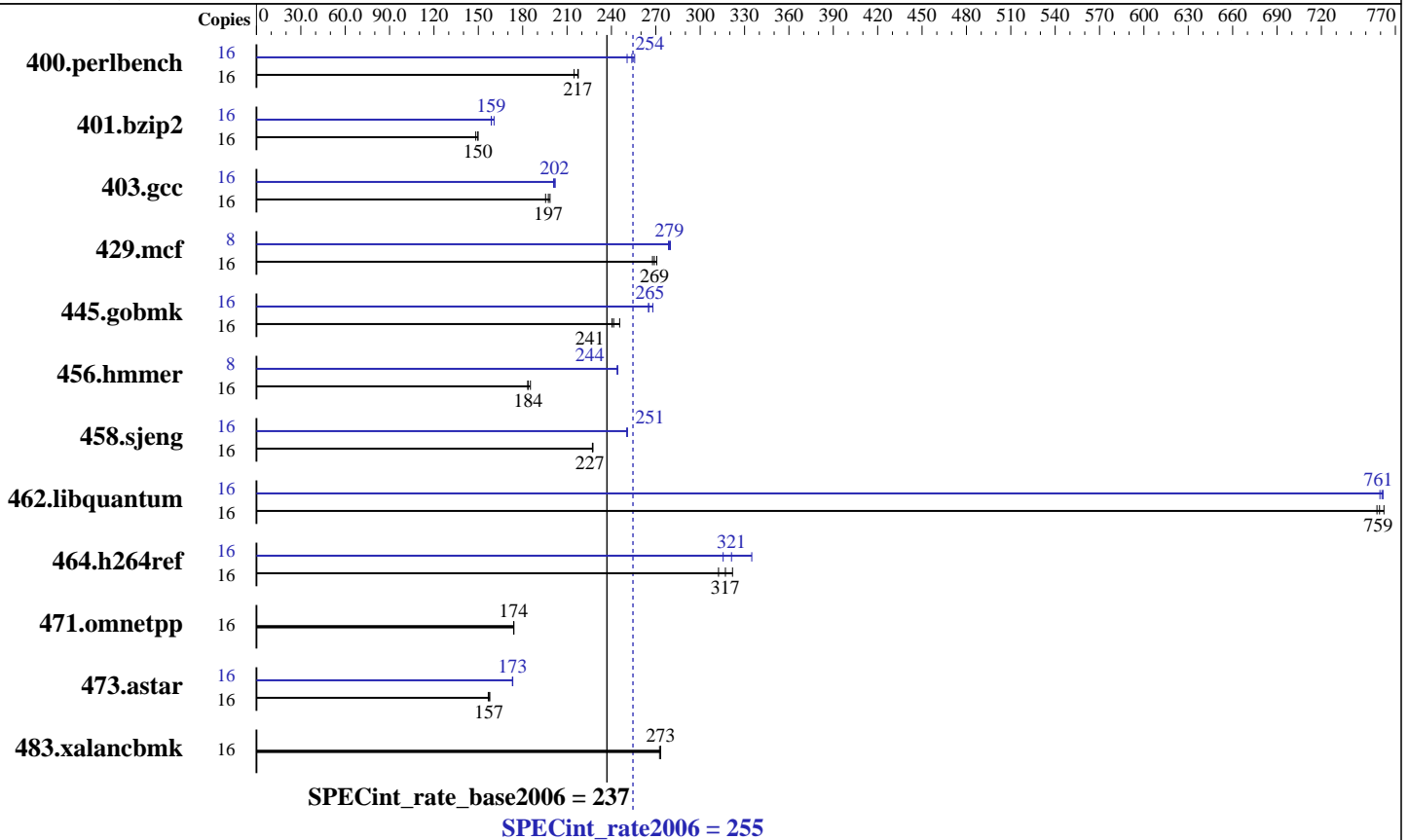
SPECint[®]_rate2006 = 255

Servidor Itaotec MX203 (Intel Xeon X5570)

SPECint_rate_base2006 = 237

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Oct-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4GB DDR3-1333, CL 9, ECC)
 Disk Subsystem: 1 x 160 GB SATA-2, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	728	215	718	218	<u>719</u>	<u>217</u>	16	611	256	<u>616</u>	<u>254</u>	624	251
401.bzip2	16	<u>1031</u>	<u>150</u>	1041	148	1031	150	16	961	161	973	159	<u>972</u>	<u>159</u>
403.gcc	16	<u>653</u>	<u>197</u>	649	198	659	195	16	<u>639</u>	<u>202</u>	641	201	638	202
429.mcf	16	545	268	<u>542</u>	<u>269</u>	539	271	8	262	279	<u>261</u>	<u>279</u>	261	280
445.gobmk	16	684	246	<u>695</u>	<u>241</u>	698	240	16	626	268	633	265	<u>633</u>	<u>265</u>
456.hammer	16	814	183	806	185	<u>813</u>	<u>184</u>	8	306	244	306	244	<u>306</u>	<u>244</u>
458.sjeng	16	852	227	<u>852</u>	<u>227</u>	851	228	16	<u>772</u>	<u>251</u>	772	251	773	250
462.libquantum	16	<u>437</u>	<u>759</u>	438	758	435	762	16	436	760	435	762	<u>436</u>	<u>761</u>
464.h264ref	16	1100	322	<u>1117</u>	<u>317</u>	1133	312	16	1057	335	1122	316	<u>1102</u>	<u>321</u>
471.omnetpp	16	575	174	<u>575</u>	<u>174</u>	575	174	16	575	174	<u>575</u>	<u>174</u>	575	174
473.astar	16	717	157	<u>714</u>	<u>157</u>	712	158	16	649	173	648	173	<u>649</u>	<u>173</u>
483.xalanbmk	16	<u>404</u>	<u>273</u>	404	273	405	273	16	<u>404</u>	<u>273</u>	404	273	405	273

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

General Notes

This result was measured on the Servidor Itautec MX223.
The Servidor Itautec MX203 and the Servidor Itautec MX223 are electronically equivalent.
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/richard/sh/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/081/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/081/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/081/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/081/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs
-L/home/richard/sh/SmartHeap_8.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.0-int-linux64-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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