SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX920 S1, Intel Xeon X5560, 2.80 GHz

SPECint®_rate2006 = 127

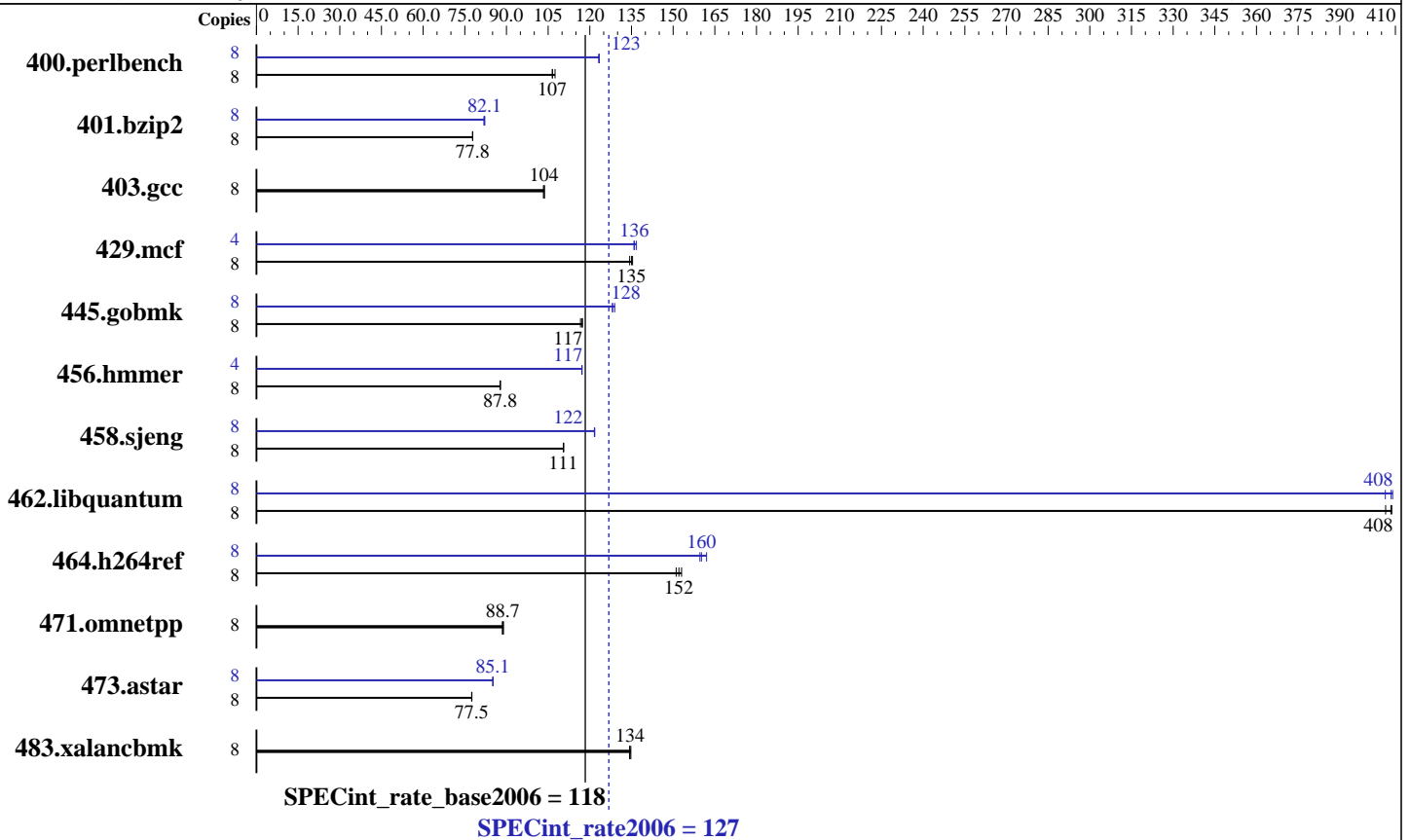SPECint_rate_base2006 = 118

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Oct-2009
Hardware Availability: Jun-2009
Software Availability: Feb-2009

Copies		
400.perlbench	8 / 8	123 / 107
401.bzip2	8 / 8	82.1 / 77.8
403.gcc	8	104
429.mcf	4 / 8	136 / 135
445.gobmk	8 / 8	128 / 117
456.hmmer	4 / 8	117 / 87.8
458.sjeng	8 / 8	122 / 111
462.libquantum	8 / 8	408 / 408
464.h264ref	8 / 8	160 / 152
471.omnetpp	8	88.7
473.astar	8 / 8	85.1 / 77.5
483.xalancbmk	8	134

SPECint_rate_base2006 = 118

SPECint_rate2006 = 127

Hardware

CPU Name:	Intel Xeon X5560
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (3x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SATA, 250 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result

Fujitsu

PRIMERGY BX920 S1, Intel Xeon X5560, 2.80 GHz

SPECint_rate2006 =   127

SPECint_rate_base2006 = 118

CPU2006 license: 19	**Test date:**	Oct-2009
Test sponsor: Fujitsu	**Hardware Availability:**	Jun-2009
Tested by: Fujitsu	**Software Availability:**	Feb-2009

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	727	107	734	107	**734**	**107**	8	**633**	**123**	633	123	634	123
401.bzip2	8	**992**	**77.8**	992	77.8	992	77.8	8	939	82.2	**940**	**82.1**	943	81.9
403.gcc	8	623	103	**622**	**104**	621	104	8	623	103	**622**	**104**	621	104
429.mcf	8	543	134	**540**	**135**	539	135	4	268	136	**268**	**136**	267	137
445.gobmk	8	715	117	719	117	**716**	**117**	8	651	129	**655**	**128**	655	128
456.hmmer	8	**851**	**87.8**	851	87.7	850	87.8	4	319	117	318	117	**319**	**117**
458.sjeng	8	876	110	875	111	**876**	**111**	8	**795**	**122**	795	122	796	122
462.libquantum	8	**406**	**408**	406	409	408	406	8	**406**	**408**	405	409	408	406
464.h264ref	8	1172	151	**1164**	**152**	1157	153	8	1093	162	1109	160	**1105**	**160**
471.omnetpp	8	562	88.9	565	88.5	**564**	**88.7**	8	562	88.9	565	88.5	**564**	**88.7**
473.astar	8	724	77.6	**725**	**77.5**	725	77.5	8	659	85.2	660	85.1	**660**	**85.1**
483.xalancbmk	8	410	135	**410**	**134**	411	134	8	410	135	**410**	**134**	411	134

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
    -opt-malloc-options=3 -opt-prefetch

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX920 S1, Intel Xeon X5560, 2.80 GHz

SPECint_rate2006 = 127

SPECint_rate_base2006 = 118

CPU2006 license: 19	**Test date:** Oct-2009
Test sponsor: Fujitsu	**Hardware Availability:** Jun-2009
Tested by: Fujitsu	**Software Availability:** Feb-2009

Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):
```
icpc
```

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias -opt-prefetch
```

Fujitsu

PRIMERGY BX920 S1, Intel Xeon X5560, 2.80 GHz

SPECint_rate2006 = 127

SPECint_rate_base2006 = 118

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Oct-2009
Hardware Availability: Jun-2009
Software Availability: Feb-2009

Peak Optimization Flags (Continued)

```
       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

         403.gcc: basepeak = yes

         429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -opt-prefetch

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                  -ipo -no-prec-div -ansi-alias

       456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                  -ansi-alias -auto-ilp32

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll4 -auto-ilp32

   462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
                  -opt-malloc-options=3 -opt-prefetch

      464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                  -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
     471.omnetpp: basepeak = yes

       473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
                  -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

     483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
         403.gcc: -Dalloca=_alloca
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX920 S1, Intel Xeon X5560, 2.80 GHz

SPECint_rate2006 = 127

SPECint_rate_base2006 = 118

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Oct-2009
Hardware Availability: Jun-2009
Software Availability: Feb-2009

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:52:58 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 November 2009.