# SPEC® CINT2006 Result
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## Fujitsu

PRIMERGY BX620 S5, Intel Xeon E5506, 2.13 GHz

$SPECint^®\_rate2006 = \quad 72.8$

$SPECint\_rate\_base2006 = \quad 68.0$

| | |
|---|---|
| **CPU2006 license:** 19 | **Test date:** Sep-2009 |
| **Test sponsor:** Fujitsu | **Hardware Availability:** Apr-2009 |
| **Tested by:** Fujitsu | **Software Availability:** Feb-2009 |

**Copies**

| Benchmark | Copies | Base | Peak |
|---|---|---|---|
| 400.perlbench | 4 | 63.6 | 73.9 |
| 401.bzip2 | 4 | 38.3 | 40.6 |
| 403.gcc | 4 | 56.0 | |
| 429.mcf | 4 | 89.0 | |
| 445.gobmk | 4 | 61.5 | 66.1 |
| 456.hmmer | 4 | 59.8 | 81.1 |
| 458.sjeng | 4 | 63.8 | 68.5 |
| 462.libquantum | 4 | 236 | 238 |
| 464.h264ref | 4 | 96.5 | 102 |
| 471.omnetpp | 4 | 48.1 | |
| 473.astar | 4 | 39.9 | 44.0 |
| 483.xalancbmk | 4 | 78.7 | |

SPECint_rate_base2006 = 68.0
SPECint_rate2006 = 72.8

## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5506 |
| CPU Characteristics: | |
| CPU MHz: | 2133 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 4 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (6x4 GB PC3-8500R, 2 rank, CL7-7-7, ECC, see add'l detail in notes) |
| Disk Subsystem: | 1 x SATA, 250 GB, 7200 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp |
| Compiler: | Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Multi-User Run Level 3 |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502 |

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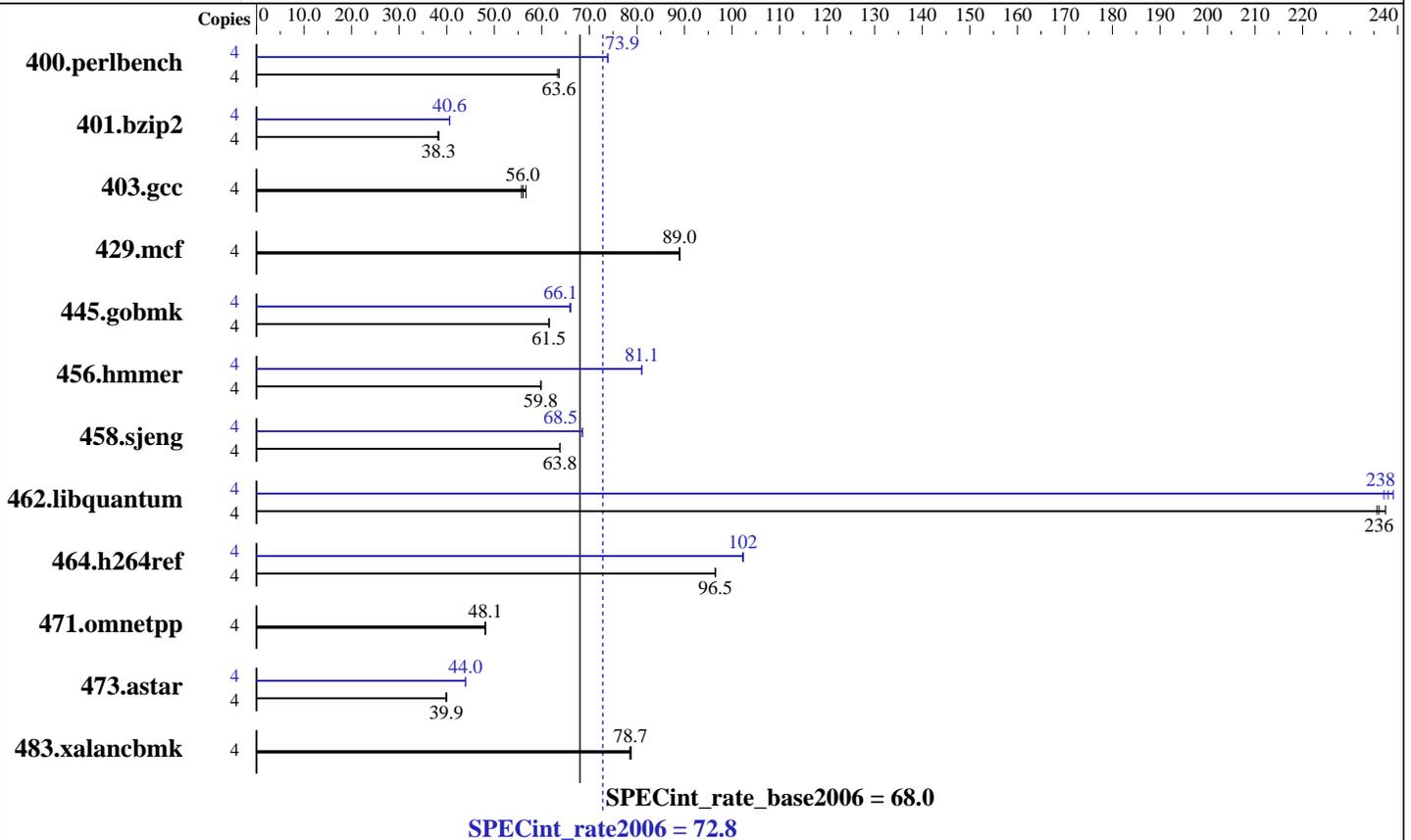
SPECint_rate_base2006 =  68.0

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## Results Table

| Benchmark | Copies | Base Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Peak Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 400.perlbench | 4 | **614** | **63.6** | 614 | 63.7 | 617 | 63.3 | 4 | 530 | 73.8 | **529** | **73.9** | 529 | 73.9 |
| 401.bzip2 | 4 | **1009** | **38.3** | 1013 | 38.1 | 1007 | 38.3 | 4 | 950 | 40.6 | 951 | 40.6 | **950** | **40.6** |
| 403.gcc | 4 | 578 | 55.7 | 568 | 56.7 | **575** | **56.0** | 4 | 578 | 55.7 | 568 | 56.7 | **575** | **56.0** |
| 429.mcf | 4 | **410** | **89.0** | 410 | 88.9 | 410 | 89.0 | 4 | **410** | **89.0** | 410 | 88.9 | 410 | 89.0 |
| 445.gobmk | 4 | 681 | 61.6 | 682 | 61.5 | **682** | **61.5** | 4 | **635** | **66.1** | 635 | 66.1 | 637 | 65.9 |
| 456.hmmer | 4 | 625 | 59.7 | 624 | 59.8 | **624** | **59.8** | 4 | 461 | 81.0 | 460 | 81.1 | **460** | **81.1** |
| 458.sjeng | 4 | 758 | 63.8 | 759 | 63.8 | **759** | **63.8** | 4 | **706** | **68.5** | 707 | 68.5 | 706 | 68.6 |
| 462.libquantum | 4 | 352 | 236 | **351** | **236** | 349 | 237 | 4 | **348** | **238** | 347 | 239 | 350 | 237 |
| 464.h264ref | 4 | **917** | **96.5** | 918 | 96.4 | 917 | 96.5 | 4 | 865 | 102 | **865** | **102** | 866 | 102 |
| 471.omnetpp | 4 | 520 | 48.0 | 519 | 48.2 | **519** | **48.1** | 4 | 520 | 48.0 | 519 | 48.2 | **519** | **48.1** |
| 473.astar | 4 | 705 | 39.9 | 702 | 40.0 | **704** | **39.9** | 4 | **639** | **44.0** | 638 | 44.0 | 640 | 43.9 |
| 483.xalancbmk | 4 | **351** | **78.7** | 352 | 78.5 | 350 | 78.8 | 4 | **351** | **78.7** | 352 | 78.5 | 350 | 78.8 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 800 MHz.

## General Notes

For information about Fujitsu please visit: http://www.fujitsu.com

## Base Compiler Invocation

C benchmarks:
     icc

C++ benchmarks:
     icpc

## Base Portability Flags

     400.perlbench: -DSPEC_CPU_LINUX_IA32
     462.libquantum: -DSPEC_CPU_LINUX

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# Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

# Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
    -opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/spec/cpu2006.1.1/lib -lsmartheap

# Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

# Peak Compiler Invocation

C benchmarks (except as noted below):
    icc

    401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

    456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

    458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):
    icpc

    473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

# Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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# Peak Portability Flags (Continued)

473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

# Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -ansi-alias -opt-prefetch`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
`-ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
`-ansi-alias -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `-xSSE4.2 -ipo -O3 -no-prec-div -static`
`-opt-malloc-options=3 -opt-prefetch`

464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
`-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32`
`-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64`

483.xalancbmk: `basepeak = yes`

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## Peter Other Flags

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.xml

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For other inquiries, please contact webmaster@spec.org.

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