



SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = Not Run

PowerEdge T610 (Intel Xeon E5502, 1.86 GHz)

SPECint_base2006 = 16.9

CPU2006 license: 55

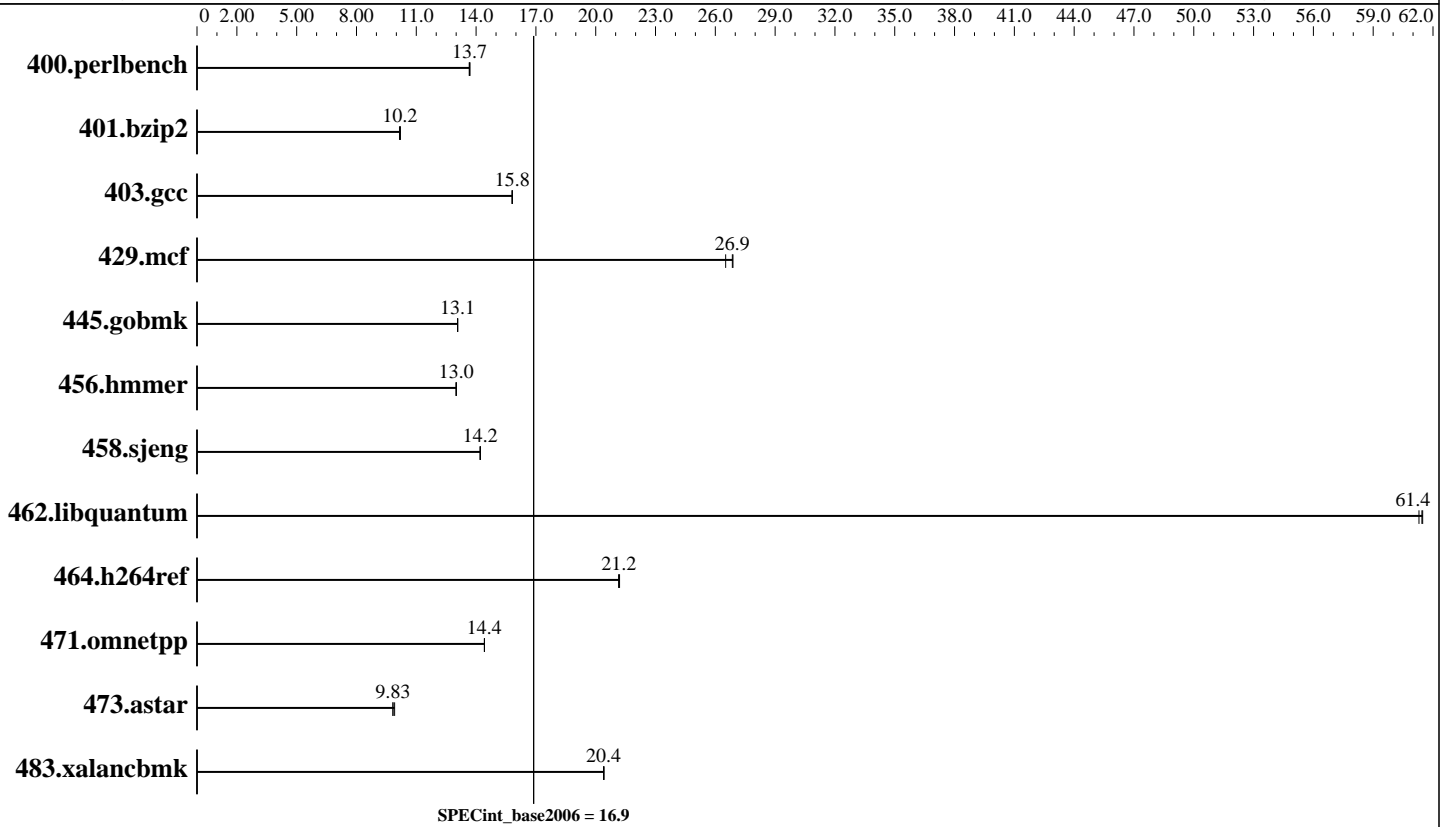
Test date: Sep-2009

Test sponsor: Dell Inc.

Hardware Availability: Mar-2009

Tested by: Dell Inc.

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics:
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 2 GB (2 x 1 GB DDR3-1066 DR RDIMM downclocked to 800 MHz)
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	713	13.7	<u>715</u>	<u>13.7</u>	715	13.7						
401.bzip2	946	10.2	<u>947</u>	<u>10.2</u>	950	10.2						
403.gcc	509	15.8	509	15.8	<u>509</u>	<u>15.8</u>						
429.mcf	344	26.5	<u>340</u>	<u>26.9</u>	339	26.9						
445.gobmk	<u>802</u>	<u>13.1</u>	802	13.1	803	13.1						
456.hmmer	717	13.0	<u>717</u>	<u>13.0</u>	718	13.0						
458.sjeng	851	14.2	853	14.2	<u>851</u>	<u>14.2</u>						
462.libquantum	337	61.5	<u>337</u>	<u>61.4</u>	338	61.3						
464.h264ref	<u>1045</u>	<u>21.2</u>	1044	21.2	1046	21.2						
471.omnetpp	434	14.4	<u>433</u>	<u>14.4</u>	433	14.4						
473.astar	714	9.83	<u>714</u>	<u>9.83</u>	708	9.91						
483.xalancbmk	338	20.4	<u>338</u>	<u>20.4</u>	338	20.4						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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