



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint[®]_rate2006 = 36.1

PRIMERGY TX200 S5, Intel Xeon E5502, 1.87 GHz

SPECint_rate_base2006 = 33.6

CPU2006 license: 19

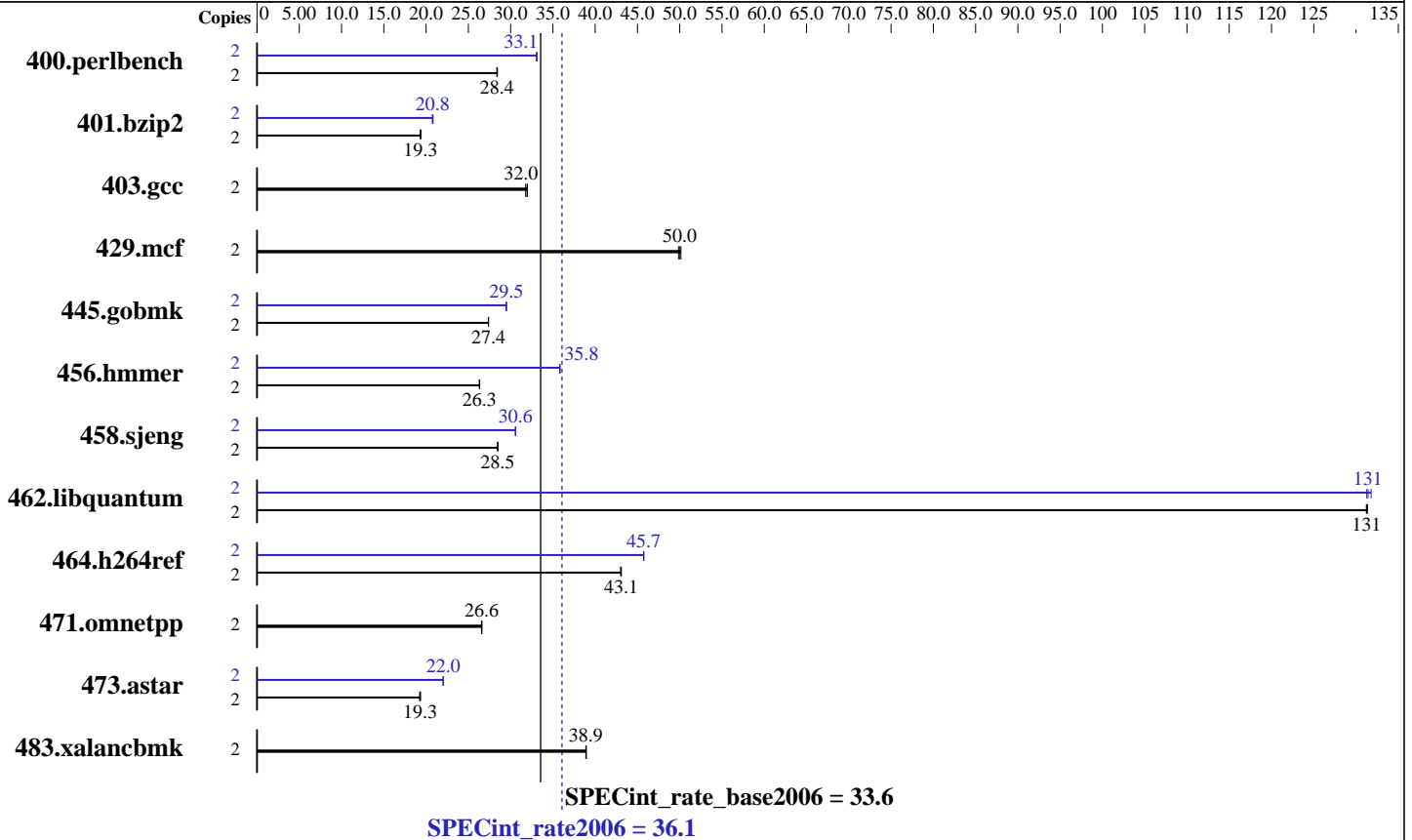
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics:
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: No
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap Library, Version 8.1 Binutils 2.18.50.0.7.20080502



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 36.1

PRIMERGY TX200 S5, Intel Xeon E5502, 1.87 GHz

SPECint_rate_base2006 = 33.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	688	28.4	687	28.4	689	28.4	2	591	33.1	591	33.1	590	33.1
401.bzip2	2	995	19.4	999	19.3	998	19.3	2	927	20.8	928	20.8	930	20.7
403.gcc	2	507	31.8	504	32.0	504	32.0	2	507	31.8	504	32.0	504	32.0
429.mcf	2	364	50.1	365	50.0	366	49.9	2	364	50.1	365	50.0	366	49.9
445.gobmk	2	767	27.4	766	27.4	766	27.4	2	712	29.5	711	29.5	711	29.5
456.hammer	2	709	26.3	710	26.3	708	26.3	2	521	35.8	521	35.8	521	35.8
458.sjeng	2	850	28.5	850	28.5	850	28.5	2	792	30.6	792	30.6	791	30.6
462.libquantum	2	315	131	316	131	316	131	2	314	132	315	131	316	131
464.h264ref	2	1027	43.1	1029	43.0	1027	43.1	2	967	45.8	968	45.7	968	45.7
471.omnetpp	2	470	26.6	470	26.6	471	26.6	2	470	26.6	470	26.6	471	26.6
473.astar	2	730	19.2	726	19.3	727	19.3	2	637	22.0	637	22.0	638	22.0
483.xalancbmk	2	355	38.9	354	39.0	355	38.9	2	355	38.9	354	39.0	355	38.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 800 MHz.

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 36.1

PRIMERGY TX200 S5, Intel Xeon E5502, 1.87 GHz

SPECint_rate_base2006 = 33.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009

Base Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 36.1

PRIMERGY TX200 S5, Intel Xeon E5502, 1.87 GHz

SPECint_rate_base2006 = 33.6

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009

Peak Portability Flags (Continued)

473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
403.gcc: basepeak = yes
429.mcf: basepeak = yes
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64
483.xalancbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 36.1

PRIMERGY TX200 S5, Intel Xeon E5502, 1.87 GHz

SPECint_rate_base2006 = 33.6

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Aug-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:42:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 13 October 2009.