



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®2006 = 27.2**

PRIMERGY TX200 S5, Intel Xeon E5520, 2.27 GHz

**SPECint\_base2006 = 24.6**

CPU2006 license: 19

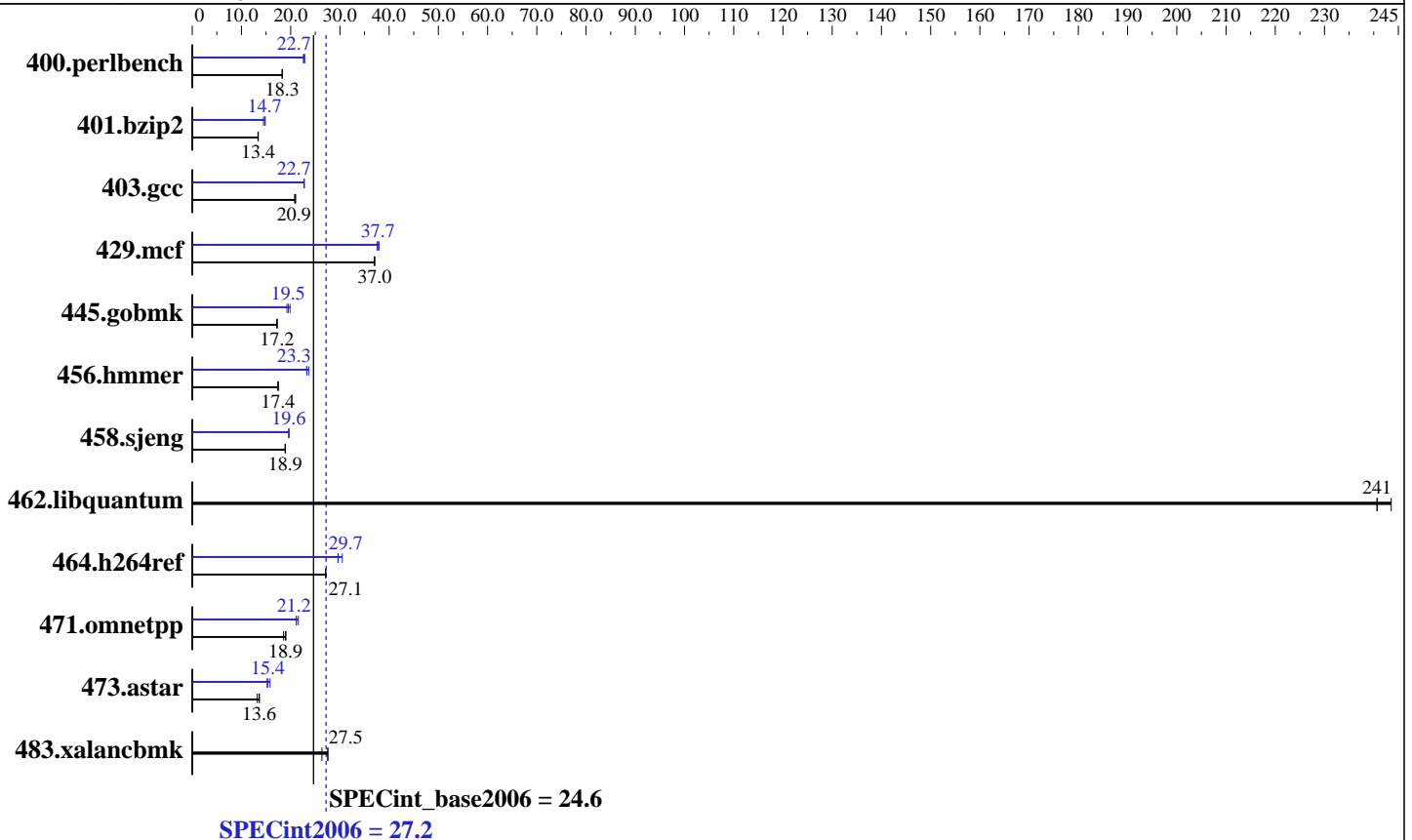
Test date: Jun-2009

Test sponsor: Fujitsu

Hardware Availability: Jun-2009

Tested by: Fujitsu

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5520  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>534</b>	<b>18.3</b>	534	18.3	535	18.3	<b>431</b>	<b>22.7</b>	432	22.6	427	22.9
401.bzip2	723	13.4	<b>720</b>	<b>13.4</b>	720	13.4	667	14.5	<b>655</b>	<b>14.7</b>	650	14.8
403.gcc	<b>384</b>	<b>20.9</b>	387	20.8	383	21.0	355	22.7	<b>354</b>	<b>22.7</b>	354	22.8
429.mcf	247	37.0	246	37.1	<b>246</b>	<b>37.0</b>	243	37.5	<b>242</b>	<b>37.7</b>	240	38.0
445.gobmk	609	17.2	<b>609</b>	<b>17.2</b>	609	17.2	<b>538</b>	<b>19.5</b>	528	19.9	545	19.2
456.hammer	535	17.4	<b>535</b>	<b>17.4</b>	534	17.5	394	23.7	401	23.3	<b>400</b>	<b>23.3</b>
458.sjeng	639	18.9	<b>640</b>	<b>18.9</b>	641	18.9	616	19.7	<b>617</b>	<b>19.6</b>	617	19.6
462.libquantum	86.1	241	<b>86.1</b>	<b>241</b>	85.1	244	86.1	241	<b>86.1</b>	<b>241</b>	85.1	244
464.h264ref	816	27.1	816	27.1	<b>816</b>	<b>27.1</b>	748	29.6	726	30.5	<b>746</b>	<b>29.7</b>
471.omnetpp	337	18.6	328	19.0	<b>330</b>	<b>18.9</b>	290	21.6	<b>295</b>	<b>21.2</b>	296	21.1
473.astar	<b>516</b>	<b>13.6</b>	532	13.2	515	13.6	462	15.2	<b>456</b>	<b>15.4</b>	444	15.8
483.xalancbmk	<b>251</b>	<b>27.5</b>	251	27.5	262	26.3	<b>251</b>	<b>27.5</b>	251	27.5	262	26.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 1066 MHz.

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
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## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc  
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc  
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc  
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc  
C++ benchmarks (except as noted below):  
icpc  
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch  
401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: basepeak = yes  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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