



# SPEC® CINT2006 Result

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## Fujitsu

### SPECint®\_rate2006 = 140

PRIMERGY RX300 S5, Intel Xeon W5590, 3.33 GHz

### SPECint\_rate\_base2006 = 130

CPU2006 license: 19

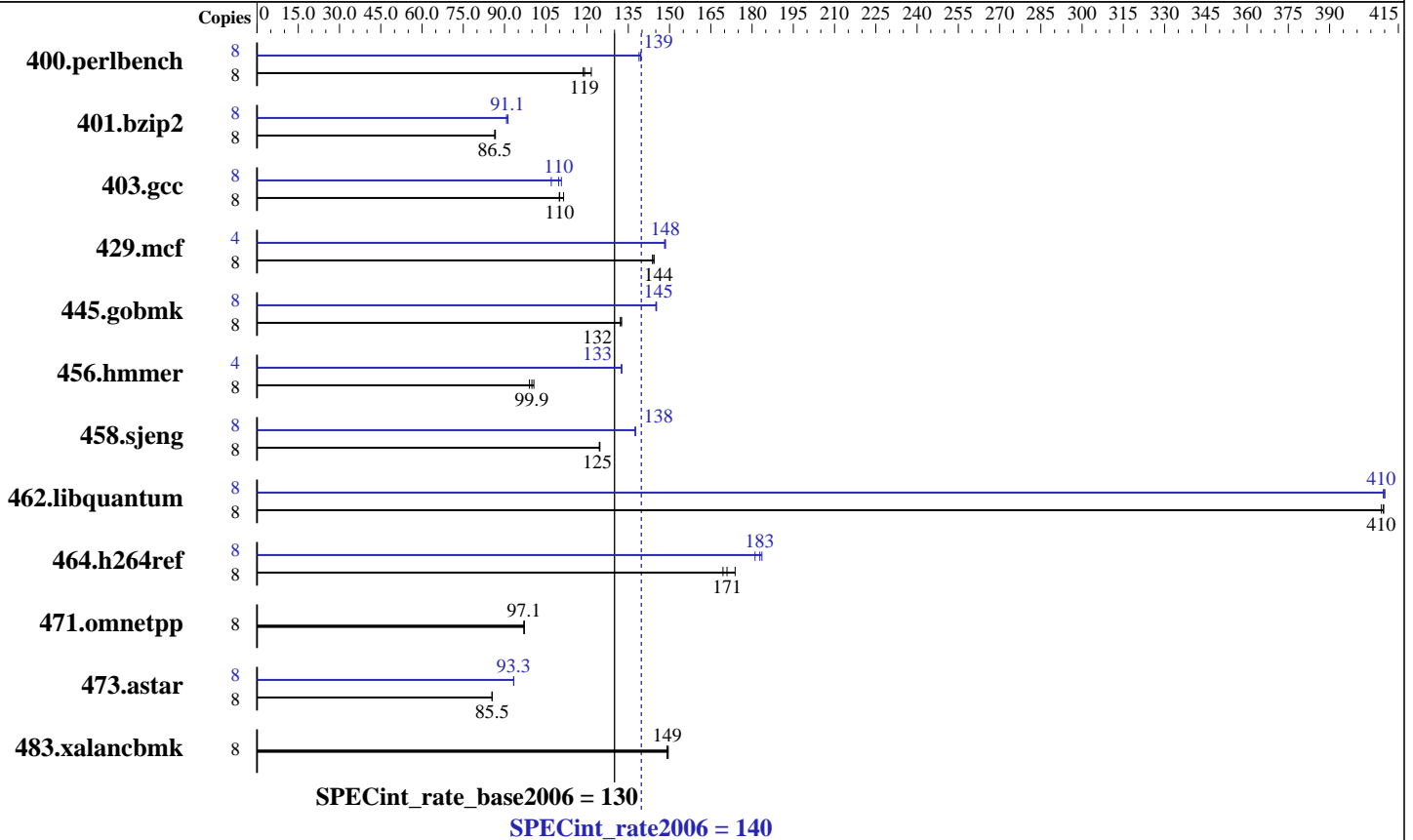
Test date: Sep-2009

Test sponsor: Fujitsu

Hardware Availability: Oct-2009

Tested by: Fujitsu

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon W5590  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	643	122	<b>656</b>	<b>119</b>	659	119	8	560	140	<b>561</b>	<b>139</b>	563	139
401.bzip2	8	890	86.7	893	86.5	<b>893</b>	<b>86.5</b>	8	<b>848</b>	<b>91.1</b>	851	90.7	846	91.3
403.gcc	8	<b>585</b>	<b>110</b>	578	111	586	110	8	582	111	602	107	<b>587</b>	<b>110</b>
429.mcf	8	507	144	505	144	<b>507</b>	<b>144</b>	4	246	148	246	149	<b>246</b>	<b>148</b>
445.gobmk	8	635	132	633	133	<b>635</b>	<b>132</b>	8	<b>578</b>	<b>145</b>	579	145	578	145
456.hammer	8	<b>747</b>	<b>99.9</b>	754	99.0	741	101	4	282	133	282	133	<b>282</b>	<b>133</b>
458.sjeng	8	<b>777</b>	<b>125</b>	777	125	778	124	8	704	138	<b>704</b>	<b>138</b>	703	138
462.libquantum	8	405	409	<b>405</b>	<b>410</b>	405	410	8	404	410	405	410	<b>405</b>	<b>410</b>
464.h264ref	8	<b>1036</b>	<b>171</b>	1018	174	1045	169	8	<b>968</b>	<b>183</b>	964	184	978	181
471.omnetpp	8	514	97.2	515	97.1	<b>515</b>	<b>97.1</b>	8	514	97.2	515	97.1	<b>515</b>	<b>97.1</b>
473.astar	8	656	85.6	<b>657</b>	<b>85.5</b>	657	85.5	8	<b>602</b>	<b>93.3</b>	602	93.3	601	93.4
483.xalancbmk	8	369	150	370	149	<b>370</b>	<b>149</b>	8	369	150	370	149	<b>370</b>	<b>149</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Memory Mode = Max Performance

## General Notes

This result was measured on the PRIMERGY TX300 S5. The PRIMERGY TX300 S5 and the PRIMERGY RX300 S5 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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