



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint®2006 = 37.7

PRIMERGY TX300 S5, Intel Xeon W5590, 3.33 GHz

SPECint_base2006 = 33.9

CPU2006 license: 19

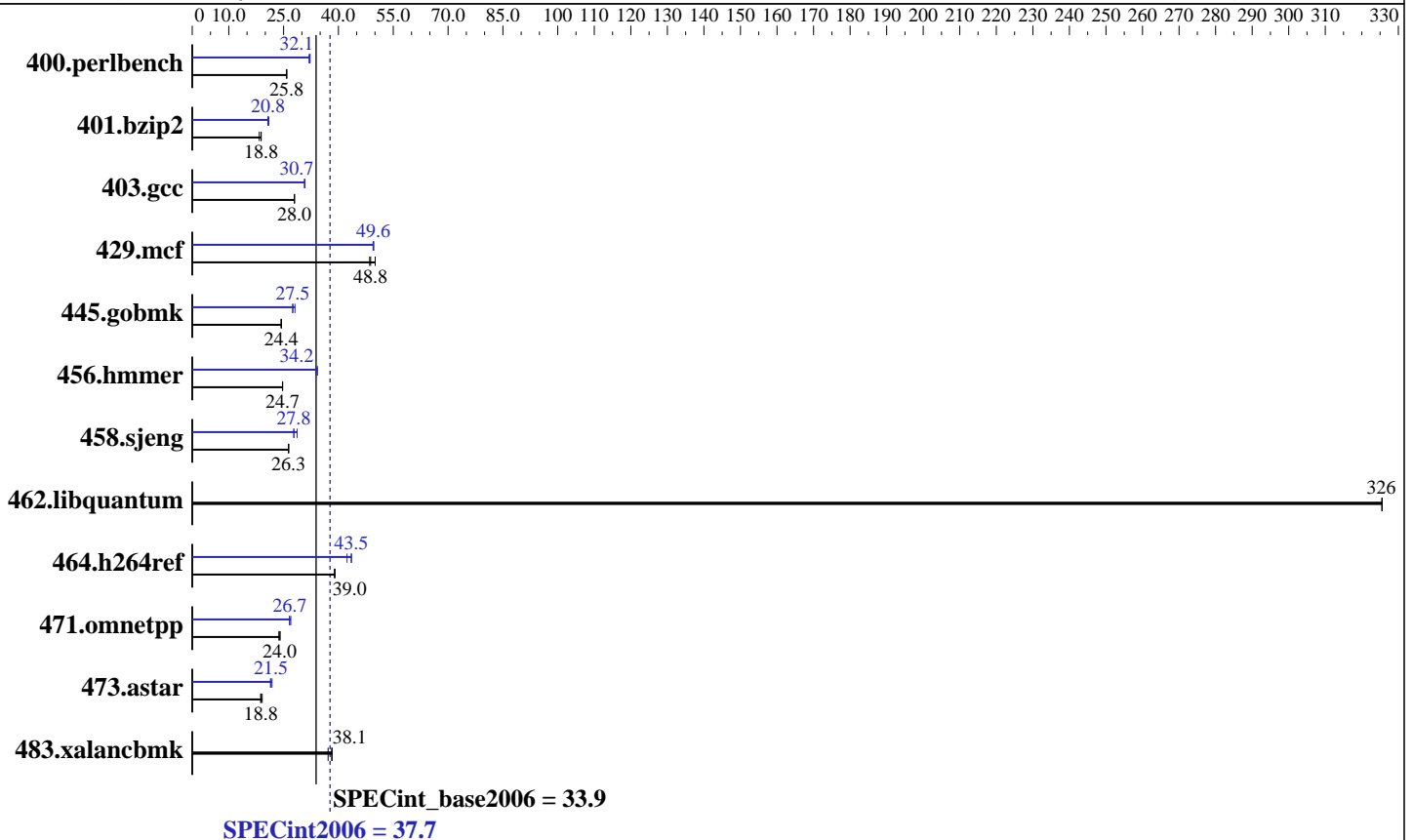
Test date: Sep-2009

Test sponsor: Fujitsu

Hardware Availability: Oct-2009

Tested by: Fujitsu

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon W5590
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = **37.7**

PRIMERGY TX300 S5, Intel Xeon W5590, 3.33 GHz

SPECint_base2006 = **33.9**

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2009
Hardware Availability: Oct-2009
Software Availability: Feb-2009

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	378	25.9	<u>378</u>	<u>25.8</u>	379	25.8	<u>304</u>	<u>32.1</u>	303	32.2	306	32.0
401.bzip2	526	18.3	<u>512</u>	<u>18.8</u>	512	18.9	466	20.7	<u>465</u>	<u>20.8</u>	460	21.0
403.gcc	288	27.9	<u>288</u>	<u>28.0</u>	288	28.0	<u>262</u>	<u>30.7</u>	262	30.7	261	30.9
429.mcf	<u>187</u>	<u>48.8</u>	188	48.5	182	50.1	<u>184</u>	<u>49.6</u>	184	49.5	184	49.6
445.gobmk	<u>431</u>	<u>24.4</u>	431	24.3	430	24.4	<u>381</u>	<u>27.5</u>	382	27.5	373	28.1
456.hammer	377	24.7	<u>377</u>	<u>24.7</u>	378	24.7	276	33.8	<u>273</u>	<u>34.2</u>	273	34.2
458.sjeng	<u>459</u>	<u>26.3</u>	458	26.4	460	26.3	<u>435</u>	<u>27.8</u>	435	27.8	422	28.7
462.libquantum	<u>63.6</u>	<u>326</u>	63.6	326	63.7	326	<u>63.6</u>	<u>326</u>	63.6	326	63.7	326
464.h264ref	567	39.0	<u>568</u>	<u>39.0</u>	568	38.9	508	43.6	522	42.4	<u>509</u>	<u>43.5</u>
471.omnetpp	264	23.7	260	24.0	<u>261</u>	<u>24.0</u>	232	27.0	235	26.6	<u>234</u>	<u>26.7</u>
473.astar	374	18.8	367	19.1	<u>374</u>	<u>18.8</u>	322	21.8	328	21.4	<u>326</u>	<u>21.5</u>
483.xalancbmk	186	37.2	<u>181</u>	<u>38.1</u>	180	38.3	186	37.2	<u>181</u>	<u>38.1</u>	180	38.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Memory Mode = Max Performance

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
This result was measured on the PRIMERGY TX300 S5. The PRIMERGY TX300 S5 and the PRIMERGY RX300 S5 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 37.7

PRIMERGY TX300 S5, Intel Xeon W5590, 3.33 GHz

SPECint_base2006 = 33.9

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2009
Hardware Availability: Oct-2009
Software Availability: Feb-2009

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
C++ benchmarks (except as noted below):
icpc
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 37.7

PRIMERGY TX300 S5, Intel Xeon W5590, 3.33 GHz

SPECint_base2006 = 33.9

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2009

Hardware Availability: Oct-2009

Software Availability: Feb-2009

Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch
401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint2006 = 37.7

PRIMERGY TX300 S5, Intel Xeon W5590, 3.33 GHz

SPECint_base2006 = 33.9

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2009

Hardware Availability: Oct-2009

Software Availability: Feb-2009

Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:38:52 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 13 October 2009.