



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®2006 = 38.6

PowerEdge T710 (Intel Xeon X5560, 2.80 GHz)

SPECfp\_base2006 = 36.4

CPU2006 license: 55

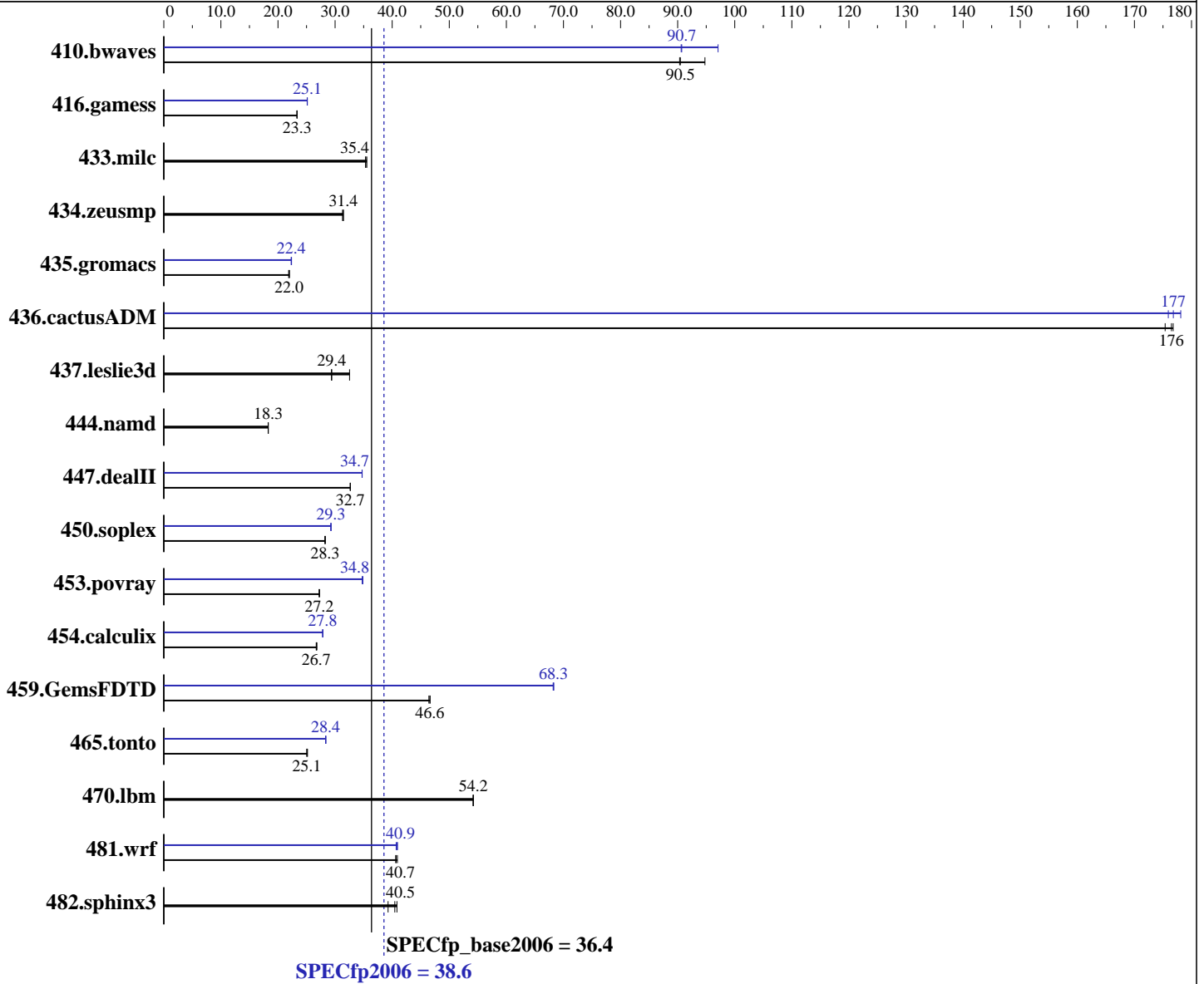
Test date: May-2009

Test sponsor: Dell Inc.

Hardware Availability: Jun-2009

Tested by: Dell Inc.

Software Availability: Feb-2009



## Hardware

CPU Name: Intel Xeon X5560  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-sm  
 Compiler: Intel C++ and Fortran Compiler Professional 11.0 for Linux  
 Build 20090131 Package ID: l\_cproc\_p\_11.0.080, l\_cprof\_p\_11.0.080  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 24 GB (6 x 4 GB DDR3-1333 DR RDIMM)  
Disk Subsystem: 1 x 73 GB 15000 RPM SAS  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	143	94.8	150	90.3	<u>150</u>	<u>90.5</u>	140	97.1	150	90.6	<u>150</u>	<u>90.7</u>
416.gamess	838	23.4	840	23.3	<u>839</u>	<u>23.3</u>	780	25.1	779	25.1	<u>779</u>	<u>25.1</u>
433.milc	258	35.6	260	35.3	<u>259</u>	<u>35.4</u>	258	35.6	260	35.3	<u>259</u>	<u>35.4</u>
434.zeusmp	289	31.5	<u>289</u>	<u>31.4</u>	291	31.3	289	31.5	<u>289</u>	<u>31.4</u>	291	31.3
435.gromacs	324	22.0	327	21.9	<u>325</u>	<u>22.0</u>	319	22.4	<u>319</u>	<u>22.4</u>	320	22.3
436.cactusADM	<u>67.7</u>	<u>176</u>	67.6	177	68.1	175	<u>67.6</u>	<u>177</u>	67.1	178	67.9	176
437.leslie3d	289	32.5	320	29.4	<u>320</u>	<u>29.4</u>	289	32.5	320	29.4	<u>320</u>	<u>29.4</u>
444.namd	438	18.3	439	18.3	<u>439</u>	<u>18.3</u>	438	18.3	439	18.3	<u>439</u>	<u>18.3</u>
447.dealII	<u>350</u>	<u>32.7</u>	350	32.7	350	32.7	329	34.7	329	34.7	<u>329</u>	<u>34.7</u>
450.soplex	295	28.3	296	28.2	<u>295</u>	<u>28.3</u>	285	29.3	285	29.3	<u>285</u>	<u>29.3</u>
453.povray	<u>195</u>	<u>27.2</u>	195	27.2	195	27.3	153	34.9	153	34.8	<u>153</u>	<u>34.8</u>
454.calculix	308	26.8	<u>309</u>	<u>26.7</u>	309	26.7	<u>296</u>	<u>27.8</u>	296	27.8	296	27.8
459.GemsFDTD	228	46.6	<u>228</u>	<u>46.6</u>	229	46.4	<u>155</u>	<u>68.3</u>	155	68.3	156	68.2
465.tonto	<u>393</u>	<u>25.1</u>	393	25.1	391	25.2	347	28.4	347	28.3	<u>347</u>	<u>28.4</u>
470.lbm	<u>253</u>	<u>54.2</u>	253	54.2	254	54.2	<u>253</u>	<u>54.2</u>	253	54.2	254	54.2
481.wrf	275	40.6	<u>275</u>	<u>40.7</u>	273	40.9	274	40.7	273	40.9	<u>273</u>	<u>40.9</u>
482.sphinx3	477	40.8	496	39.3	<u>482</u>	<u>40.5</u>	477	40.8	496	39.3	<u>482</u>	<u>40.5</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
KMP\_STACKSIZE set to 200M

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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## Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel -auto-ilp32



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090805.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090805.01.xml>

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