



SPEC® CFP2006 Result

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NEC Corporation

Express5800/B120a
(Intel Xeon L5520)

SPECfp®2006 = 31.5

SPECfp_base2006 = 29.5

CPU2006 license: 9006

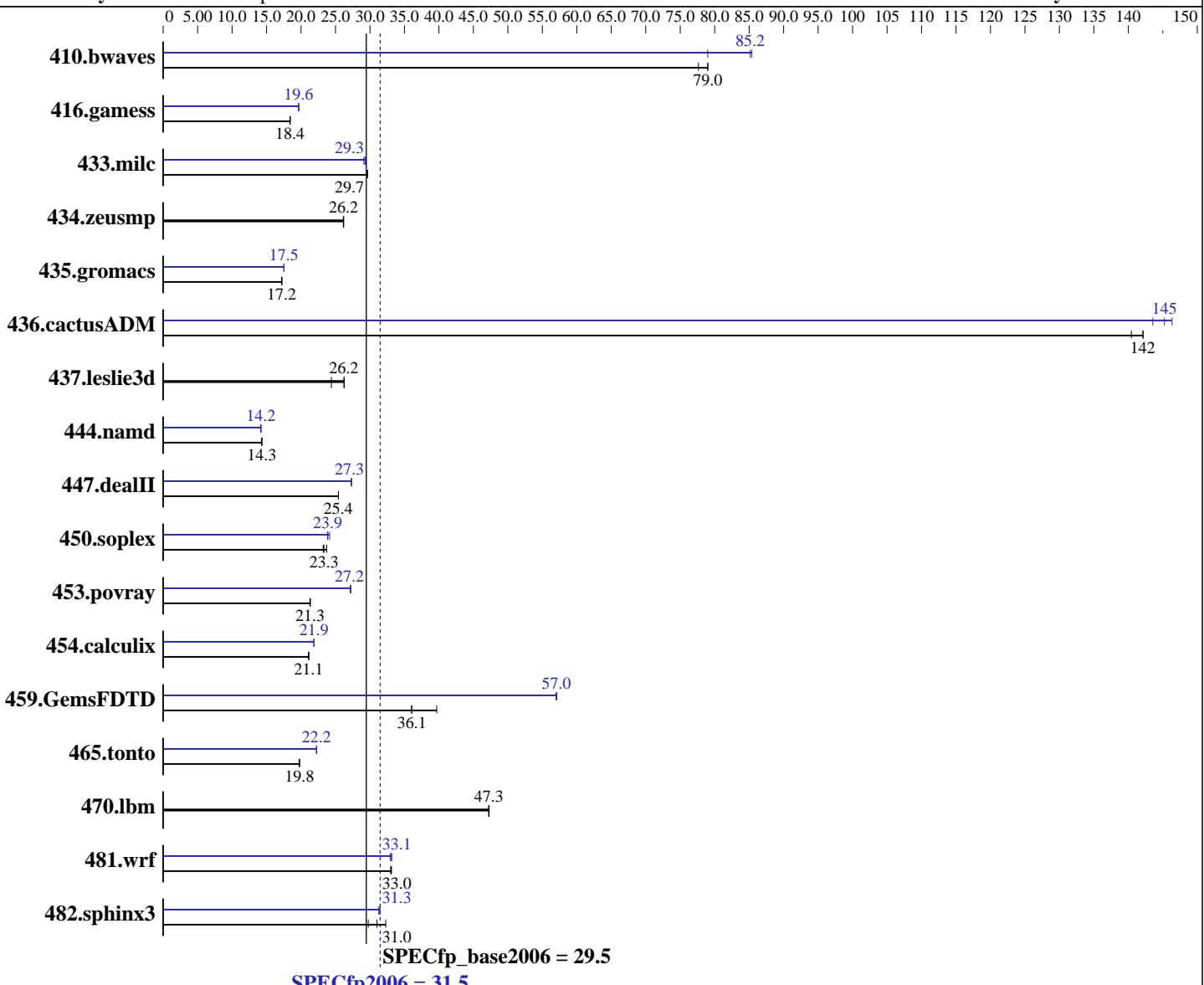
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jul-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon L5520
CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz: 2267
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64)
SP2 with patch Linux kernel 20090119,
Kernel 2.6.16.60-0.34-smp
Compiler: Intel C++ and Fortran Compiler Professional 11.0
for Linux
Build 20090131 Package ID: l_cproc_p_11.0.081,
l_cprof_p_11.0.081
Auto Parallel: Yes
File System: ReiserFS

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 X 4 GB PC3-8500R, 2 rank, CL7, ECC)
Disk Subsystem: 1x73 GB SATA2, 10000 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	175	77.6	172	79.0	<u>172</u>	<u>79.0</u>	159	85.4	<u>160</u>	<u>85.2</u>	172	79.0
416.gamess	1062	18.4	<u>1064</u>	<u>18.4</u>	1064	18.4	<u>997</u>	<u>19.6</u>	995	19.7	997	19.6
433.milc	310	29.7	<u>310</u>	<u>29.7</u>	310	29.6	<u>313</u>	<u>29.3</u>	315	29.1	313	29.3
434.zeusmp	347	26.2	348	26.1	<u>348</u>	<u>26.2</u>	347	26.2	348	26.1	<u>348</u>	<u>26.2</u>
435.gromacs	414	17.2	<u>415</u>	<u>17.2</u>	416	17.2	<u>407</u>	<u>17.5</u>	408	17.5	407	17.5
436.cactusADM	84.1	142	<u>84.1</u>	<u>142</u>	85.1	140	81.7	146	83.3	144	<u>82.3</u>	<u>145</u>
437.leslie3d	358	26.3	385	24.4	<u>358</u>	<u>26.2</u>	358	26.3	385	24.4	<u>358</u>	<u>26.2</u>
444.namd	560	14.3	561	14.3	<u>560</u>	<u>14.3</u>	566	14.2	<u>566</u>	<u>14.2</u>	566	14.2
447.dealII	450	25.4	<u>450</u>	<u>25.4</u>	450	25.4	<u>419</u>	<u>27.3</u>	419	27.3	419	27.3
450.soplex	359	23.2	351	23.7	<u>358</u>	<u>23.3</u>	345	24.1	<u>349</u>	<u>23.9</u>	349	23.9
453.povray	250	21.3	249	21.4	<u>250</u>	<u>21.3</u>	195	27.2	196	27.1	<u>196</u>	<u>27.2</u>
454.calculix	390	21.1	<u>391</u>	<u>21.1</u>	391	21.1	377	21.9	<u>377</u>	<u>21.9</u>	377	21.9
459.GemsFDTD	295	36.0	<u>294</u>	<u>36.1</u>	267	39.7	186	57.1	<u>186</u>	<u>57.0</u>	186	57.0
465.tonto	498	19.7	497	19.8	<u>498</u>	<u>19.8</u>	442	22.2	<u>442</u>	<u>22.2</u>	443	22.2
470.lbm	291	47.3	291	47.2	<u>291</u>	<u>47.3</u>	291	47.3	291	47.2	<u>291</u>	<u>47.3</u>
481.wrf	<u>338</u>	<u>33.0</u>	338	33.0	337	33.1	339	33.0	337	33.1	<u>337</u>	<u>33.1</u>
482.sphinx3	604	32.3	655	29.8	<u>628</u>	<u>31.0</u>	<u>622</u>	<u>31.3</u>	<u>623</u>	<u>31.3</u>	620	31.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

Platform Notes

BIOS setting:
NUMA configuration : Enabled



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Fortran benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

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Peak Optimization Flags (Continued)

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
           -fno-alias -auto-ilp32
```

```
447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll12 -ansi-alias -scalar-rep -opt-prefetch
```

```
450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -opt-malloc-options=3
```

```
453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -unroll14 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
            -parallel
```

```
416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -unroll12 -Ob0 -ansi-alias -scalar-rep-
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -unroll12 -Ob0 -opt-prefetch -parallel
```

```
465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
            -unroll14 -auto
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -opt-prefetch -auto-ilp32
```

```
436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                 -unroll12 -opt-prefetch -parallel -auto-ilp32
```

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Peak Optimization Flags (Continued)

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revH.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revH.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.xml>

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For other inquiries, please contact webmaster@spec.org.

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