# SPEC® CINT2006 Result

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## Dell Inc.

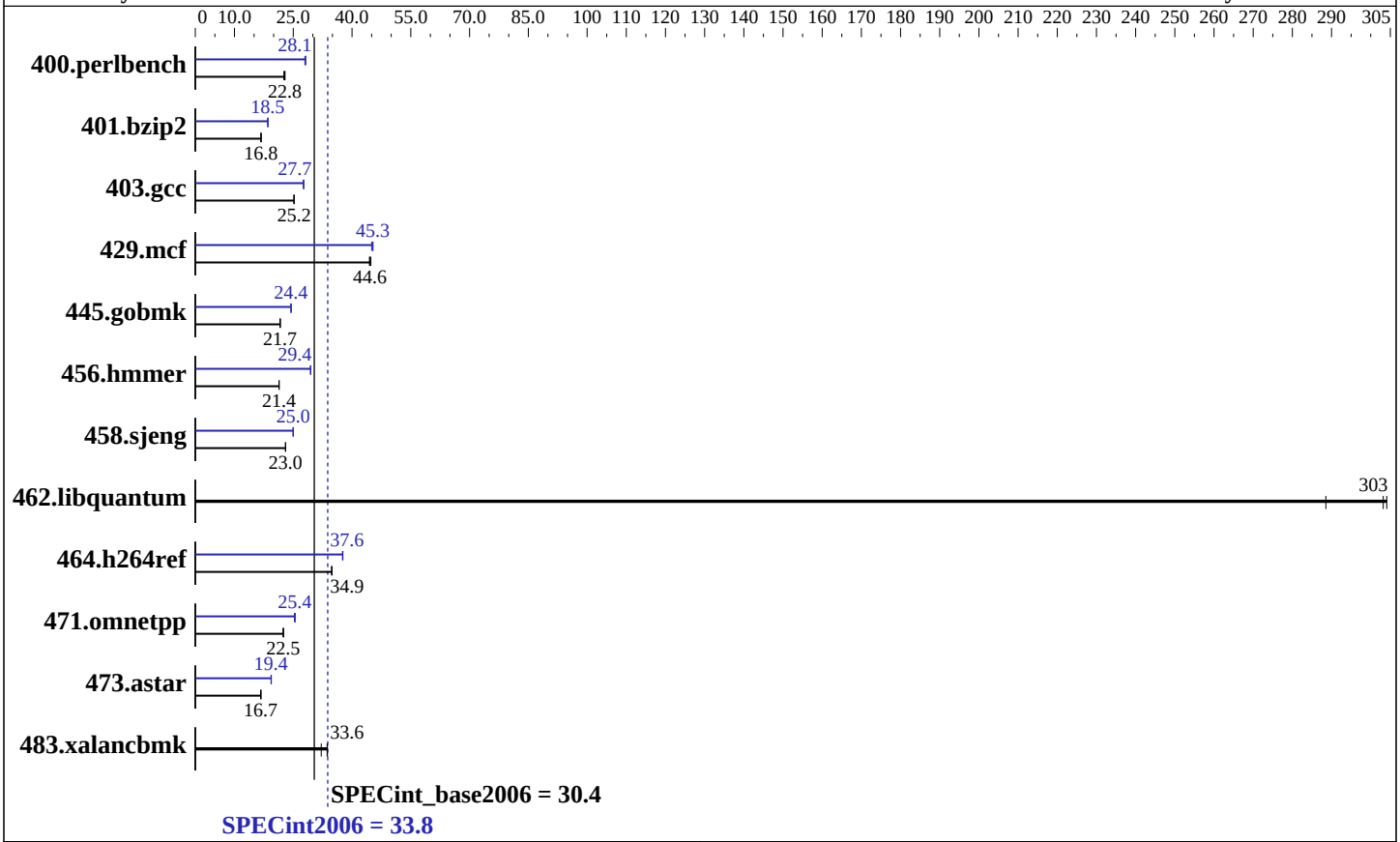
PowerEdge M610 (Intel Xeon X5560, 2.80 GHz)

SPECint®2006 = 33.8

SPECint_base2006 = 30.4

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Mar-2009
**Hardware Availability:** Mar-2009
**Software Availability:** Feb-2009




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5560 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.20 GHz |
| CPU MHz: | 2800 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 24 GB (6 x 4 GB DDR3-1333 DR RDIMM) |
| Disk Subsystem: | 1 x 146 GB 10000 RPM SAS |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp |
| Compiler: | Intel C++ Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080 |
| Auto Parallel: | Yes |
| File System: | ReiserFS |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V8.1<br>Binutils 2.18.50.0.7.20080502 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 432 | 22.6 | 427 | 22.9 | **428** | **22.8** | **347** | **28.1** | 349 | 28.0 | 347 | 28.2 |
| 401.bzip2 | 577 | 16.7 | 574 | 16.8 | **575** | **16.8** | **521** | **18.5** | 521 | 18.5 | 521 | 18.5 |
| 403.gcc | 319 | 25.2 | **319** | **25.2** | 320 | 25.2 | 291 | 27.7 | **291** | **27.7** | 292 | 27.6 |
| 429.mcf | **205** | **44.6** | 205 | 44.4 | 204 | 44.8 | 203 | 45.0 | 201 | 45.4 | **201** | **45.3** |
| 445.gobmk | 484 | 21.7 | 484 | 21.7 | **484** | **21.7** | 429 | 24.4 | 429 | 24.4 | **429** | **24.4** |
| 456.hmmer | 436 | 21.4 | 437 | 21.4 | **436** | **21.4** | 317 | 29.4 | 317 | 29.4 | **317** | **29.4** |
| 458.sjeng | 525 | 23.1 | **525** | **23.0** | 526 | 23.0 | 484 | 25.0 | 484 | 25.0 | **484** | **25.0** |
| 462.libquantum | 71.8 | 289 | **68.3** | **303** | 68.1 | 304 | 71.8 | 289 | **68.3** | **303** | 68.1 | 304 |
| 464.h264ref | 634 | 34.9 | 636 | 34.8 | **634** | **34.9** | 587 | 37.7 | 589 | 37.6 | **588** | **37.6** |
| 471.omnetpp | 278 | 22.5 | 279 | 22.4 | **278** | **22.5** | 247 | 25.3 | **246** | **25.4** | 245 | 25.5 |
| 473.astar | **420** | **16.7** | 419 | 16.7 | 420 | 16.7 | **363** | **19.4** | 363 | 19.3 | 362 | 19.4 |
| 483.xalancbmk | 205 | 33.7 | **205** | **33.6** | 215 | 32.1 | 205 | 33.7 | **205** | **33.6** | 215 | 32.1 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
```

## Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -par-runtime-control -opt-prefetch`

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## Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

## Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

458.sjeng: `/opt/intel/Compiler/11.0/080/bin/intel64/icc`

C++ benchmarks (except as noted below):
`icpc`

473.astar: `/opt/intel/Compiler/11.0/080/bin/intel64/icpc`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-ansi-alias -opt-prefetch`

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## Peak Optimization Flags (Continued)

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml


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