



SPEC® CFP2006 Result

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Intel Corporation

Supermicro SuperServer 6026T-NTR+ (Intel Xeon E5540, 2.53 GHz)

SPECfp®_rate2006 = 169

SPECfp_rate_base2006 = 162

CPU2006 license: 13

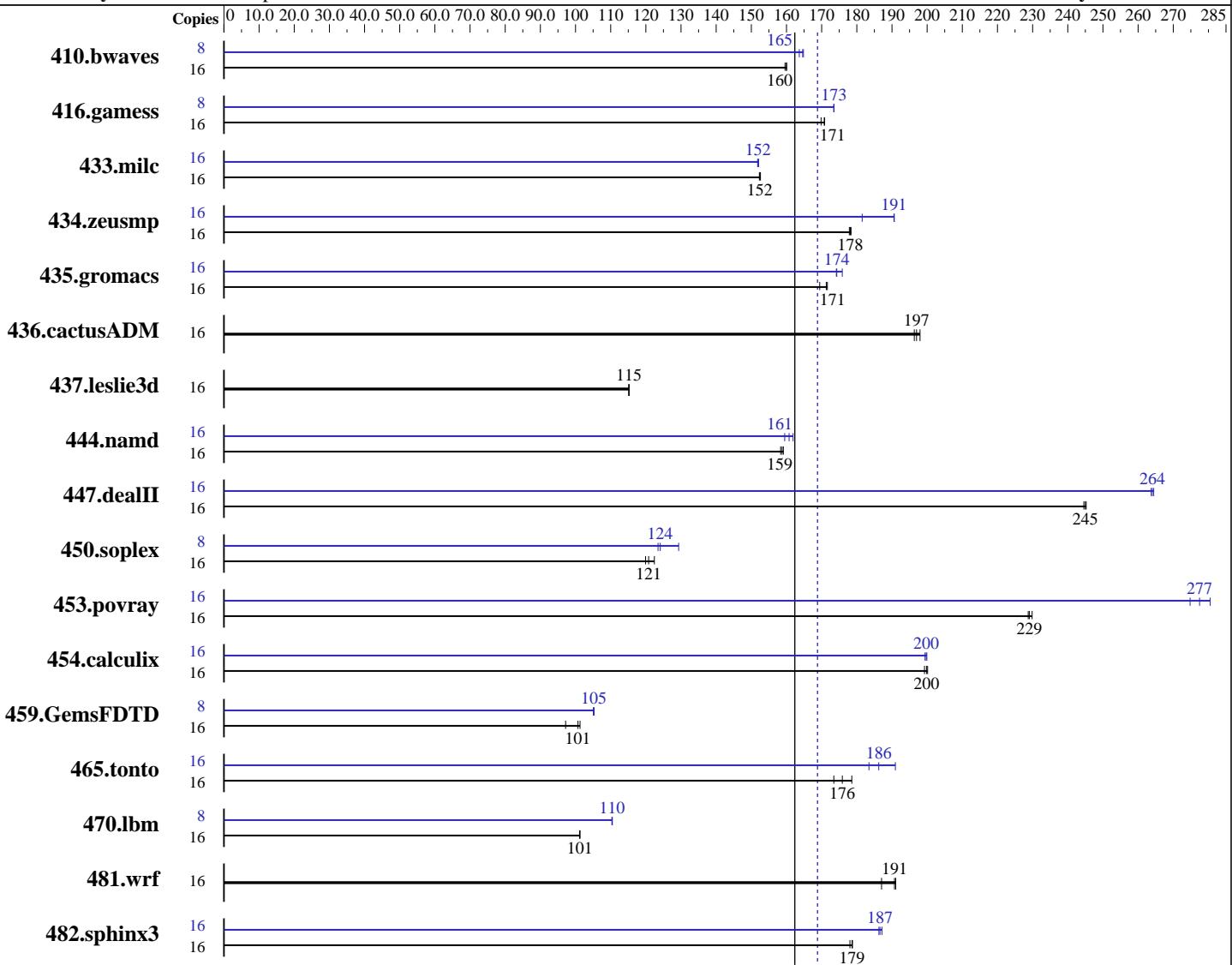
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Mar-2009

Hardware Availability: Mar-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon E5540
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2533
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.34-smp for x86_64
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (12 x 2GB DDR3-1066, CL7)
 Disk Subsystem: 1 x 150 GB SATA, 10000RPM
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1359	160	1363	160	1359	160	8	664	164	661	165	660	165
416.gamess	16	1845	170	1835	171	1834	171	8	903	174	903	173	903	173
433.milc	16	965	152	963	153	964	152	16	968	152	966	152	967	152
434.zeusmp	16	816	178	819	178	817	178	16	764	191	764	191	802	182
435.gromacs	16	667	171	666	172	674	169	16	655	174	650	176	656	174
436.cactusADM	16	974	196	971	197	966	198	16	974	196	971	197	966	198
437.leslie3d	16	1306	115	1306	115	1305	115	16	1306	115	1306	115	1305	115
444.namd	16	807	159	810	158	808	159	16	798	161	793	162	805	159
447.dealII	16	747	245	746	245	748	245	16	693	264	692	264	694	264
450.soplex	16	1113	120	1105	121	1090	122	8	540	123	538	124	516	129
453.povray	16	372	229	372	229	370	230	16	310	275	307	277	303	281
454.calculix	16	660	200	661	200	662	199	16	661	200	660	200	662	199
459.GemsFDTD	16	1747	97.2	1686	101	1676	101	8	806	105	807	105	808	105
465.tonto	16	882	179	895	176	908	173	16	824	191	845	186	858	183
470.lbm	16	2171	101	2174	101	2172	101	8	995	110	995	110	996	110
481.wrf	16	937	191	935	191	956	187	16	937	191	935	191	956	187
482.sphinx3	16	1751	178	1746	179	1744	179	16	1674	186	1666	187	1671	187

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Base Compiler Invocation

C benchmarks:
 icc

C++ benchmarks:
 icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
        470.lbm: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

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Peak Compiler Invocation (Continued)

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):

`icpc`

450.soplex: `icpc -m32`

Fortran benchmarks:

`ifort`

Benchmarks using both Fortran and C:

`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
 433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
 444.namd: `-DSPEC_CPU_LP64`
 447.dealII: `-DSPEC_CPU_LP64`
 453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
 465.tonto: `-DSPEC_CPU_LP64`
 470.lbm: `-DSPEC_CPU_LP64`
 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4_2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)`
 `-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)`
 `-fno-alias`

470.lbm: `-xSSE4_2 -ipo -O3 -no-prec-div -static -opt-prefetch`
 `-auto-ilp32`

482.sphinx3: `-xSSE4_2 -ipo -O3 -no-prec-div -static -unroll12`

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -fno-alias -auto-ilp32

447.dealII: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll12 -ansi-alias -scalar-rep-

450.soplex: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-malloc-options=3

453.povray: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsse4 .2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll12 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll12 -Ob0 -opt-prefetch

465.tonto: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: -xsse4 .2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.xml>

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