SPEC® CFP2006 Result

Intel Corporation

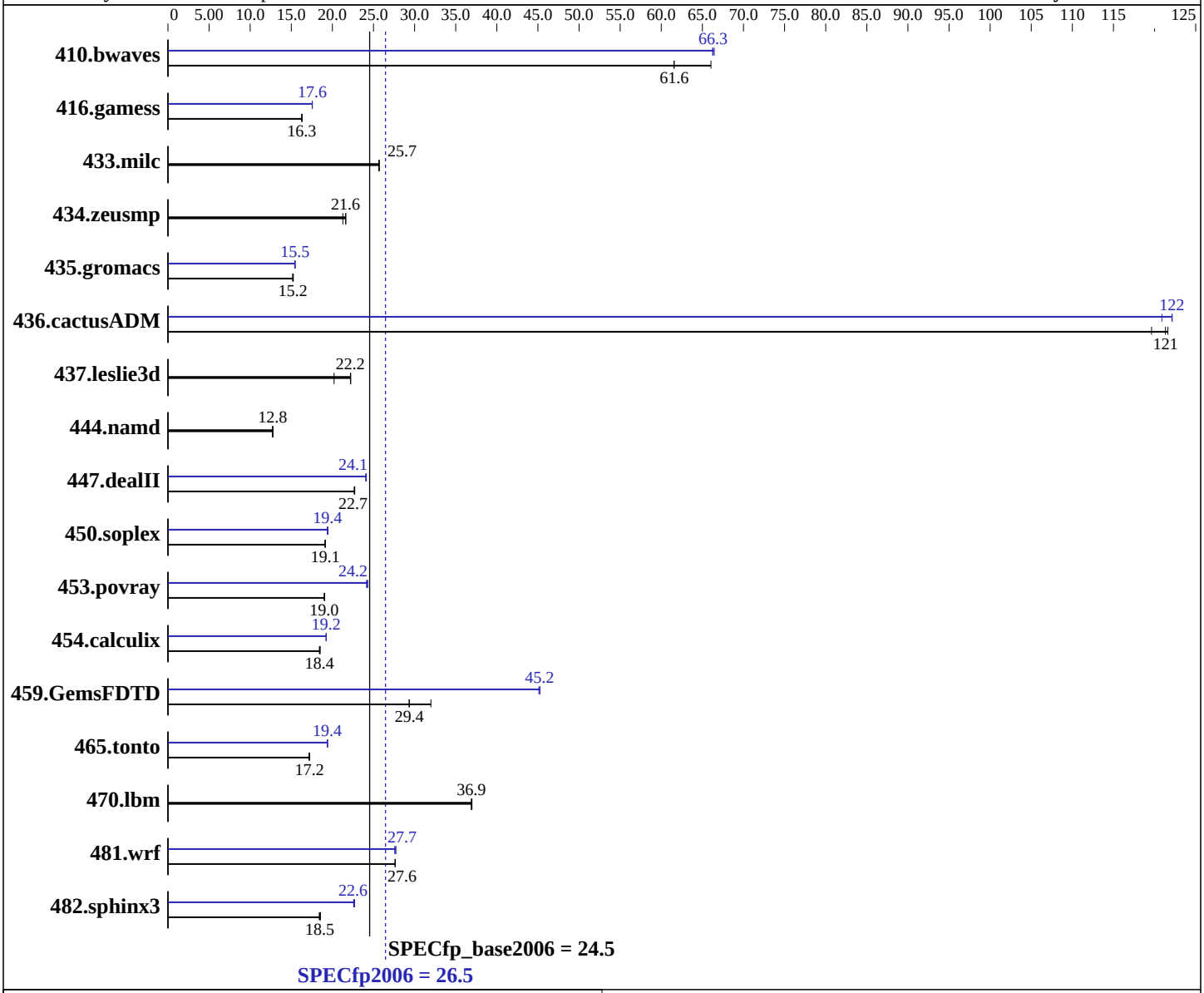
Supermicro SuperServer 6026T-NTR+ (Intel Xeon L5506, 2.13 GHz)

SPECfp®2006 = 26.5

SPECfp_base2006 = 24.5

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Mar-2009
Hardware Availability: Mar-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon L5506
CPU Characteristics:	
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.34-smp for x86_64
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (12 x 2GB DDR3-1066, CL7)
Disk Subsystem: 1 x 150 GB SATA, 10000RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	221	61.5	206	66.0	**221**	**61.6**	205	66.2	205	66.4	**205**	**66.3**
416.gamess	**1201**	**16.3**	1204	16.3	1199	16.3	1116	17.5	**1115**	**17.6**	1115	17.6
433.milc	**358**	**25.7**	357	25.7	358	25.7	**358**	**25.7**	357	25.7	358	25.7
434.zeusmp	**421**	**21.6**	421	21.6	428	21.3	**421**	**21.6**	421	21.6	428	21.3
435.gromacs	468	15.3	**470**	**15.2**	471	15.2	461	15.5	**462**	**15.5**	463	15.4
436.cactusADM	98.3	122	**98.5**	**121**	99.9	120	**97.9**	**122**	97.8	122	98.9	121
437.leslie3d	**424**	**22.2**	465	20.2	423	22.2	**424**	**22.2**	465	20.2	423	22.2
444.namd	629	12.8	631	12.7	**629**	**12.8**	629	12.8	631	12.7	**629**	**12.8**
447.dealII	503	22.7	505	22.6	**504**	**22.7**	476	24.0	**474**	**24.1**	474	24.1
450.soplex	437	19.1	436	19.1	**436**	**19.1**	**429**	**19.4**	429	19.5	430	19.4
453.povray	279	19.0	280	19.0	**280**	**19.0**	219	24.3	**219**	**24.2**	220	24.1
454.calculix	**447**	**18.4**	445	18.5	448	18.4	429	19.2	429	19.2	**429**	**19.2**
459.GemsFDTD	**361**	**29.4**	362	29.3	332	32.0	235	45.1	**235**	**45.2**	235	45.2
465.tonto	571	17.2	574	17.2	**572**	**17.2**	**507**	**19.4**	506	19.4	508	19.4
470.lbm	372	37.0	372	36.9	**372**	**36.9**	372	37.0	372	36.9	**372**	**36.9**
481.wrf	**404**	**27.6**	404	27.7	404	27.6	**403**	**27.7**	405	27.6	403	27.7
482.sphinx3	1059	18.4	1051	18.6	**1056**	**18.5**	**862**	**22.6**	863	22.6	858	22.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

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Peak Compiler Invocation (Continued)

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):
icpc

450.soplex: icpc -m32

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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