



SPEC® CINT2006 Result

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ASUSTeK Computer Inc.

SPECint®2006 = 35.6

ASUS P6T6 WS REVOLUTION workstation motherboard
(Intel Core i7-965 Extreme Edition)

SPECint_base2006 = 32.1

CPU2006 license: 9016

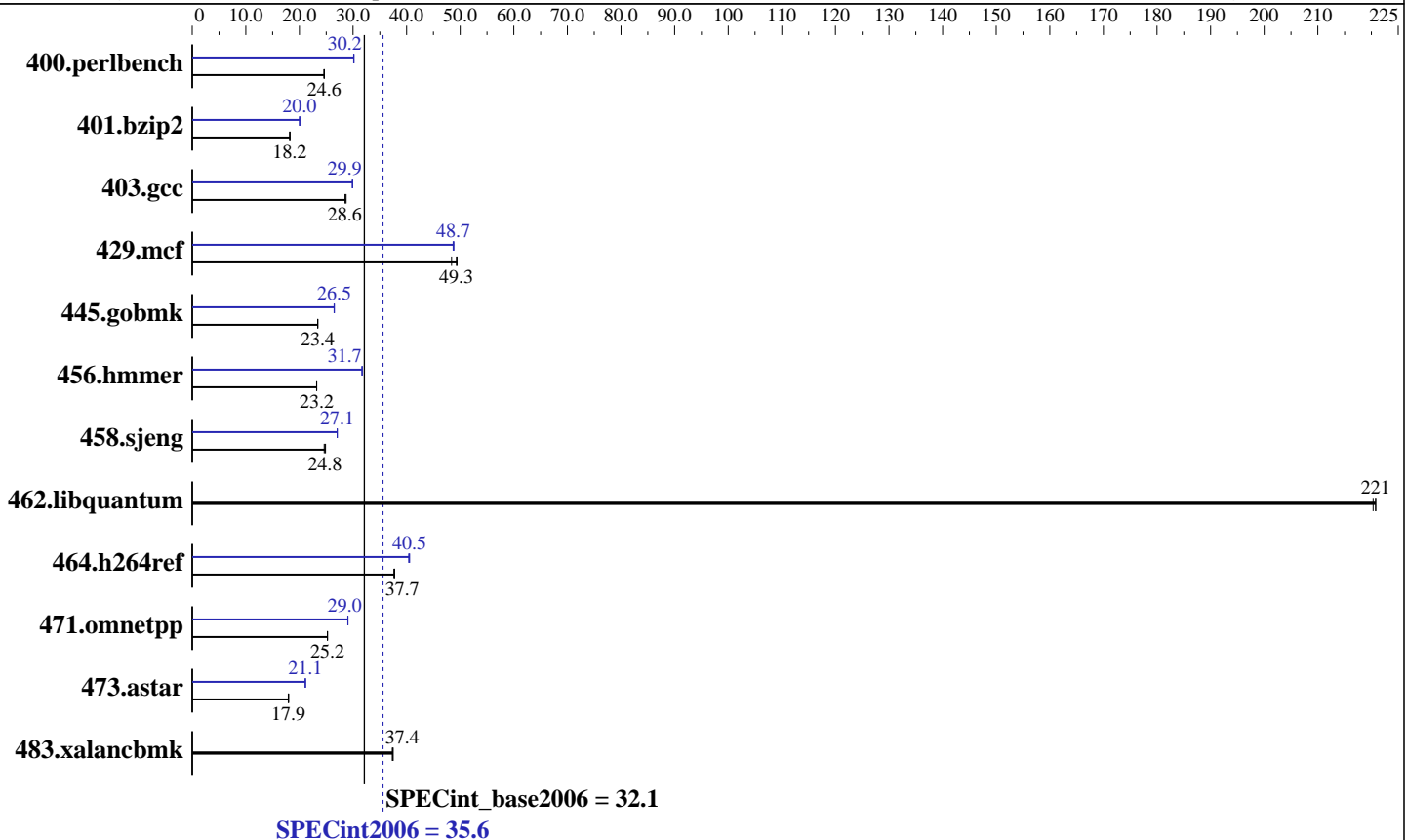
Test date: Feb-2009

Test sponsor: ASUSTeK Computer Inc.

Hardware Availability: Nov-2008

Tested by: ASUSTeK Computer Inc.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core i7-965 Extreme Edition
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 12 GB (6 x 2 GB PC3-10600E, CL=9)
 Disk Subsystem: HITACHI HDT725050VLA360 500GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 SP2
 Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler Professional 11.0 for Linux
 Build 20090131 Package ID: l_cproc_p_11.0.080
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	396	24.6	396	24.7	398	24.6	324	30.2	324	30.2	324	30.1
401.bzip2	531	18.2	529	18.2	530	18.2	481	20.0	481	20.1	481	20.0
403.gcc	280	28.7	282	28.5	282	28.6	269	29.9	269	29.9	269	29.9
429.mcf	188	48.4	185	49.4	185	49.3	187	48.7	187	48.7	187	48.8
445.gobmk	448	23.4	448	23.4	448	23.4	396	26.5	396	26.5	396	26.5
456.hmmer	402	23.2	402	23.2	402	23.2	294	31.7	294	31.7	294	31.7
458.sjeng	487	24.9	491	24.6	489	24.8	447	27.1	448	27.0	447	27.1
462.libquantum	94.0	220	93.8	221	93.8	221	94.0	220	93.8	221	93.8	221
464.h264ref	588	37.6	586	37.8	587	37.7	546	40.5	546	40.5	548	40.4
471.omnetpp	248	25.2	248	25.2	248	25.2	215	29.1	215	29.0	215	29.0
473.astar	391	17.9	390	18.0	392	17.9	333	21.1	333	21.1	332	21.1
483.xalancbmk	185	37.3	184	37.5	184	37.4	185	37.3	184	37.5	184	37.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
```

Platform Notes

```
BIOS setting:
Hardware Prefetcher: Enabled
Adjacent Cache Line Prefetch: Enabled
Tested system case compliance with Intel ATX spec
600W or higher ATX Power Supply
System was configured with ATi RV530LE discrete graphics card
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.aster: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

473.aster: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.02.xml>

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For other inquiries, please contact webmaster@spec.org.

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