SPEC® CFP2006 Result

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NEC Corporation

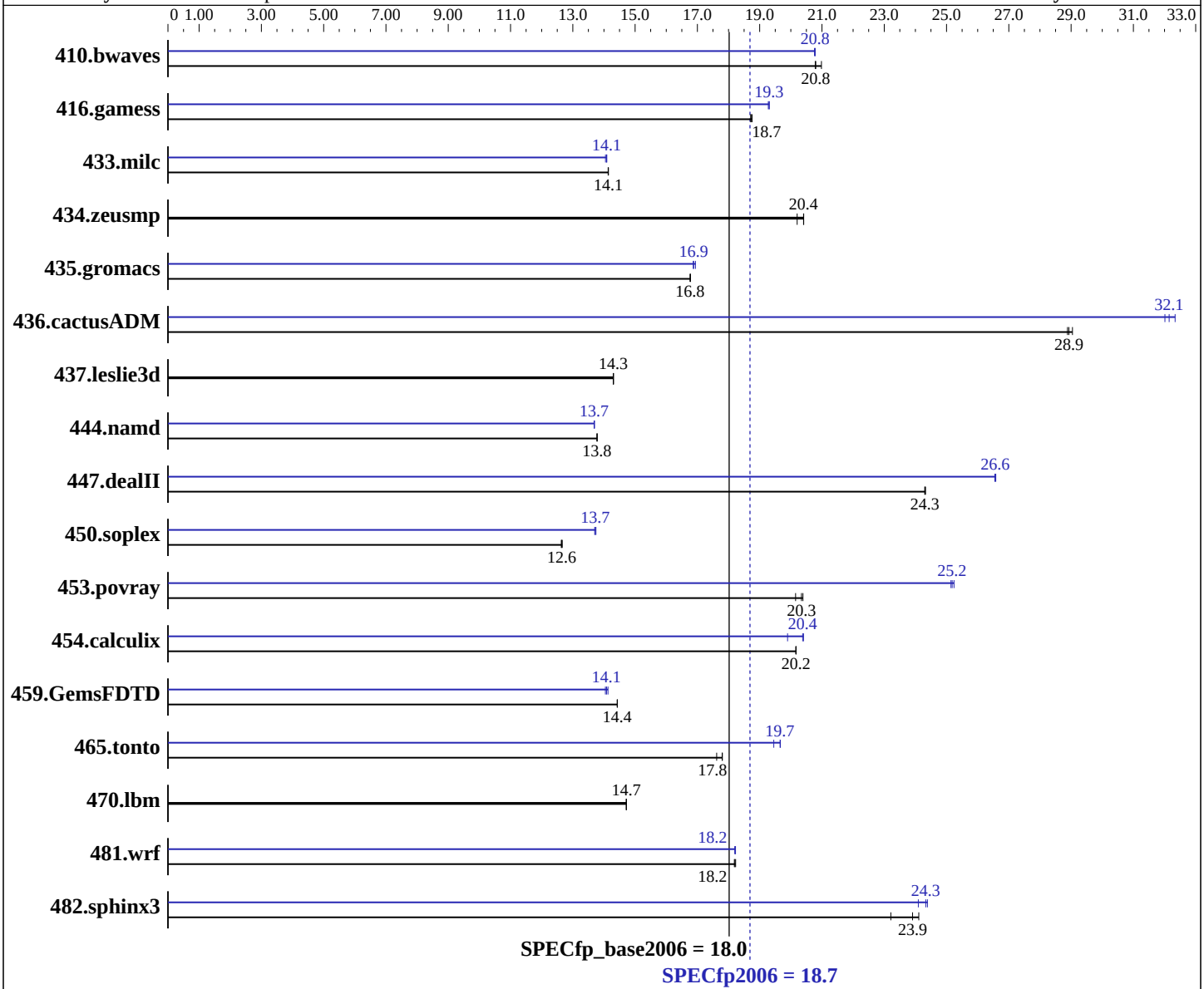
Express5800/iR110a-1H
(Intel Core 2 Duo T9400)

SPECfp®2006 = 18.7

SPECfp_base2006 = 18.0

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Feb-2009
Hardware Availability: Jan-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core 2 Duo T9400
CPU Characteristics:	1066 MHz system bus
CPU MHz:	2533
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20081105 Package ID: l_cproc_p_11.0.074, l_fproc_p_11.0.074
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-5300P, 1 rank, CL5-5-5, ECC)
Disk Subsystem: 1x160 GB SATAII, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	648	21.0	**653**	**20.8**	654	20.8	**654**	**20.8**	655	20.8	654	20.8
416.gamess	1046	18.7	1044	18.8	**1046**	**18.7**	1014	19.3	**1014**	**19.3**	1016	19.3
433.milc	649	14.1	649	14.1	**649**	**14.1**	653	14.0	**652**	**14.1**	652	14.1
434.zeusmp	**446**	**20.4**	446	20.4	450	20.2	**446**	**20.4**	446	20.4	450	20.2
435.gromacs	426	16.8	**426**	**16.8**	426	16.8	**423**	**16.9**	423	16.9	422	16.9
436.cactusADM	**413**	**28.9**	411	29.0	414	28.9	373	32.0	370	32.3	**372**	**32.1**
437.leslie3d	**657**	**14.3**	657	14.3	657	14.3	**657**	**14.3**	657	14.3	657	14.3
444.namd	582	13.8	582	13.8	**582**	**13.8**	586	13.7	586	13.7	**586**	**13.7**
447.dealII	**471**	**24.3**	470	24.3	471	24.3	430	26.6	431	26.6	**431**	**26.6**
450.soplex	660	12.6	659	12.7	**660**	**12.6**	**608**	**13.7**	607	13.7	609	13.7
453.povray	264	20.2	261	20.4	**262**	**20.3**	211	25.2	**211**	**25.2**	212	25.1
454.calculix	409	20.2	**409**	**20.2**	409	20.2	**405**	**20.4**	404	20.4	415	19.9
459.GemsFDTD	735	14.4	**735**	**14.4**	735	14.4	755	14.0	**753**	**14.1**	751	14.1
465.tonto	**553**	**17.8**	553	17.8	558	17.6	500	19.7	**501**	**19.7**	506	19.5
470.lbm	**934**	**14.7**	934	14.7	933	14.7	**934**	**14.7**	934	14.7	933	14.7
481.wrf	614	18.2	613	18.2	**614**	**18.2**	**613**	**18.2**	614	18.2	613	18.2
482.sphinx3	808	24.1	**815**	**23.9**	840	23.2	809	24.1	**801**	**24.3**	799	24.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 200M
```

Platform Notes

```
Bios settings:
Hardware Prefetcher:            Enabled
Adjacent Cache Line Prefetch:   Enabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

482.sphinx3: /opt/intel/Compiler/11.0/074/bin/ia32/icc
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/Compiler/11.0/074/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

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Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revE.20090710.html`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revE.20090710.xml`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml`

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Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 22:29:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 March 2009.