SPEC® CFP2006 Result

IBM Corporation

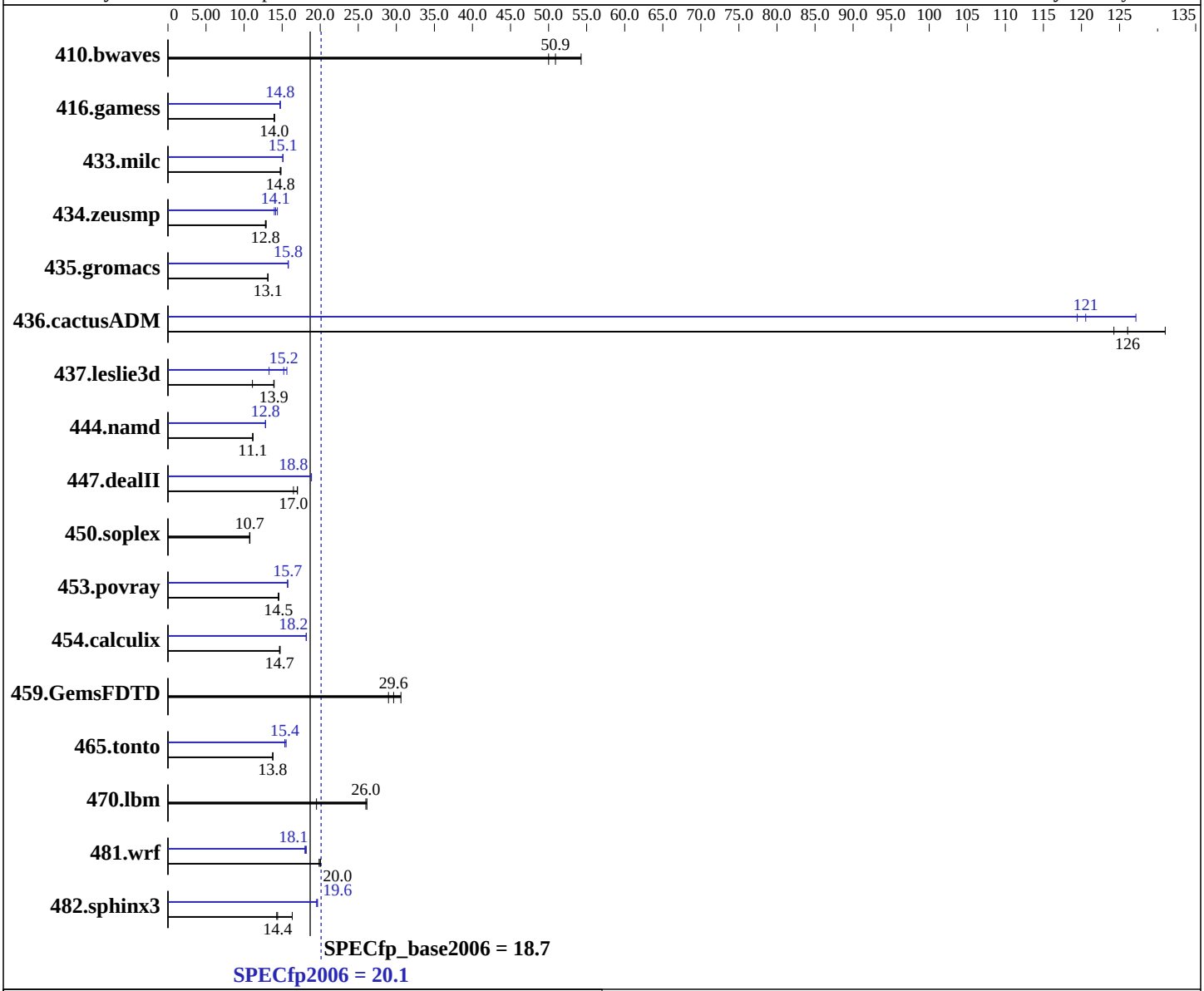
IBM BladeCenter LS42 (AMD Opteron 8376 HE)

SPECfp®2006 = 20.1

SPECfp_base2006 = 18.7

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Feb-2009
Hardware Availability: Mar-2009
Software Availability: May-2008



Hardware

CPU Name:	AMD Opteron 8376 HE
CPU Characteristics:	
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	1,2,3,4 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	PGI Server Complete Version 7.2
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	32/64-bit
Peak Pointers:	64-bit
Other Software:	binutils 2.18.50

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 64 GB (16 x 4 GB DDR2-6400 ECC)
Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
Other Hardware: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**267**	**50.9**	272	50.0	250	54.3	**267**	**50.9**	272	50.0	250	54.3
416.gamess	1399	14.0	**1400**	**14.0**	1405	13.9	**1327**	**14.8**	1330	14.7	1326	14.8
433.milc	618	14.9	**621**	**14.8**	622	14.8	609	15.1	607	15.1	**608**	**15.1**
434.zeusmp	711	12.8	705	12.9	**710**	**12.8**	633	14.4	**644**	**14.1**	651	14.0
435.gromacs	**544**	**13.1**	544	13.1	544	13.1	452	15.8	453	15.8	**452**	**15.8**
436.cactusADM	96.2	124	**94.8**	**126**	91.2	131	**99.1**	**121**	100	119	94.0	127
437.leslie3d	**676**	**13.9**	674	14.0	847	11.1	**618**	**15.2**	602	15.6	708	13.3
444.namd	719	11.2	**720**	**11.1**	720	11.1	**626**	**12.8**	625	12.8	626	12.8
447.dealII	694	16.5	673	17.0	**673**	**17.0**	607	18.9	607	18.8	**607**	**18.8**
450.soplex	**777**	**10.7**	774	10.8	777	10.7	**777**	**10.7**	774	10.8	777	10.7
453.povray	**366**	**14.5**	367	14.5	365	14.6	**338**	**15.7**	338	15.7	338	15.7
454.calculix	563	14.7	560	14.7	**562**	**14.7**	455	18.1	454	18.2	**454**	**18.2**
459.GemsFDTD	366	29.0	347	30.6	**358**	**29.6**	366	29.0	347	30.6	**358**	**29.6**
465.tonto	**714**	**13.8**	715	13.8	714	13.8	641	15.4	**640**	**15.4**	634	15.5
470.lbm	**529**	**26.0**	526	26.1	704	19.5	**529**	**26.0**	526	26.1	704	19.5
481.wrf	**559**	**20.0**	563	19.9	558	20.0	**618**	**18.1**	615	18.2	621	18.0
482.sphinx3	**1353**	**14.4**	1194	16.3	1364	14.3	998	19.5	**995**	**19.6**	992	19.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores
```

Operating System Notes

```
The libhugetlbfs libraries were installed using the
installation rpms that came with the distribution.

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=14336 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

Continued on next page

Operating System Notes (Continued)

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH to "/cpu2006/pgi72/linux_lib32:/cpu2006/pgi72/linux_lib64"
PGI_HUGE_PAGES = "14336"
SPEC_DIR = "/cpu2006"
NCPUS = "16"

Processor Performance States Disabled in BIOS
Memory ChipKill Disabled in BIOS
```

Base Compiler Invocation

C benchmarks:
pgcc

C++ benchmarks:
pgcpp

Fortran benchmarks:
pgf95

Benchmarks using both Fortran and C:
pgcc pgf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -Mnomain
436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -Mnomain
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-Mvect=cachesize:6291456 -fastsse -Msmartalloc=huge -Mconcur
-Mfprelaxed -Mipa=fast -Mipa=inline -tp barcelona-64 -Bstatic_pgi
```

C++ benchmarks:
```
-Mvect=cachesize:6291456 -fastsse -Msmartalloc=huge -Mfprelaxed
-Mconcur --zc_eh -Mipa=fast -Mipa=inline -tp barcelona-64
-Bstatic_pgi
```

Fortran benchmarks:
```
-Mvect=cachesize:6291456 -fastsse -Mfprelaxed -Msmartalloc=huge
-Mconcur -Mipa=fast -Mipa=inline -tp barcelona-64 -Bstatic_pgi
```

Benchmarks using both Fortran and C:
```
-Mvect=cachesize:6291456 -fastsse -Msmartalloc=huge -Mconcur
-Mfprelaxed -Mipa=fast -Mipa=inline -tp barcelona-64 -Bstatic_pgi
```

Base Other Flags

C benchmarks:
```
-Mipa=jobs:8
```

C++ benchmarks:
```
-Mipa=jobs:8
```

Fortran benchmarks:
```
-Mipa=jobs:8
```

Benchmarks using both Fortran and C:
```
-Mipa=jobs:8
```

Peak Compiler Invocation

C benchmarks:
```
pgcc
```

C++ benchmarks:
```
pgcpp
```

Fortran benchmarks:
```
pgf95
```

Benchmarks using both Fortran and C:
```
pgcc pgf95
```

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -Mnomain
436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -Mnomain
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -Mvect=cachesize:6291456 -fastsse -Msmartalloc=huge
-Msafeptr -Mconcur -Mfprelaxed -Mipa=inline -Mipa=arg
-Mipa=const -Mipa=ptr -Mipa=shape -tp barcelona-64
-Bstatic_pgi

470.lbm: basepeak = yes

482.sphinx3: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2)
-Mipa=inline(pass 2) -Mvect=cachesize:6291456 -fastsse
-Mfprelaxed -Msmartalloc -tp barcelona-64 -Bstatic_pgi

C++ benchmarks:

444.namd: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2)
-Mipa=inline(pass 2) -Mvect=cachesize:6291456 -fastsse
-Munroll=n:4 -Munroll=m:8 -Msmartalloc=huge -Mnodepchk
-Mfprelaxed --zc_eh -tp barcelona-64 -Bstatic_pgi

447.dealII: -Mvect=cachesize:6291456 -fastsse -alias=ansi
-Msmartalloc=huge -Mprefetch=t0 -Mnovect -Mfprelaxed
--zc_eh -Mipa=fast -Mipa=inline -tp barcelona-32
-Bstatic_pgi

450.soplex: basepeak = yes

453.povray: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inlinenopfo:3(pass 2)
-Mipa=staticfunc(pass 2) -Mvect=cachesize:6291456 -fastsse

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Peak Optimization Flags (Continued)

453.povray (continued):
-Msmartalloc=huge -Mprefetch=t0 -Mfprelaxed
-tp barcelona-64 -Bstatic_pgi

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2)
-Mipa=inline(pass 2) -Mvect=cachesize:6291456 -fastsse
-Msmartalloc=huge -Mvect=noaltcode -Mprefetch=t0
-Mfprelaxed -tp barcelona-64 -Bstatic_pgi

434.zeusmp: -Mvect=cachesize:6291456 -fastsse -Mfprelaxed -Mconcur
-Mprefetch=distance:8 -Mprefetch=t0 -Msmartalloc=huge
-Msmartalloc=hugebss -Mipa=fast -Mipa=inline
-tp barcelona-64 -Bstatic_pgi

437.leslie3d: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mconcur=noaltcode(pass 2) -Mipa=fast(pass 2)
-Mipa=inline(pass 2) -Mvect=cachesize:6291456 -fastsse
-Mvect=fuse -Msmartalloc=huge -Mprefetch=distance:8
-Mprefetch=t0 -Mfprelaxed -tp barcelona-64 -Bstatic_pgi

459.GemsFDTD: basepeak = yes

465.tonto: -Mvect=cachesize:6291456 -fastsse -O4 -Mvect=noaltcode
-Msmartalloc=huge -Mprefetch=distance:8 -Mprefetch=t0
-Mfprelaxed -Mipa=fast -Mipa=inline -tp barcelona-64
-Bstatic_pgi

Benchmarks using both Fortran and C:

435.gromacs: -Mvect=cachesize:6291456 -fastsse -Msmartalloc=huge
-Mfprelaxed -Mconcur -Mfpapprox=rsqrt -Mipa=fast
-Mipa=inline -tp barcelona-64 -Bstatic_pgi

436.cactusADM: -Mvect=cachesize:6291456 -fastsse -Msmartalloc=huge
-Mfprelaxed -Mconcur -Mdse -Mipa=fast -Mipa=inline
-tp barcelona-64 -Bstatic_pgi

454.calculix: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2)
-Mvect=cachesize:6291456 -fastsse -Msmartalloc=huge
-Mloop32 -Mprefetch=t0 -Mpre -Mfprelaxed -tp barcelona-64
-Bstatic_pgi

481.wrf: -Mvect=cachesize:6291456 -fastsse -Mvect=noaltcode
-Msmartalloc=huge -Mprefetch=distance:8 -Mconcur=noaltcode
-Mfprelaxed -tp barcelona-64 -Bstatic_pgi

Peak Other Flags

C benchmarks:
`-Mipa=jobs:8`(pass 2)

C++ benchmarks:
`-Mipa=jobs:8`(pass 2)

Fortran benchmarks:
`-Mipa=jobs:8`

Benchmarks using both Fortran and C (except as noted below):
`-Mipa=jobs:8`(pass 2)

481.wrf: `No flags used`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/pgi72_linux_flags.20090713.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/pgi72_linux_flags.20090713.00.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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