



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®2006 = 25.1

PowerEdge T300 (Intel Xeon X3353, 2.66 GHz)

SPECint\_base2006 = 22.3

CPU2006 license: 55

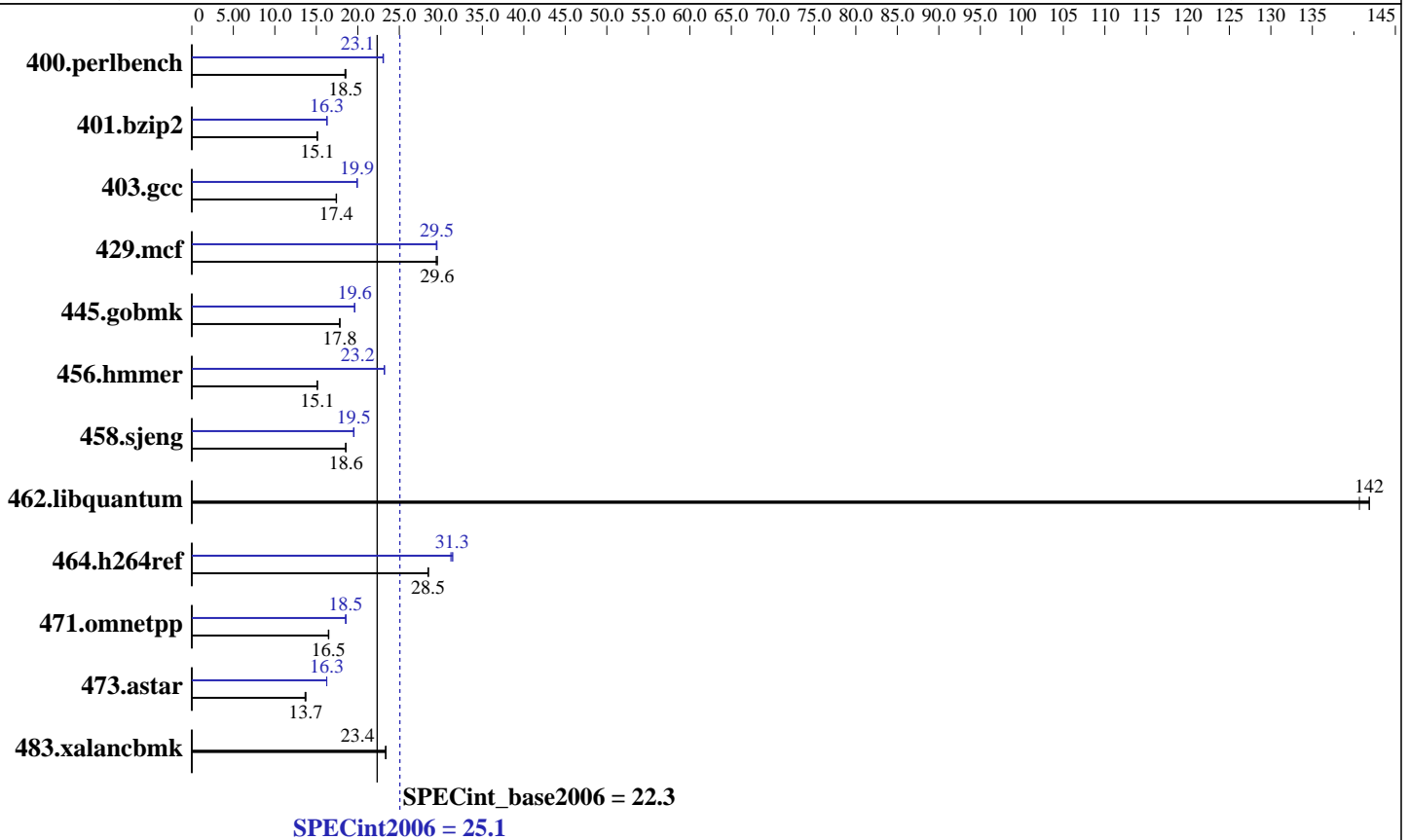
Test date: Nov-2008

Test sponsor: Dell Inc.

Hardware Availability: Oct-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



## Hardware

CPU Name: Intel Xeon X3353  
 CPU Characteristics:  
 CPU MHz: 2666  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB DDR2-667 FBDIMM)  
 Disk Subsystem: 1 x 300 GB SAS 15k RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>528</b>	<b>18.5</b>	530	18.4	527	18.6	423	23.1	424	23.0	<b>424</b>	<b>23.1</b>
401.bzip2	638	15.1	639	15.1	<b>638</b>	<b>15.1</b>	592	16.3	<b>593</b>	<b>16.3</b>	593	16.3
403.gcc	463	17.4	<b>462</b>	<b>17.4</b>	462	17.4	404	19.9	<b>404</b>	<b>19.9</b>	404	19.9
429.mcf	310	29.4	<b>309</b>	<b>29.6</b>	308	29.6	309	29.5	310	29.5	<b>309</b>	<b>29.5</b>
445.gobmk	588	17.8	<b>588</b>	<b>17.8</b>	589	17.8	535	19.6	<b>535</b>	<b>19.6</b>	535	19.6
456.hammer	617	15.1	617	15.1	<b>617</b>	<b>15.1</b>	402	23.2	<b>402</b>	<b>23.2</b>	402	23.2
458.sjeng	651	18.6	653	18.5	<b>652</b>	<b>18.6</b>	621	19.5	<b>620</b>	<b>19.5</b>	619	19.5
462.libquantum	147	141	<b>146</b>	<b>142</b>	146	142	147	141	<b>146</b>	<b>142</b>	146	142
464.h264ref	777	28.5	<b>777</b>	<b>28.5</b>	777	28.5	<b>706</b>	<b>31.3</b>	709	31.2	704	31.4
471.omnetpp	380	16.5	<b>379</b>	<b>16.5</b>	379	16.5	<b>337</b>	<b>18.5</b>	337	18.5	337	18.6
473.astar	511	13.7	<b>512</b>	<b>13.7</b>	515	13.6	<b>432</b>	<b>16.3</b>	432	16.2	432	16.3
483.xalancbmk	<b>295</b>	<b>23.4</b>	295	23.4	296	23.3	<b>295</b>	<b>23.4</b>	295	23.4	296	23.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc  
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib  
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -auto-ilp32 -opt-prefetch  
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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