SPEC® CINT2006 Result
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NEC Corporation

Express5800/R140a-4
(Intel Xeon E7450)

$SPECint^®2006 =$ 22.2

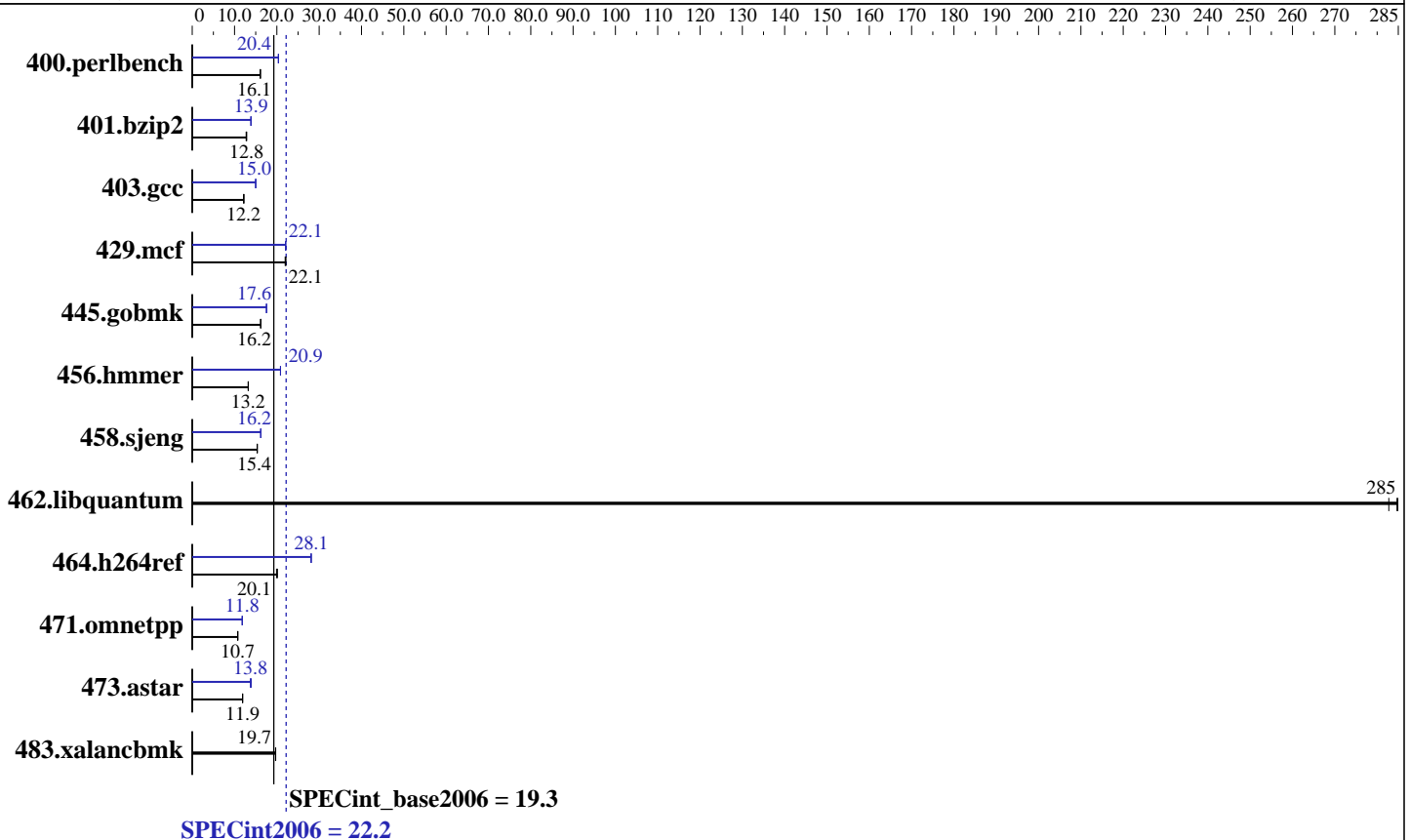$SPECint\_base2006 =$ 19.3

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: Bull SAS

Test date: Dec-2008

Hardware Availability: Nov-2008

Software Availability: Nov-2008

Benchmark	Value
400.perlbench	20.4 / 16.1
401.bzip2	13.9 / 12.8
403.gcc	15.0 / 12.2
429.mcf	22.1 / 22.1
445.gobmk	17.6 / 16.2
456.hmmer	20.9 / 13.2
458.sjeng	16.2 / 15.4
462.libquantum	285
464.h264ref	28.1 / 20.1
471.omnetpp	11.8 / 10.7
473.astar	13.8 / 11.9
483.xalancbmk	19.7

SPECint_base2006 = 19.3

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Hardware

CPU Name: Intel Xeon E7450

CPU Characteristics: 1066 MHz system bus

CPU MHz: 2400

FPU: Integrated

CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip

CPU(s) orderable: 1,2,3,4 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 9 MB I+D on chip per chip, 3 MB shared / 2 cores

L3 Cache: 12 MB I+D on chip per chip

Other Cache: None

Memory: 64 GB (16 x 4GB DDR2-667 FBDIMM)

Disk Subsystem: 1x146 GB SAS, 10000 RPM

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp

Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042

Auto Parallel: Yes

File System: ReiserFS

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**607**	**16.1**	608	16.1	607	16.1	**479**	**20.4**	479	20.4	480	20.3
401.bzip2	758	12.7	**752**	**12.8**	751	12.8	695	13.9	**696**	**13.9**	696	13.9
403.gcc	662	12.2	**661**	**12.2**	659	12.2	**537**	**15.0**	537	15.0	536	15.0
429.mcf	415	22.0	412	22.1	**413**	**22.1**	**413**	**22.1**	414	22.0	412	22.1
445.gobmk	648	16.2	**648**	**16.2**	648	16.2	598	17.5	597	17.6	**597**	**17.6**
456.hmmer	705	13.2	704	13.2	**705**	**13.2**	447	20.9	448	20.8	**447**	**20.9**
458.sjeng	**788**	**15.4**	786	15.4	790	15.3	**747**	**16.2**	748	16.2	747	16.2
462.libquantum	73.3	283	**72.8**	**285**	72.7	285	73.3	283	**72.8**	**285**	72.7	285
464.h264ref	1111	19.9	1100	20.1	**1102**	**20.1**	789	28.0	786	28.1	**787**	**28.1**
471.omnetpp	582	10.7	**582**	**10.7**	579	10.8	529	11.8	528	11.8	**529**	**11.8**
473.astar	589	11.9	590	11.9	**589**	**11.9**	**510**	**13.8**	510	13.8	503	14.0
483.xalancbmk	**350**	**19.7**	350	19.7	350	19.7	**350**	**19.7**	350	19.7	350	19.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
```

Platform Notes

```
BIOS Settings:
Adjacent Cache Line Prefetch = Enabled
Hardware Prefetcher = Enabled
```

General Notes

```
The NEC Express5800/R140a-4(Intel Xeon E7450) and
the Bull NovaScale R480 E1(Intel Xeon E7450, 2.40 GHz) models are electronically equivalent.
The results have been measured on a Bull NovaScale R480 E1(Intel Xeon E7450, 2.40 GHz) model.
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
           -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
           -I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

```
456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
           -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
           -I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
```

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CPU2006 license: 9006		**Test date:**	Dec-2008
Test sponsor: NEC Corporation		**Hardware Availability:**	Nov-2008
Tested by: Bull SAS		**Software Availability:**	Nov-2008

Peak Portability Flags (Continued)

```
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -ansi-alias -opt-prefetch

   401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -auto-ilp32 -opt-prefetch
               -ansi-alias

     403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
               -opt-malloc-options=3

     429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

   445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
               -no-prec-div -ansi-alias

   456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias -auto-ilp32

   458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

  464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -static -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -ansi-alias -opt-ra-region-strategy=block
               -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

    473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
               -no-prec-div -ansi-alias -opt-ra-region-strategy=routine
               -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes
```

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CPU2006 license: 9006		**Test date:**	Dec-2008
Test sponsor:	NEC Corporation	**Hardware Availability:**	Nov-2008
Tested by:	Bull SAS	**Software Availability:**	Nov-2008

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.01.html
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.01.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.01.xml
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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