



# SPEC® CFP2006 Result

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## NEC Corporation

Express5800/R140a-4  
(Intel Xeon E7450)

SPECfp®2006 = 21.7

SPECfp\_base2006 = 20.5

CPU2006 license: 9006

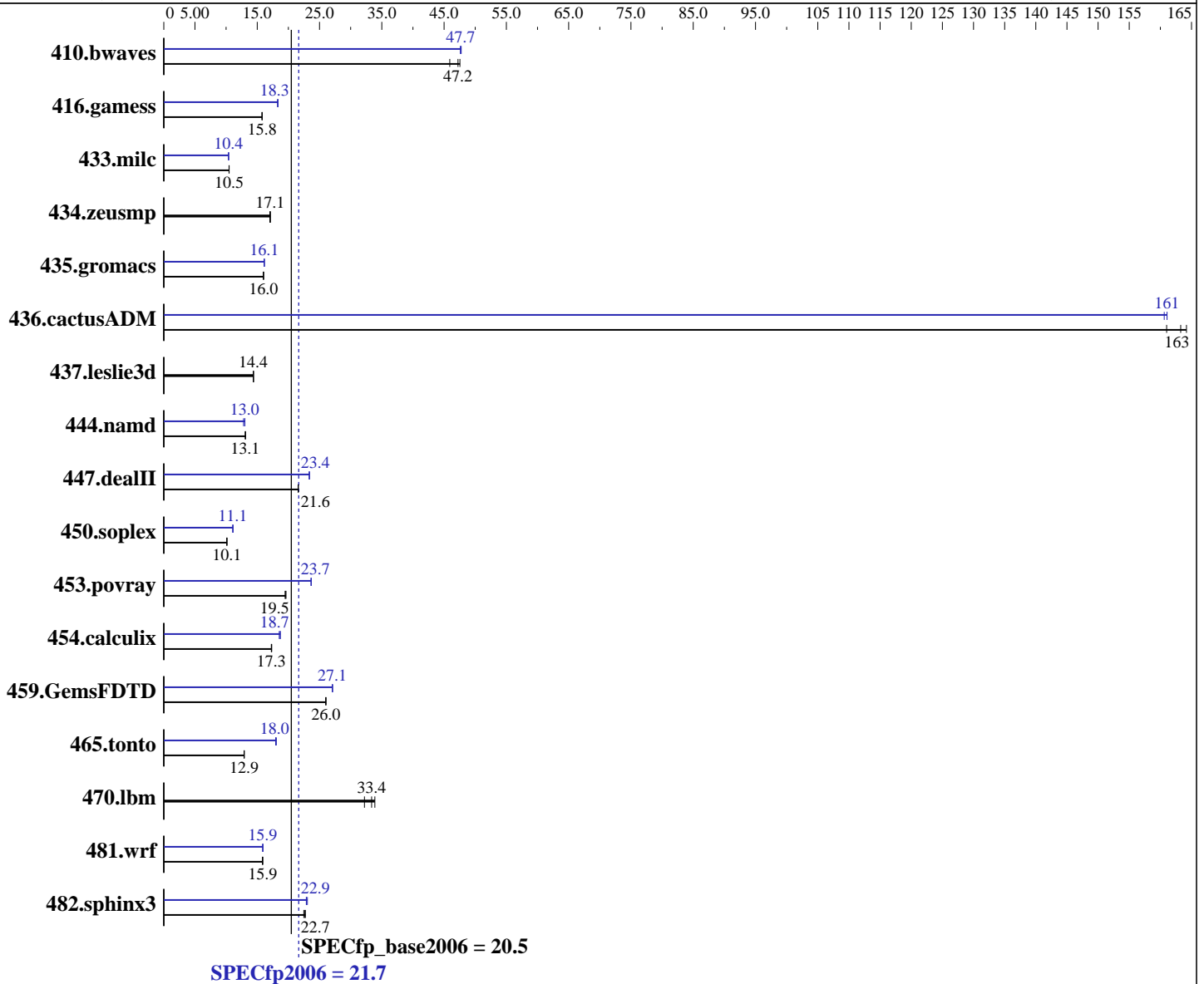
Test sponsor: NEC Corporation

Tested by: Bull SAS

Test date: Dec-2008

Hardware Availability: Nov-2008

Software Availability: Nov-2008



SPECfp2006 = 21.7

### Hardware

CPU Name: Intel Xeon E7450  
 CPU Characteristics: 1066 MHz system bus  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip  
 CPU(s) orderable: 1,2,3,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 9 MB I+D on chip per chip, 3 MB shared / 2 cores

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### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042, l\_fproc\_b\_11.0.042  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 64 GB (16 x 4GB DDR2-667 FBDIMM)  
Disk Subsystem: 1x146 GB SAS, 10000 RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	296	45.9	<b><u>288</u></b>	<b><u>47.2</u></b>	286	47.5	285	47.8	285	47.6	<b><u>285</u></b>	<b><u>47.7</u></b>
416.gamess	1243	15.7	1239	15.8	<b><u>1239</u></b>	<b><u>15.8</u></b>	1072	18.3	1068	18.3	<b><u>1069</u></b>	<b><u>18.3</u></b>
433.milc	877	10.5	875	10.5	<b><u>876</u></b>	<b><u>10.5</u></b>	883	10.4	<b><u>883</u></b>	<b><u>10.4</u></b>	882	10.4
434.zeusmp	534	17.0	<b><u>533</u></b>	<b><u>17.1</u></b>	532	17.1	534	17.0	<b><u>533</u></b>	<b><u>17.1</u></b>	532	17.1
435.gromacs	447	16.0	<b><u>446</u></b>	<b><u>16.0</u></b>	446	16.0	442	16.1	<b><u>443</u></b>	<b><u>16.1</u></b>	443	16.1
436.cactusADM	74.2	161	72.8	164	<b><u>73.2</u></b>	<b><u>163</u></b>	74.4	161	74.2	161	<b><u>74.2</u></b>	<b><u>161</u></b>
437.leslie3d	<b><u>654</u></b>	<b><u>14.4</u></b>	654	14.4	653	14.4	<b><u>654</u></b>	<b><u>14.4</u></b>	654	14.4	653	14.4
444.namd	<b><u>613</u></b>	<b><u>13.1</u></b>	611	13.1	613	13.1	616	13.0	627	12.8	<b><u>618</u></b>	<b><u>13.0</u></b>
447.dealII	<b><u>530</u></b>	<b><u>21.6</u></b>	530	21.6	530	21.6	490	23.4	489	23.4	<b><u>490</u></b>	<b><u>23.4</u></b>
450.soplex	<b><u>824</u></b>	<b><u>10.1</u></b>	823	10.1	825	10.1	750	11.1	753	11.1	<b><u>753</u></b>	<b><u>11.1</u></b>
453.povray	273	19.5	271	19.6	<b><u>273</u></b>	<b><u>19.5</u></b>	<b><u>225</u></b>	<b><u>23.7</u></b>	225	23.7	225	23.7
454.calculix	476	17.3	<b><u>477</u></b>	<b><u>17.3</u></b>	478	17.3	445	18.5	441	18.7	<b><u>441</u></b>	<b><u>18.7</u></b>
459.GemsFDTD	408	26.0	408	26.0	<b><u>408</u></b>	<b><u>26.0</u></b>	392	27.0	<b><u>392</u></b>	<b><u>27.1</u></b>	391	27.1
465.tonto	761	12.9	764	12.9	<b><u>764</u></b>	<b><u>12.9</u></b>	545	18.1	<b><u>547</u></b>	<b><u>18.0</u></b>	548	18.0
470.lbm	426	32.2	<b><u>412</u></b>	<b><u>33.4</u></b>	405	33.9	426	32.2	<b><u>412</u></b>	<b><u>33.4</u></b>	405	33.9
481.wrf	706	15.8	<b><u>704</u></b>	<b><u>15.9</u></b>	703	15.9	702	15.9	703	15.9	<b><u>703</u></b>	<b><u>15.9</u></b>
482.sphinx3	867	22.5	857	22.7	<b><u>860</u></b>	<b><u>22.7</u></b>	852	22.9	845	23.1	<b><u>851</u></b>	<b><u>22.9</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to physical,0  
KMP\_STACKSIZE set to 200M

## Platform Notes

BIOS Settings:  
Adjacent Cache Line Prefetch = Enabled  
Hardware Prefetcher = Enabled



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## General Notes

The NEC Express5800/R140a-4(Intel Xeon E7450) and the Bull NovaScale R480 E1(Intel Xeon E7450, 2.40 GHz) models are electronically equivalent. The results have been measured on a Bull NovaScale R480 E1(Intel Xeon E7450, 2.40 GHz) model.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc  
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc  
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib  
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

### C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-  
-opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -Ob0 -ansi-alias  
-scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch  
-parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4 -auto

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -auto-ilp32

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## Peak Optimization Flags (Continued)

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -opt-prefetch -parallel  
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.01.html>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.04.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.01.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.04.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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