SPEC® CFP2006 Result

NEC Corporation

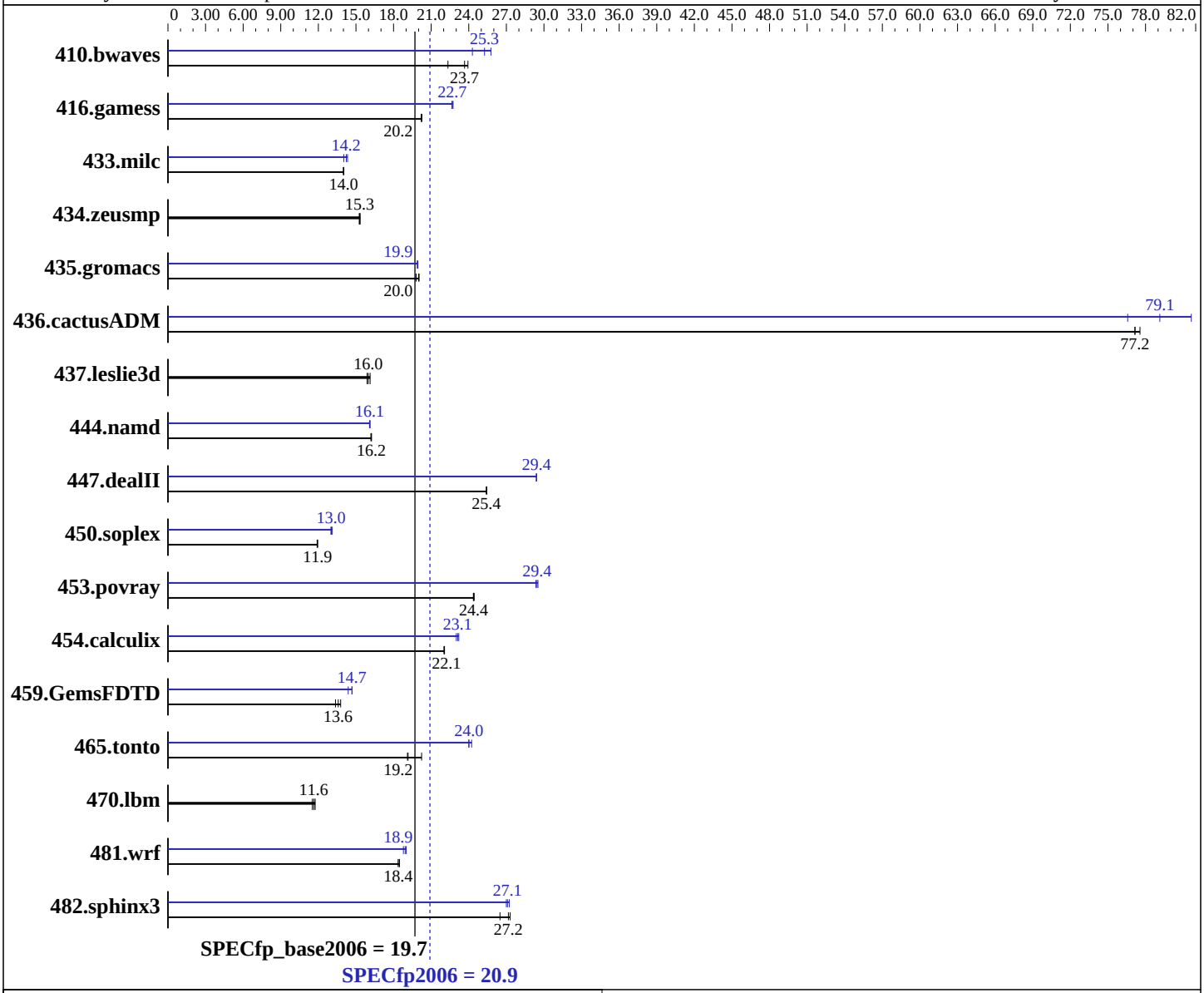
Express5800/320Fd-MR
(Intel Xeon E5450)

SPECfp®2006 = 20.9

SPECfp_base2006 = 19.7

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Dec-2008
Hardware Availability: Oct-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E5450
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips (fault tolerant, see Platform Notes)
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	Red Hat Enterprise Linux Server release 5.2 Advanced Platform, Kernel 2.6.18-92.1.13.el5 on an x86_64
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20081105 Package ID: l_cproc_p_11.0.074, l_fproc_p_11.0.074
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache: None
Other Cache: None
Memory: 24 GB (6x4 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 2x146.5 GB SAS, 15000 RPM, Software RAID Level1
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: ft Server Control Software 6.0.2-198

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	608	22.3	**574**	**23.7**	568	23.9	559	24.3	**538**	**25.3**	527	25.8
416.gamess	**967**	**20.2**	967	20.2	969	20.2	864	22.7	861	22.7	**863**	**22.7**
433.milc	656	14.0	654	14.0	**654**	**14.0**	654	14.0	**645**	**14.2**	641	14.3
434.zeusmp	596	15.3	593	15.3	**595**	**15.3**	596	15.3	593	15.3	**595**	**15.3**
435.gromacs	**357**	**20.0**	356	20.0	361	19.8	358	19.9	359	19.9	**359**	**19.9**
436.cactusADM	155	77.2	**155**	**77.2**	154	77.6	156	76.6	**151**	**79.1**	146	81.6
437.leslie3d	592	15.9	**588**	**16.0**	583	16.1	592	15.9	**588**	**16.0**	583	16.1
444.namd	**494**	**16.2**	495	16.2	494	16.2	497	16.1	**498**	**16.1**	499	16.1
447.dealII	450	25.4	**450**	**25.4**	450	25.4	389	29.4	389	29.4	**389**	**29.4**
450.soplex	701	11.9	**699**	**11.9**	699	11.9	**640**	**13.0**	641	13.0	636	13.1
453.povray	**218**	**24.4**	218	24.4	218	24.4	**181**	**29.4**	181	29.4	180	29.5
454.calculix	**374**	**22.1**	375	22.0	374	22.1	359	23.0	**357**	**23.1**	355	23.2
459.GemsFDTD	794	13.4	**781**	**13.6**	770	13.8	738	14.4	**723**	**14.7**	722	14.7
465.tonto	515	19.1	**514**	**19.2**	486	20.2	406	24.2	410	24.0	**410**	**24.0**
470.lbm	1193	11.5	**1180**	**11.6**	1170	11.7	1193	11.5	**1180**	**11.6**	1170	11.7
481.wrf	608	18.4	605	18.5	**606**	**18.4**	595	18.8	**590**	**18.9**	588	19.0
482.sphinx3	714	27.3	736	26.5	**718**	**27.2**	722	27.0	716	27.2	**719**	**27.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 200M
```

Platform Notes

```
This Express5800/320Fd-MR is a fault-tolerant server.
Two modules are installed in this server.
Each module physically has "2CPU chips,24GB memory", The total physical configuration
is "4CPU chips,48GB memory".
Using fault-tolerant lockstep technology, these two modules communicate with each other
and execute the same instructions at the same time, The operating system only sees
"2CPU chips,24GB memory" as the other components add only redundancy and do not
```

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Platform Notes (Continued)

contribute to any performance benefit.

General Notes

The NEC Express5800/320Fd-MR(Intel Xeon E5450) and
the Bull NovaScale R630 E1 MR(Intel Xeon E5450, 3.00 GHz) models are electronically equivalent.
The results have been measured on a NEC Express5800/320Fd-MR(Intel Xeon E5450) model.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -parallel -opt-prefetch`

C++ benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Fortran benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.1 -ipo -O3 -no-prec-div -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

482.sphinx3: `/opt/intel/Compiler/11.0/074/bin/ia32/icc -L/opt/intel/Compiler/11.0/074/ipp/ia32/lib -I/opt/intel/Compiler/11.0/074/ipp/ia32/include`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/Compiler/11.0/074/bin/ia32/icpc -L/opt/intel/Compiler/11.0/074/ipp/ia32/lib -I/opt/intel/Compiler/11.0/074/ipp/ia32/include`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch -parallel

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Peak Optimization Flags (Continued)

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32`

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.html
http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.xml
http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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