



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 29.7

IBM System x3450 (Intel Xeon X5482)

SPECint_base2006 = 26.1

CPU2006 license: 11

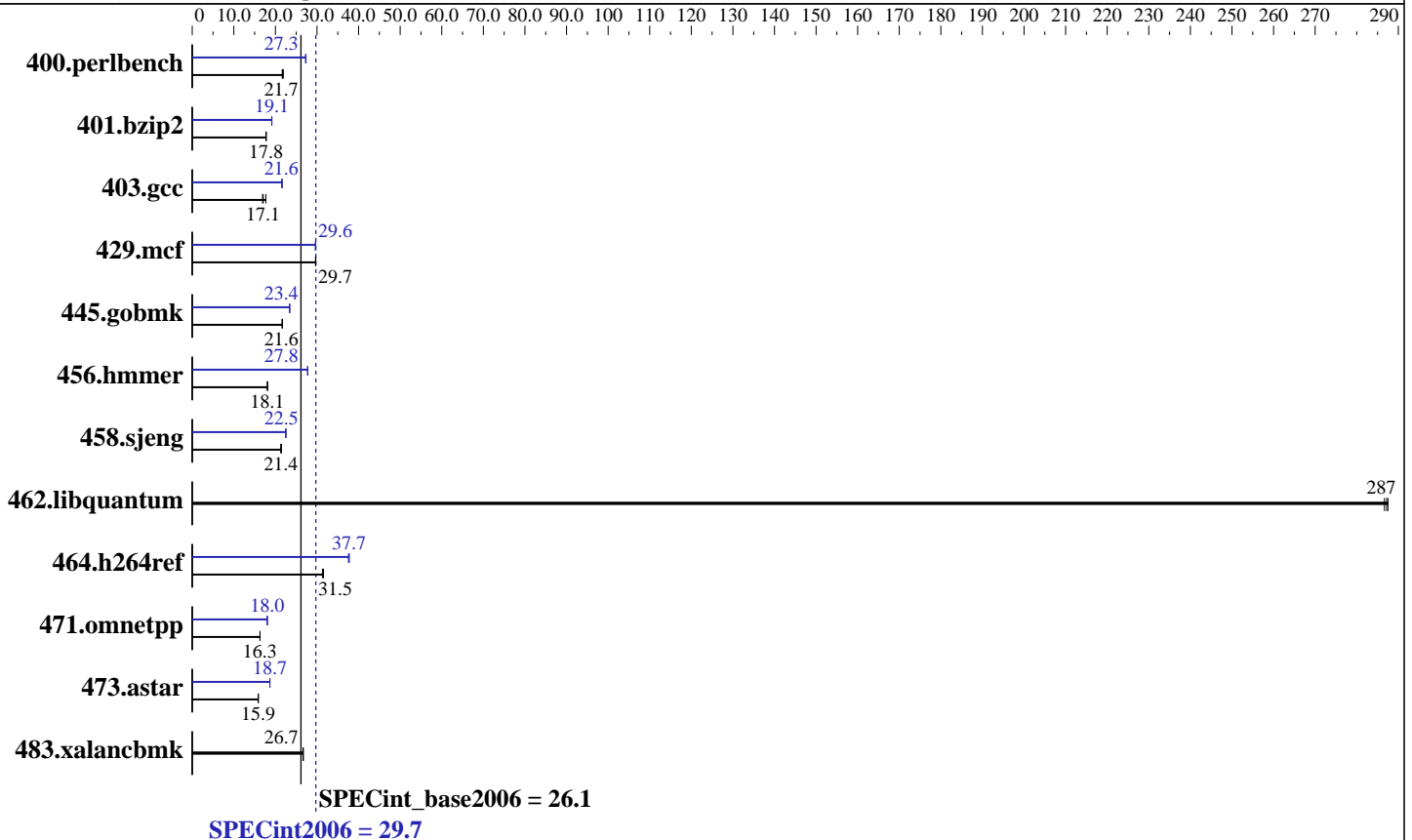
Test date: Dec-2008

Test sponsor: IBM Corporation

Hardware Availability: Nov-2008

Tested by: IBM Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5482
 CPU Characteristics: 1600MHz system bus
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 32 GB (8 x 4 GB DDR2-6400F ECC)
 Disk Subsystem: 1 x 80 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	451	21.7	446	21.9	451	21.7	358	27.3	357	27.4	358	27.3
401.bzip2	544	17.7	543	17.8	543	17.8	504	19.2	504	19.1	505	19.1
403.gcc	476	16.9	471	17.1	454	17.7	373	21.6	373	21.6	373	21.6
429.mcf	308	29.6	307	29.7	307	29.7	307	29.7	308	29.6	308	29.6
445.gobmk	485	21.6	485	21.6	484	21.7	448	23.4	448	23.4	448	23.4
456.hammer	516	18.1	516	18.1	516	18.1	336	27.8	336	27.8	336	27.8
458.sjeng	566	21.4	564	21.4	568	21.3	538	22.5	539	22.5	537	22.5
462.libquantum	72.3	287	72.1	288	72.2	287	72.3	287	72.1	288	72.2	287
464.h264ref	707	31.3	703	31.5	701	31.6	586	37.7	589	37.6	587	37.7
471.omnetpp	383	16.3	384	16.3	383	16.3	346	18.1	347	18.0	347	18.0
473.astar	443	15.9	442	15.9	442	15.9	376	18.7	376	18.7	376	18.7
483.xalanbmk	259	26.7	258	26.7	259	26.6	259	26.7	258	26.7	259	26.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

OMP_NUM_THREADS set to number of processors

KMP_AFFINITY set to "physical,0"

Hardware Prefetch Enabled, Adjacent Sector Prefetch Enabled

Powersave daemon was disabled in OS

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
 -xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
 -par-runtime-control -opt-prefetch

C++ benchmarks:
 -xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
 -L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
 403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
 icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
 -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
 -I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
 -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
 -I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:
 icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmcr: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.xml>

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