SPEC® CFP2006 Result

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YOYOtech
(Test Sponsor: Future Publishing Ltd.)

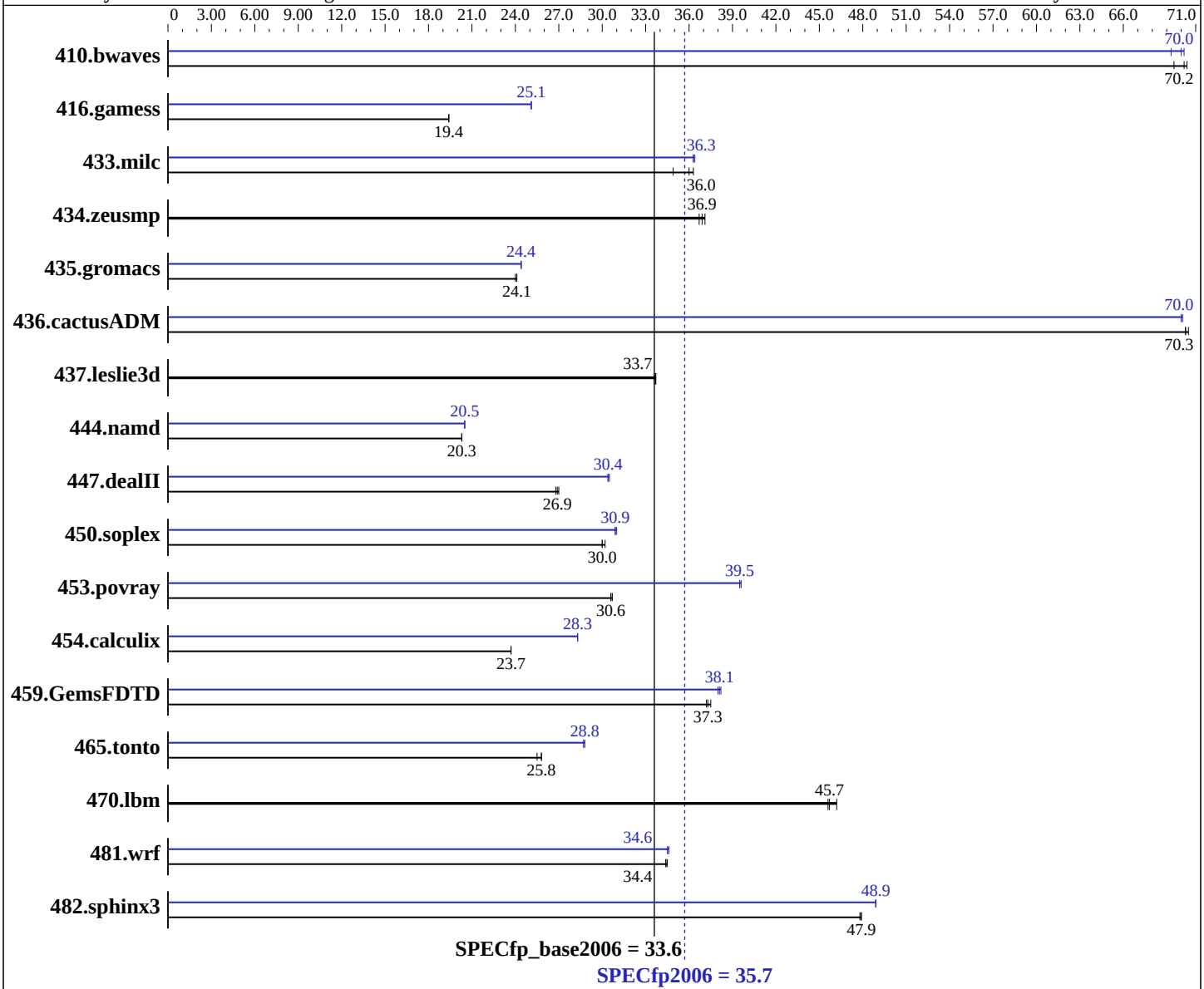
Fi7EPOWER MLK1610 (Intel Core i7-965)

SPECfp®2006 = 35.7

SPECfp_base2006 = 33.6

CPU2006 license: 3772
Test sponsor: Future Publishing Ltd.
Tested by: Future Publishing Ltd.

Test date: Oct-2008
Hardware Availability: Nov-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core i7-965 Extreme Edition
CPU Characteristics:	Intel Turbo Boost Technology disabled, clocked at 3.73 GHz
CPU MHz:	3733
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Intel Visual Fortran Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cprof_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 9 GB (3x 2GB and 3x 1GB Corsair DDR3-1066, 9-9-9-24)
Disk Subsystem: 80 GB SATA, SSD
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.1 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**194**	**70.2**	193	70.4	196	69.5	194	70.2	**194**	**70.0**	196	69.3
416.gamess	1010	19.4	**1009**	**19.4**	1007	19.4	**781**	**25.1**	781	25.1	781	25.1
433.milc	**255**	**36.0**	263	34.9	253	36.3	**253**	**36.3**	252	36.4	253	36.3
434.zeusmp	**246**	**36.9**	248	36.7	245	37.1	**246**	**36.9**	248	36.7	245	37.1
435.gromacs	297	24.0	**296**	**24.1**	296	24.1	293	24.4	**293**	**24.4**	293	24.4
436.cactusADM	**170**	**70.3**	170	70.5	170	70.3	**171**	**70.0**	171	70.0	170	70.1
437.leslie3d	280	33.6	279	33.7	**279**	**33.7**	280	33.6	279	33.7	**279**	**33.7**
444.namd	394	20.3	**395**	**20.3**	395	20.3	392	20.5	392	20.5	**392**	**20.5**
447.dealII	**425**	**26.9**	427	26.8	424	27.0	**376**	**30.4**	376	30.4	376	30.5
450.soplex	**278**	**30.0**	278	30.0	276	30.2	270	30.9	269	31.0	**270**	**30.9**
453.povray	174	30.7	**174**	**30.6**	174	30.6	135	39.5	135	39.6	**135**	**39.5**
454.calculix	348	23.7	**348**	**23.7**	348	23.7	292	28.3	292	28.3	**292**	**28.3**
459.GemsFDTD	**284**	**37.3**	283	37.5	285	37.2	279	38.0	**279**	**38.1**	278	38.2
465.tonto	385	25.5	**381**	**25.8**	381	25.8	343	28.7	**342**	**28.8**	342	28.8
470.lbm	**301**	**45.7**	297	46.2	301	45.6	**301**	**45.7**	297	46.2	301	45.6
481.wrf	**324**	**34.4**	325	34.4	324	34.5	**323**	**34.6**	324	34.5	323	34.6
482.sphinx3	407	47.8	407	47.9	**407**	**47.9**	399	48.9	**399**	**48.9**	399	48.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
System was configured with HIS Radeon HD 4870 X2 discrete graphics card
Binaries were built on Windows Vista Ultimate (32-bit)
OMP_NUM_THREADS set to number of logical processors as seen by the OS
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
/F1000000000

C++ benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
-Qcxx-features /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
/F1000000000

Benchmarks using both Fortran and C:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qopt-prefetch
/F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000

470.lbm: basepeak = yes

482.sphinx3: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000

C++ benchmarks:

444.namd: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch
-Qansi-alias -Qscalar-rep- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

450.soplex: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

453.povray: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F1000000000
shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
/F1000000000

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Peak Optimization Flags (Continued)

416.gamess: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch -Qparallel /F1000000000

465.tonto: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

436.cactusADM: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel /F1000000000

454.calculix: -QxSSE4.2 -Qipo -O3 -Qprec-div- /F1000000000

481.wrf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel /F1000000000

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.html
http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090713.xml
http://www.spec.org/cpu2006/flags/Intel-Win32-Platform.xml

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For other inquiries, please contact webmaster@spec.org.

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