



SPEC® CFP2006 Result

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Bull SAS

NovaScale B240
(Intel Xeon X3323, 2.50 GHz)

SPECfp®2006 = 18.6

SPECfp_base2006 = 17.2

CPU2006 license: 20

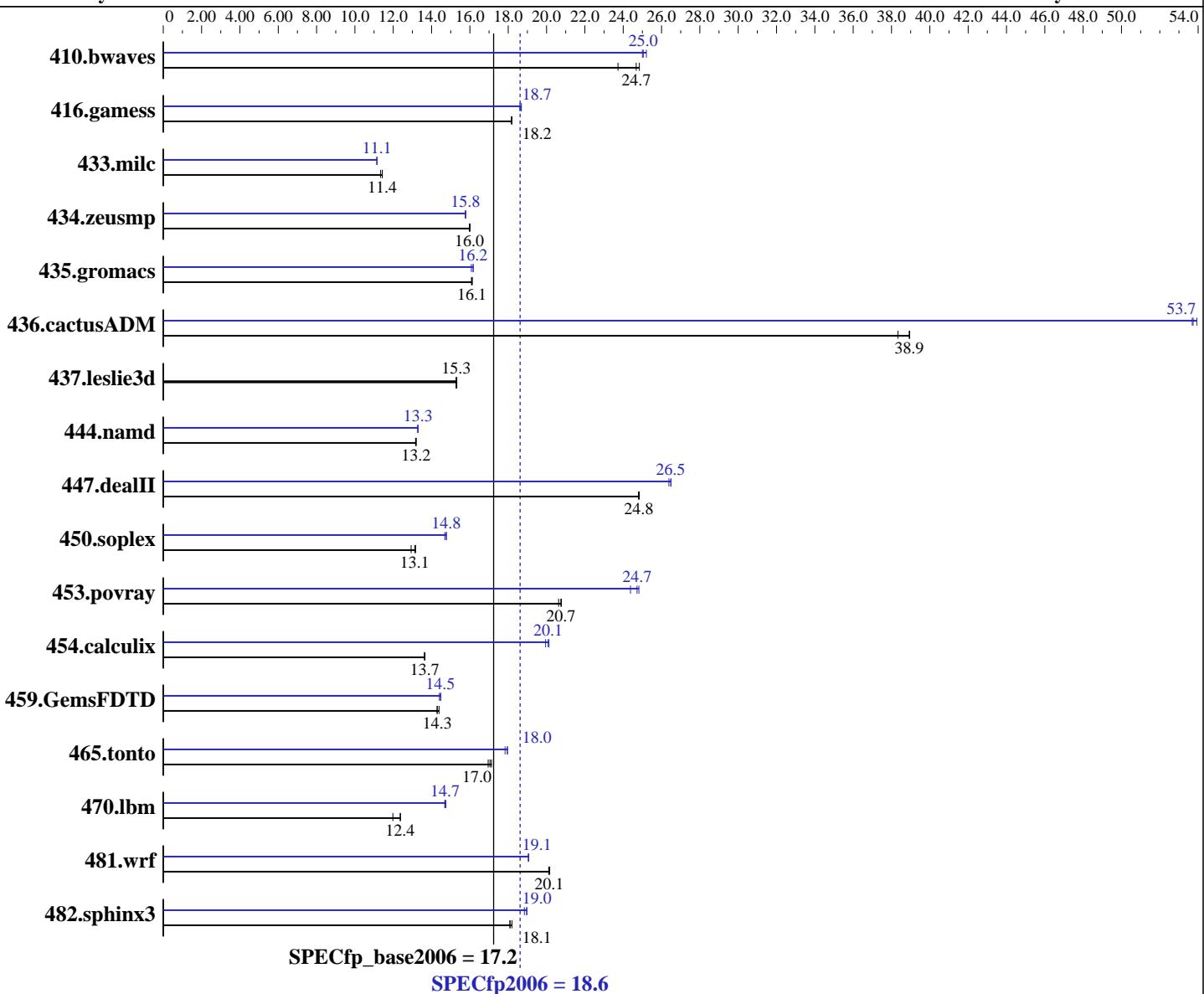
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Nov-2008

Hardware Availability: Jun-2008

Software Availability: Nov-2007



Hardware	
CPU Name:	Intel Xeon X3323
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip, 3 MB shared / 2 cores

Software	
Operating System:	SUSE LINUX Enterprise Server 10 (x86_64) SP1
Compiler:	Kernel 2.6.16.46-0.12-smp
Auto Parallel:	Intel C++ and Fortran Compiler 10.1 for Linux
File System:	Build 20070913 Package ID: l_cc_p_10.1.008,
System State:	l_fc_p_10.1.008
Base Pointers:	Yes
	ReiserFS
	Run level 3 (multi-user)
	64-bit

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x73 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	573	23.7	551	24.7	547	24.8	539	25.2	544	25.0	543	25.0
416.gamess	1078	18.2	1076	18.2	1077	18.2	1048	18.7	1052	18.6	1049	18.7
433.milc	810	11.3	803	11.4	803	11.4	823	11.1	823	11.1	823	11.2
434.zeusmp	570	16.0	570	16.0	568	16.0	576	15.8	577	15.8	577	15.8
435.gromacs	443	16.1	443	16.1	444	16.1	442	16.2	444	16.1	441	16.2
436.cactusADM	312	38.3	307	38.9	307	38.9	222	53.7	223	53.7	222	53.9
437.leslie3d	614	15.3	615	15.3	615	15.3	614	15.3	615	15.3	615	15.3
444.namd	609	13.2	608	13.2	608	13.2	603	13.3	603	13.3	604	13.3
447.dealII	461	24.8	461	24.8	461	24.8	432	26.5	434	26.4	432	26.5
450.soplex	645	12.9	636	13.1	633	13.2	565	14.8	565	14.8	568	14.7
453.povray	256	20.7	256	20.8	258	20.6	215	24.7	214	24.8	218	24.4
454.calculix	604	13.7	604	13.7	606	13.6	414	19.9	410	20.1	411	20.1
459.GemsFDTD	737	14.4	742	14.3	743	14.3	736	14.4	733	14.5	733	14.5
465.tonto	574	17.1	580	17.0	577	17.0	552	17.8	548	18.0	548	18.0
470.lbm	1147	12.0	1112	12.4	1110	12.4	935	14.7	935	14.7	932	14.7
481.wrf	555	20.1	555	20.1	554	20.2	586	19.1	586	19.1	586	19.1
482.sphinx3	1071	18.2	1077	18.1	1077	18.1	1028	19.0	1027	19.0	1034	18.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 200M

Platform Notes

BIOS Settings:
Hardware Prefetcher = Enabled
Adjacent Cache Line Prefetch = Enabled



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast -parallel

C++ benchmarks:
-fast -parallel

Fortran benchmarks:
-fast -parallel

Benchmarks using both Fortran and C:
-fast -parallel



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Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

433.milc: icc

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
        454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
        481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
    -auto-ilp32
```

```
470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
    -scalar-rep -prefetch -opt-malloc-options=3
```

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Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll12

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12 -O0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12 -O0
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.xml



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