



SPEC[®] CFP2006 Result

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Bull SAS

NovaScale B280
(Intel Xeon E5440, 2.83 GHz)

SPECfp[®]2006 = 22.3

SPECfp_base2006 = 18.8

CPU2006 license: 20

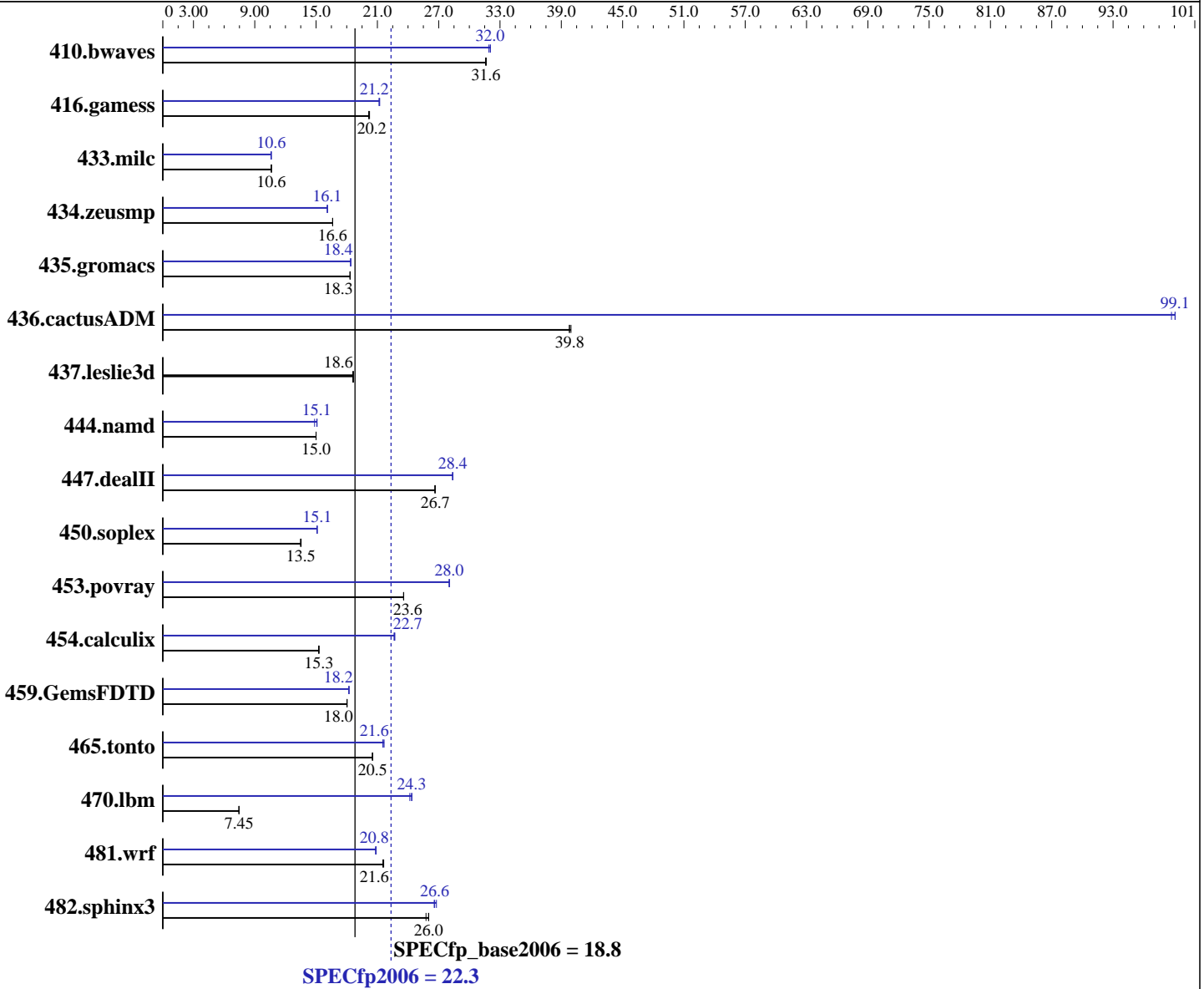
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Sep-2008

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5440
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2833
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SUSE LINUX Enterprise Server 10 (x86_64) SP1
 Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux
 Build 20070913 Package ID: l_cc_p_10.1.008,
 l_fc_p_10.1.008
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x73 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	429	31.6	430	31.6	430	31.6	424	32.0	426	31.9	424	32.1
416.gamess	971	20.2	971	20.2	968	20.2	922	21.2	925	21.2	925	21.2
433.milc	864	10.6	864	10.6	864	10.6	865	10.6	865	10.6	866	10.6
434.zeusmp	548	16.6	547	16.6	547	16.6	565	16.1	565	16.1	565	16.1
435.gromacs	390	18.3	390	18.3	389	18.3	388	18.4	388	18.4	388	18.4
436.cactusADM	300	39.8	299	39.9	300	39.8	121	98.7	121	99.1	121	99.1
437.leslie3d	504	18.6	504	18.7	506	18.6	504	18.6	504	18.7	506	18.6
444.namd	535	15.0	535	15.0	535	15.0	540	14.9	531	15.1	532	15.1
447.dealII	429	26.7	429	26.7	430	26.6	403	28.4	403	28.4	404	28.4
450.soplex	616	13.5	617	13.5	619	13.5	552	15.1	552	15.1	552	15.1
453.povray	226	23.6	226	23.6	226	23.6	190	28.1	190	28.0	190	28.0
454.calculix	541	15.2	540	15.3	540	15.3	364	22.7	363	22.7	365	22.6
459.GemsFDTD	588	18.0	588	18.0	589	18.0	582	18.2	582	18.2	583	18.2
465.tonto	479	20.5	480	20.5	479	20.5	454	21.7	457	21.5	456	21.6
470.lbm	1842	7.46	1844	7.45	1844	7.45	565	24.3	564	24.4	568	24.2
481.wrf	517	21.6	518	21.6	519	21.5	536	20.8	536	20.8	535	20.9
482.sphinx3	749	26.0	749	26.0	757	25.8	728	26.8	734	26.6	732	26.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 200M

Platform Notes

BIOS configuration:
Hardware Prefetcher Enabled
Adjacent Cache-Line Prefetch Enabled



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General Notes

All benchmarks compiled in 64-bit mode except 450.soplex, 470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast -parallel

C++ benchmarks:
-fast -parallel

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Base Optimization Flags (Continued)

Fortran benchmarks:
-fast -parallel

Benchmarks using both Fortran and C:
-fast -parallel

Peak Compiler Invocation

C benchmarks (except as noted below):
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

433.milc: icc

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090713.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090713.xml

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For other inquiries, please contact webmaster@spec.org.

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