



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp®_rate2006 = 83.4

ProLiant BL480c
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate_base2006 = 76.4

CPU2006 license: 3

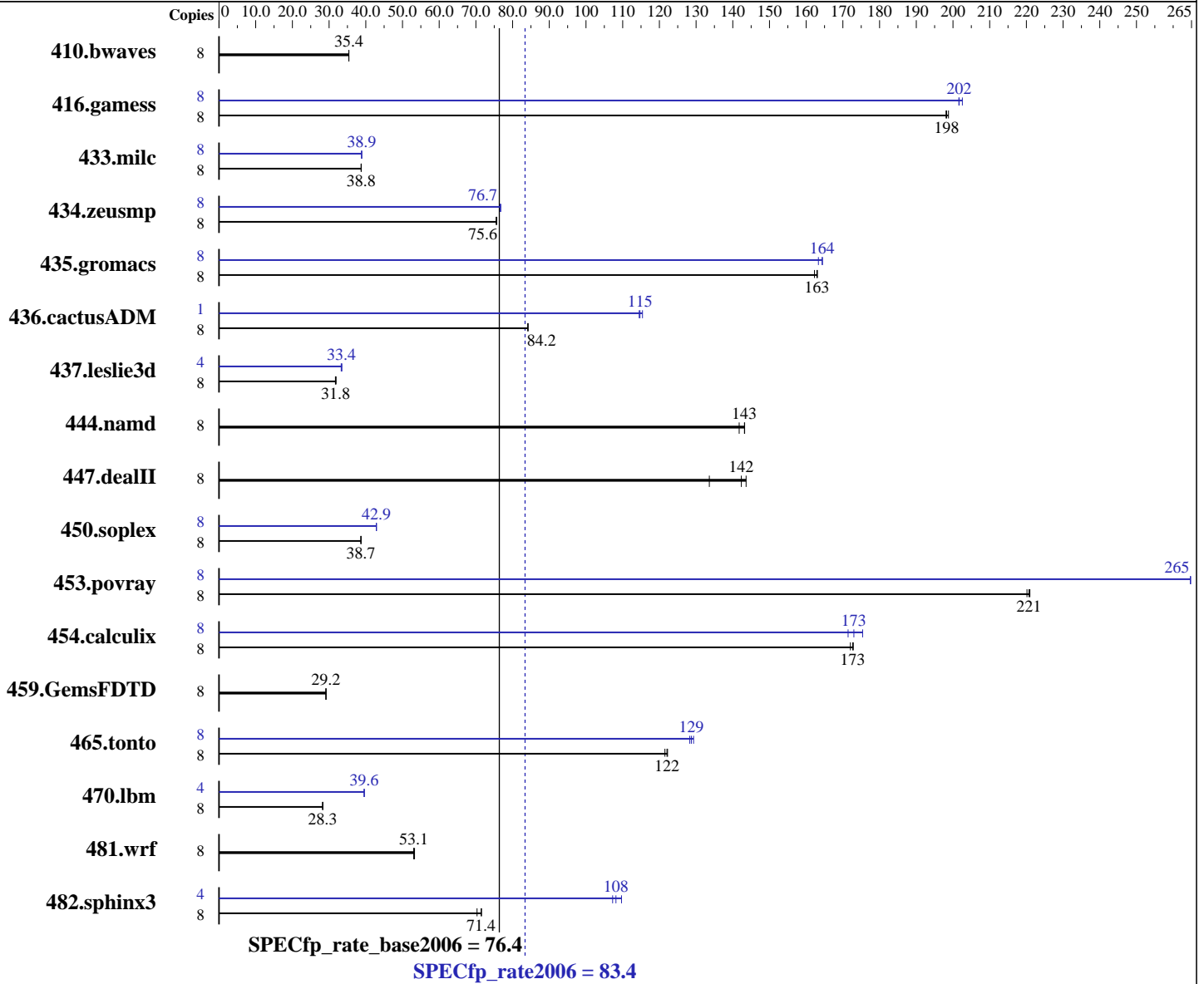
Test date: Aug-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5470
 CPU Characteristics: 3.33 GHz, 2x6 MB L2 shared, 1333 MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 83.4

ProLiant BL480c
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate_base2006 = 76.4

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Aug-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 1x146 GB 10 K SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3073	35.4	3072	35.4	3073	35.4	8	3073	35.4	3072	35.4	3073	35.4
416.gamess	8	791	198	788	199	790	198	8	777	202	777	202	773	203
433.milc	8	1895	38.8	1895	38.8	1896	38.7	8	1890	38.9	1890	38.9	1889	38.9
434.zeusmp	8	963	75.6	964	75.5	962	75.7	8	948	76.8	949	76.7	950	76.6
435.gromacs	8	352	162	350	163	351	163	8	347	164	348	164	350	163
436.cactusADM	8	1135	84.2	1136	84.2	1136	84.2	1	104	115	104	115	104	115
437.leslie3d	8	2357	31.9	2371	31.7	2368	31.8	4	1123	33.5	1129	33.3	1126	33.4
444.namd	8	453	142	448	143	448	143	8	453	142	448	143	448	143
447.dealII	8	637	144	685	134	643	142	8	637	144	685	134	643	142
450.soplex	8	1724	38.7	1725	38.7	1725	38.7	8	1556	42.9	1555	42.9	1556	42.9
453.povray	8	193	221	193	220	193	221	8	161	265	161	265	161	265
454.calculix	8	382	173	382	173	384	172	8	376	175	385	171	382	173
459.GemsFDTD	8	2910	29.2	2912	29.2	2914	29.1	8	2910	29.2	2912	29.2	2914	29.1
465.tonto	8	648	122	644	122	645	122	8	614	128	609	129	612	129
470.lbm	8	3892	28.2	3889	28.3	3889	28.3	4	1390	39.6	1388	39.6	1389	39.6
481.wrf	8	1684	53.1	1677	53.3	1684	53.1	8	1684	53.1	1677	53.3	1684	53.1
482.sphinx3	8	2183	71.4	2179	71.5	2217	70.3	4	721	108	727	107	711	110

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 436.cactusADM peak

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 83.4

ProLiant BL480c
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate_base2006 = 76.4

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Aug-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch Disabled
Hardware Prefetcher Disabled

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 83.4

ProLiant BL480c
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate_base2006 = 76.4

CPU2006 license: 3

Test date: Aug-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2008

Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: /opt/intel/Compiler/11.0/042/bin/ia32/ifort
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 83.4

ProLiant BL480c
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate_base2006 = 76.4

CPU2006 license: 3

Test date: Aug-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2008

Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECfp_rate2006 = 83.4

ProLiant BL480c
(3.33 GHz, Intel Xeon X5470)

SPECfp_rate_base2006 = 76.4

CPU2006 license: 3

Test date: Aug-2008

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2008

Tested by: Hewlett-Packard Company

Software Availability: Nov-2008

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.00.html>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090713.00.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 19:44:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 16 September 2008.