



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

SPECfp®2006 = 18.1

PRIMERGY RX200 S4, Intel Xeon E5405, 2.0 GHz

SPECfp_base2006 = 15.4

CPU2006 license: 22

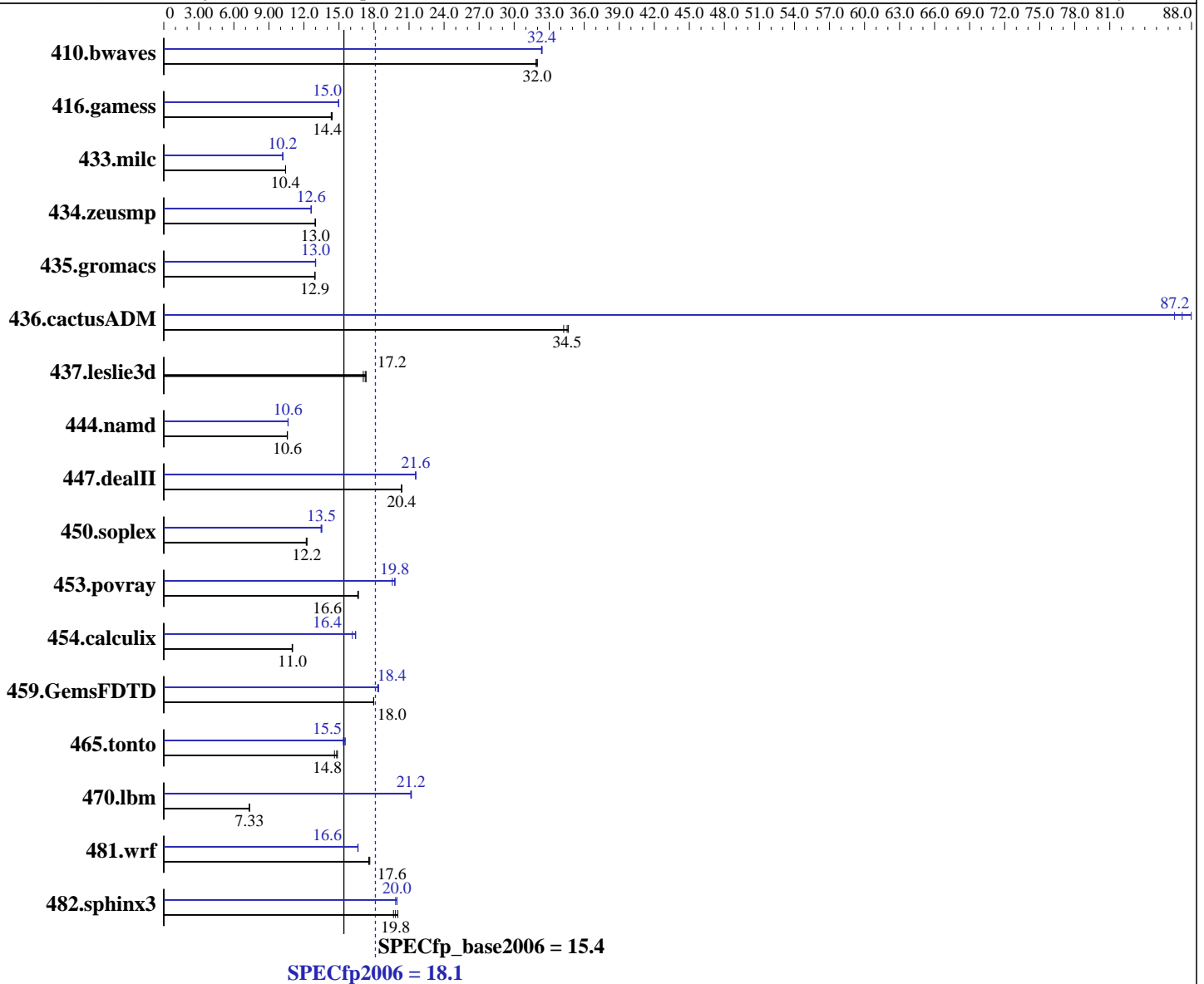
Test date: Aug-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Dec-2007

Tested by: Fujitsu Siemens Computers

Software Availability: May-2008



Hardware

CPU Name: Intel Xeon E5405
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) with SP2, kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64, version 10.1, Build 20070913
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)
Disk Subsystem: 1x SAS, 73 GB, 15000 rpm
Other Hardware: None

Other Software: binutils-2.17.50.0.5-0.1.x86_64

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	425	32.0	426	31.9	425	32.0	420	32.4	420	32.3	419	32.4
416.gamess	1366	14.3	1358	14.4	1363	14.4	1308	15.0	1308	15.0	1307	15.0
433.milc	881	10.4	880	10.4	880	10.4	901	10.2	901	10.2	901	10.2
434.zeusmp	701	13.0	701	13.0	701	13.0	721	12.6	721	12.6	721	12.6
435.gromacs	552	12.9	552	12.9	552	12.9	549	13.0	549	13.0	550	13.0
436.cactusADM	345	34.6	349	34.2	346	34.5	136	88.0	138	86.5	137	87.2
437.leslie3d	545	17.2	543	17.3	550	17.1	545	17.2	543	17.3	550	17.1
444.namd	758	10.6	757	10.6	759	10.6	754	10.6	754	10.6	753	10.6
447.dealII	562	20.4	562	20.4	562	20.4	531	21.6	530	21.6	530	21.6
450.soplex	681	12.2	681	12.3	684	12.2	617	13.5	619	13.5	617	13.5
453.povray	320	16.6	320	16.6	319	16.7	272	19.6	269	19.8	269	19.8
454.calculix	751	11.0	748	11.0	749	11.0	503	16.4	511	16.1	502	16.4
459.GemsFDTD	591	18.0	590	18.0	590	18.0	578	18.4	579	18.3	577	18.4
465.tonto	662	14.9	665	14.8	673	14.6	633	15.5	636	15.5	640	15.4
470.lbm	1874	7.33	1872	7.34	1879	7.31	648	21.2	649	21.2	650	21.2
481.wrf	633	17.6	635	17.6	636	17.5	672	16.6	672	16.6	671	16.6
482.sphinx3	972	20.0	991	19.7	982	19.8	976	20.0	982	19.8	976	20.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

All binaries were built with 64-bit Intel compiler except:
450.soplex, 470.lbm and 482.sphinx3 in peak were built with 32-bit Intel compiler.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)

Platform Notes

BIOS configuration:
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Enable



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General Notes

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast -parallel

C++ benchmarks:
-fast -parallel

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Base Optimization Flags (Continued)

Fortran benchmarks:

-fast -parallel

Benchmarks using both Fortran and C:

-fast -parallel

Peak Compiler Invocation

C benchmarks (except as noted below):

/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

433.milc: icc

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 19:14:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 September 2008.