



SPEC[®] CFP2006 Result

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Fujitsu Siemens Computers

SPECfp[®]2006 = **24.2**

PRIMERGY RX300 S4, Intel Xeon X5260, 3.33 GHz

SPECfp_base2006 = **20.7**

CPU2006 license: 22

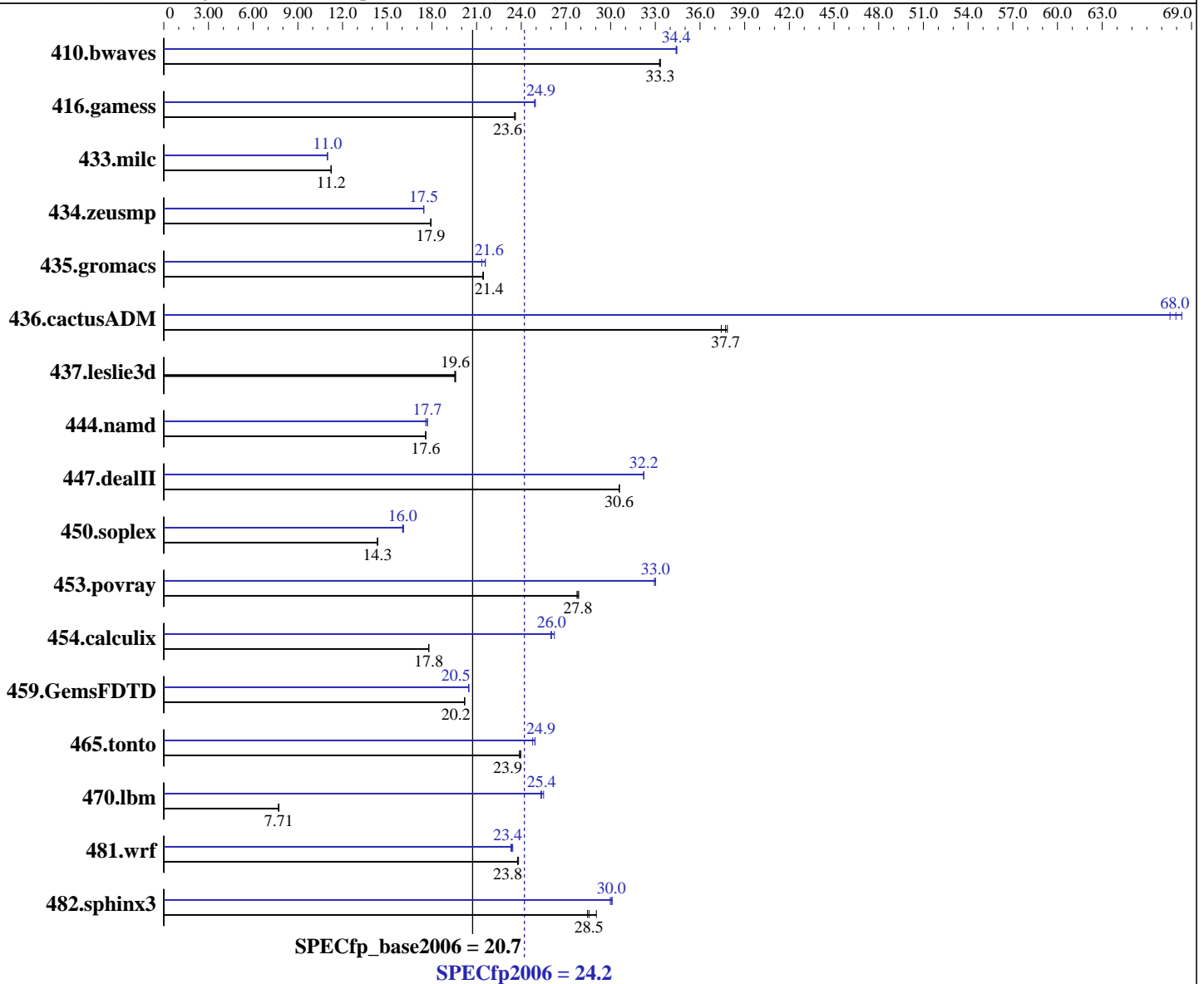
Test date: May-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Dec-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5260
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smpp
 Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64, version 10.1, Build 20070913
 Auto Parallel: Yes
 File System: ext2
 System State: Multi-User Run Level 3
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL 5-5-5, ECC)
Disk Subsystem: 1x SAS, 73 GB, 15000 rpm
Other Hardware: None

Other Software: binutils-2.17.50.0.5-0.1.x86_64

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	408	33.3	408	33.3	408	33.3	395	34.4	394	34.4	395	34.4
416.gamess	831	23.6	829	23.6	831	23.6	786	24.9	786	24.9	785	24.9
433.milc	817	11.2	817	11.2	818	11.2	836	11.0	835	11.0	835	11.0
434.zeusmp	508	17.9	508	17.9	508	17.9	521	17.5	521	17.5	521	17.5
435.gromacs	333	21.4	333	21.4	333	21.5	334	21.3	331	21.6	331	21.6
436.cactusADM	317	37.7	316	37.8	319	37.4	177	67.6	175	68.3	176	68.0
437.leslie3d	480	19.6	480	19.6	481	19.5	480	19.6	480	19.6	481	19.5
444.namd	456	17.6	456	17.6	456	17.6	453	17.7	453	17.7	456	17.6
447.dealII	374	30.6	374	30.6	374	30.6	355	32.2	355	32.2	355	32.2
450.soplex	581	14.4	581	14.3	582	14.3	520	16.0	518	16.1	520	16.0
453.povray	191	27.9	191	27.8	192	27.7	161	33.0	162	32.9	161	33.0
454.calculix	464	17.8	464	17.8	464	17.8	317	26.0	315	26.2	317	26.0
459.GemsFDTD	525	20.2	526	20.2	525	20.2	518	20.5	518	20.5	518	20.5
465.tonto	411	24.0	411	23.9	412	23.9	397	24.8	395	24.9	395	24.9
470.lbm	1777	7.73	1785	7.70	1781	7.71	539	25.5	543	25.3	542	25.4
481.wrf	469	23.8	470	23.7	470	23.8	477	23.4	479	23.3	478	23.4
482.sphinx3	683	28.5	671	29.0	685	28.4	650	30.0	647	30.1	649	30.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores (default)

General Notes

All binaries were built with 64-bit Intel compiler except:
450.soplex, 470.lbm and 482.sphinx3 in peak were built with
32-bit Intel compiler by changing the path
for include and library files.

For information about Fujitsu Siemens Computers please see:
<http://www.fujitsu-siemens.com>



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast -parallel

C++ benchmarks:

-fast -parallel

Fortran benchmarks:

-fast -parallel

Benchmarks using both Fortran and C:

-fast -parallel



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Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

433.milc: icc

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32
```

```
470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3
```

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Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealIII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/flags-ic101-linux-intel64.20090713.xml>



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