SPEC® CFP2006 Result
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NEC Corporation

Express5800/110Ri-1
(Intel Xeon X3350)

$SPECfp^{®}2006 =$ 22.3

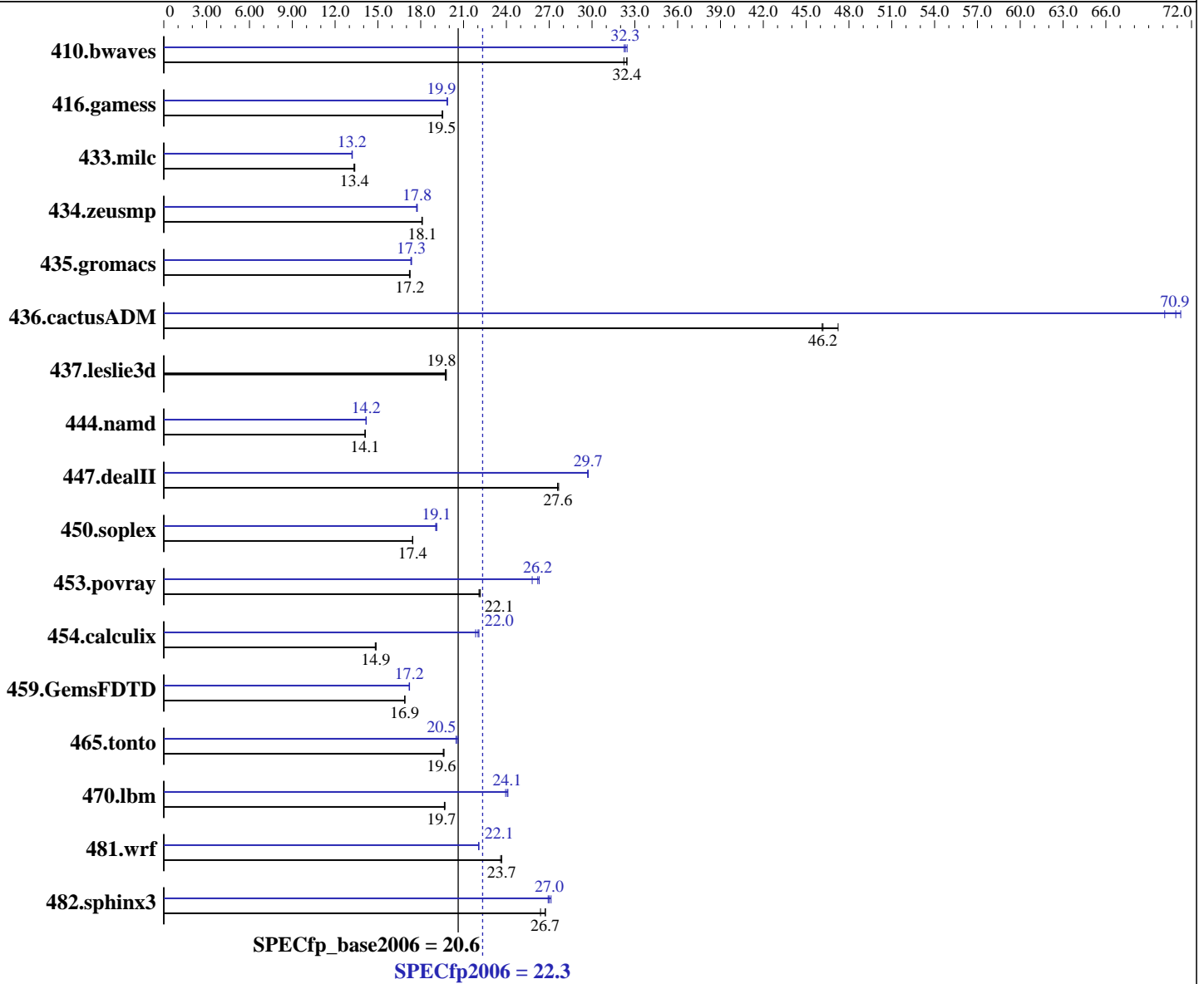$SPECfp\_base2006 =$ 20.6

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Jul-2008
Hardware Availability: Apr-2008
Software Availability: Nov-2007

410.bwaves 32.3 / 32.4
416.gamess 19.9 / 19.5
433.milc 13.2 / 13.4
434.zeusmp 17.8 / 18.1
435.gromacs 17.3 / 17.2
436.cactusADM 70.9 / 46.2
437.leslie3d 19.8
444.namd 14.2 / 14.1
447.dealII 29.7 / 27.6
450.soplex 19.1 / 17.4
453.povray 26.2 / 22.1
454.calculix 22.0 / 14.9
459.GemsFDTD 17.2 / 16.9
465.tonto 20.5 / 19.6
470.lbm 24.1 / 19.7
481.wrf 22.1 / 23.7
482.sphinx3 27.0 / 26.7

SPECfp_base2006 = 20.6
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Hardware

CPU Name: Intel Xeon X3350
CPU Characteristics: 2.66 GHz, 2x6 MB L2 shared, 1333 MHz bus
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ and Fortran Compiler for Linux version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)

Continued on next page

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L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB PC2-6400E, 2 rank, CL6-6-6, ECC)
Disk Subsystem:	1x80.0 GB SATAII, 7200RPM
Other Hardware:	None

Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	binutils 2.17

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	422	32.2	419	32.4	**419**	**32.4**	419	32.5	421	32.3	**420**	**32.3**
416.gamess	1004	19.5	1002	19.5	**1003**	**19.5**	987	19.8	985	19.9	**986**	**19.9**
433.milc	686	13.4	**687**	**13.4**	689	13.3	695	13.2	696	13.2	**696**	**13.2**
434.zeusmp	503	18.1	**503**	**18.1**	503	18.1	514	17.7	513	17.8	**513**	**17.8**
435.gromacs	**414**	**17.2**	414	17.3	415	17.2	412	17.3	**412**	**17.3**	412	17.3
436.cactusADM	253	47.2	259	46.1	**259**	**46.2**	170	70.1	**169**	**70.9**	168	71.2
437.leslie3d	475	19.8	**476**	**19.8**	477	19.7	475	19.8	**476**	**19.8**	477	19.7
444.namd	569	14.1	568	14.1	**569**	**14.1**	**566**	**14.2**	566	14.2	566	14.2
447.dealII	415	27.6	414	27.7	**414**	**27.6**	385	29.7	**385**	**29.7**	385	29.7
450.soplex	**478**	**17.4**	479	17.4	478	17.4	438	19.0	436	19.1	**437**	**19.1**
453.povray	240	22.2	**240**	**22.1**	241	22.1	202	26.3	**203**	**26.2**	206	25.8
454.calculix	557	14.8	555	14.9	**555**	**14.9**	374	22.1	378	21.8	**375**	**22.0**
459.GemsFDTD	628	16.9	629	16.9	**628**	**16.9**	616	17.2	617	17.2	**617**	**17.2**
465.tonto	**502**	**19.6**	501	19.6	502	19.6	477	20.6	**480**	**20.5**	481	20.5
470.lbm	699	19.7	697	19.7	**699**	**19.7**	570	24.1	**571**	**24.1**	574	23.9
481.wrf	473	23.6	472	23.7	**472**	**23.7**	507	22.0	506	22.1	**506**	**22.1**
482.sphinx3	**730**	**26.7**	738	26.4	729	26.7	718	27.1	**722**	**27.0**	724	26.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
```

Platform Notes

```
Bios settings:
Hardware Prefetcher:        Enabled
Adjacent Cache Line Prefetch: Enabled
Intel SpeedStep Technology:   Enabled
```

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General Notes

```
All benchmarks compiled in 64-bit mode except 450.soplex,
470.lbm and 482.sphinx3, for peak, are compiled in 32-bit mode
```

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icc ifort

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
   459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
   482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
    -fast -parallel

C++ benchmarks:
    -fast -parallel

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Base Optimization Flags (Continued)

Fortran benchmarks:
```
-fast -parallel
```

Benchmarks using both Fortran and C:
```
-fast -parallel
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

    433.milc: `icc`

C++ benchmarks (except as noted below):
```
icpc
```

    450.soplex: `/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib`
          `-I/opt/intel/cc/10.1.008/include`

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags

```
       410.bwaves: -DSPEC_CPU_LP64
       416.gamess: -DSPEC_CPU_LP64
         433.milc: -DSPEC_CPU_LP64
       434.zeusmp: -DSPEC_CPU_LP64
      435.gromacs: -DSPEC_CPU_LP64 -nofor_main
     436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
      437.leslie3d: -DSPEC_CPU_LP64
         444.namd: -DSPEC_CPU_LP64
        447.dealII: -DSPEC_CPU_LP64
       453.povray: -DSPEC_CPU_LP64
      454.calculix: -DSPEC_CPU_LP64 -nofor_main
     459.GemsFDTD: -DSPEC_CPU_LP64
         465.tonto: -DSPEC_CPU_LP64
          481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

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Peak Optimization Flags

C benchmarks:

> 433.milc: `-prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32`

> 470.lbm: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3`

> 482.sphinx3: `-fast -unroll2`

C++ benchmarks:

> 444.namd: `-prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32`

> 447.dealII: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias -scalar-rep-`

> 450.soplex: `-prof-gen(pass 1) -prof-use(pass 2) -fast -opt-malloc-options=3`

> 453.povray: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -ansi-alias`

Fortran benchmarks:

> 410.bwaves: `-fast -prefetch -parallel`

> 416.gamess: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -ansi-alias -scalar-rep-`

> 434.zeusmp: `-prof-gen(pass 1) -prof-use(pass 2) -fast`

> 437.leslie3d: `basepeak = yes`

> 459.GemsFDTD: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -prefetch -parallel`

> 465.tonto: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto`

Benchmarks using both Fortran and C:

> 435.gromacs: `-prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32`

> 436.cactusADM: `-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -prefetch -parallel -auto-ilp32`

> 454.calculix: `-fast -unroll-aggressive -auto-ilp32`

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Peak Optimization Flags (Continued)

481.wrf: `-fast -parallel -prefetch -auto-ilp32`

The flags file that was used to format this result can be browsed at

`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.html`

You can also download the XML flags source by saving the following link:

`http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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