



SPEC® CFP2006 Result

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NEC Corporation

Express5800/120Gd
(Intel Xeon E5405)

SPECfp®2006 = 17.3

SPECfp_base2006 = 14.6

CPU2006 license: 9006

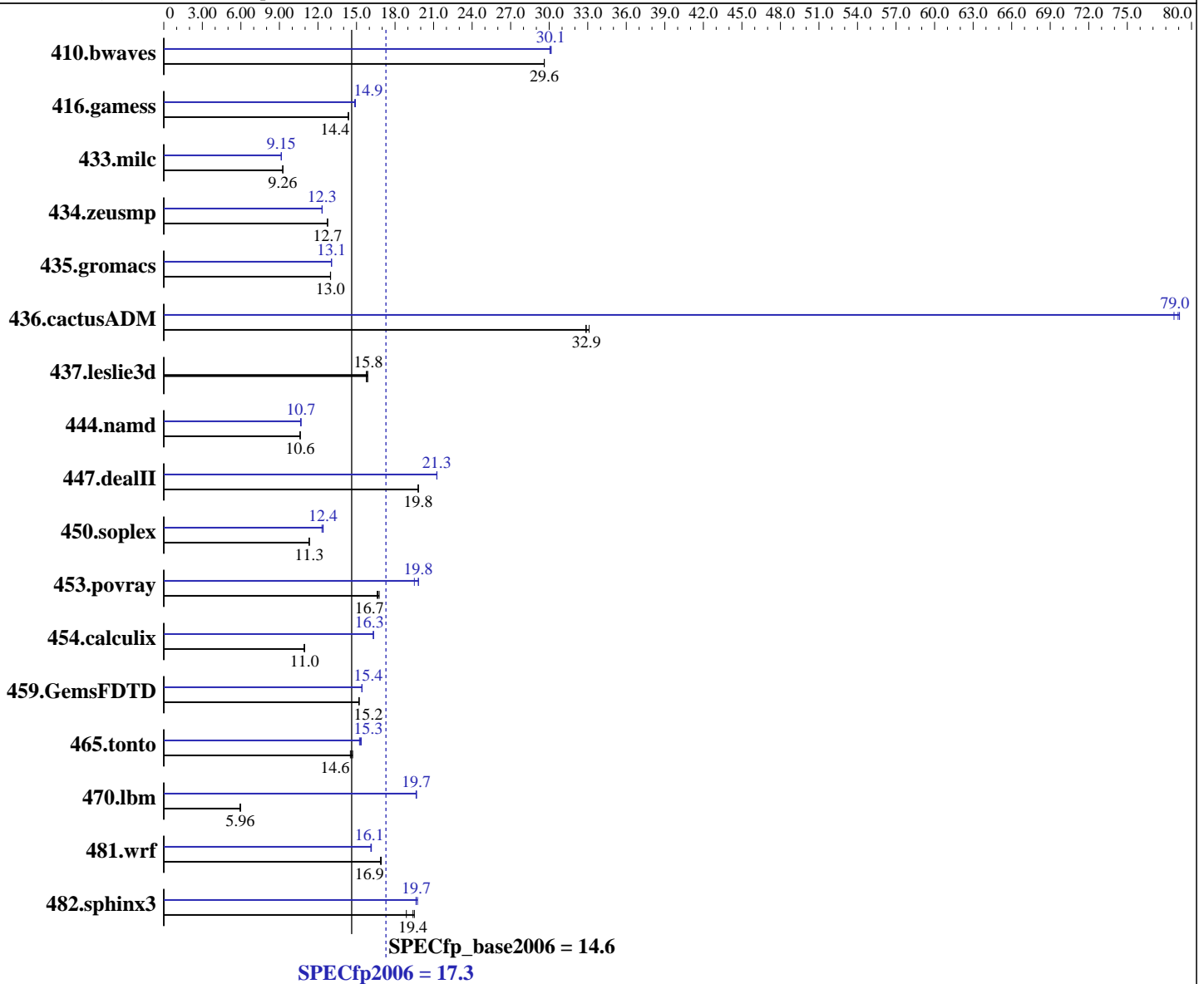
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: May-2008

Hardware Availability: Apr-2008

Software Availability: Nov-2007



Hardware		Software	
CPU Name:	Intel Xeon E5405	Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smpp
CPU Characteristics:	2.00 GHz, 2x6 MB L2 shared, 1333 MHz bus	Compiler:	Intel C++ and Fortran Compiler for Linux32 and Linux64
CPU MHz:	2000		version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
FPU:	Integrated	Auto Parallel:	Yes
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip	File System:	ext2
CPU(s) orderable:	1,2 chips		
Primary Cache:	32 KB I + 32 KB D on chip per core		
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores		
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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x250 GB SATAII, 7200RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: binutils-2.17.tar.gz, Version 2.17

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	459	29.6	459	29.6	459	29.6	452	30.1	451	30.1	451	30.2
416.gamess	1363	14.4	1364	14.4	1361	14.4	1314	14.9	1312	14.9	1317	14.9
433.milc	991	9.26	991	9.26	990	9.27	1004	9.15	1003	9.15	1004	9.14
434.zeusmp	714	12.7	714	12.7	714	12.7	738	12.3	739	12.3	738	12.3
435.gromacs	550	13.0	550	13.0	550	13.0	547	13.0	547	13.1	547	13.1
436.cactusADM	363	32.9	364	32.9	361	33.1	152	78.6	151	79.1	151	79.0
437.leslie3d	596	15.8	593	15.8	592	15.9	596	15.8	593	15.8	592	15.9
444.namd	755	10.6	756	10.6	755	10.6	751	10.7	754	10.6	751	10.7
447.dealII	578	19.8	578	19.8	577	19.8	538	21.3	538	21.3	538	21.3
450.soplex	738	11.3	737	11.3	736	11.3	677	12.3	673	12.4	672	12.4
453.povray	317	16.8	319	16.7	320	16.6	268	19.8	269	19.8	273	19.5
454.calculix	753	11.0	756	10.9	753	11.0	505	16.3	506	16.3	506	16.3
459.GemsFDTD	699	15.2	698	15.2	697	15.2	688	15.4	687	15.4	688	15.4
465.tonto	674	14.6	669	14.7	678	14.5	646	15.2	640	15.4	642	15.3
470.lbm	2315	5.94	2307	5.96	2300	5.97	699	19.7	698	19.7	700	19.6
481.wrf	661	16.9	660	16.9	662	16.9	691	16.2	693	16.1	693	16.1
482.sphinx3	1032	18.9	999	19.5	1005	19.4	992	19.7	993	19.6	987	19.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Hardware Prefetcher: Enabled
Adjacent Cache Line Prefetch: Enabled
Intel SpeedStep Technology: Disabled



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General Notes

All benchmarks compiled in 64-bit mode except 450.soplex, 470.lbm and 482.sphinx3, for peak, are compiled in 32-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast -parallel

C++ benchmarks:
-fast -parallel

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Base Optimization Flags (Continued)

Fortran benchmarks:

-fast -parallel

Benchmarks using both Fortran and C:

-fast -parallel

Peak Compiler Invocation

C benchmarks (except as noted below):

/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

433.milc: icc

C++ benchmarks (except as noted below):

icpc

450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

Continued on next page



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Peak Optimization Flags (Continued)

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

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