



# SPEC® CFP2006 Result

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## Bull SAS

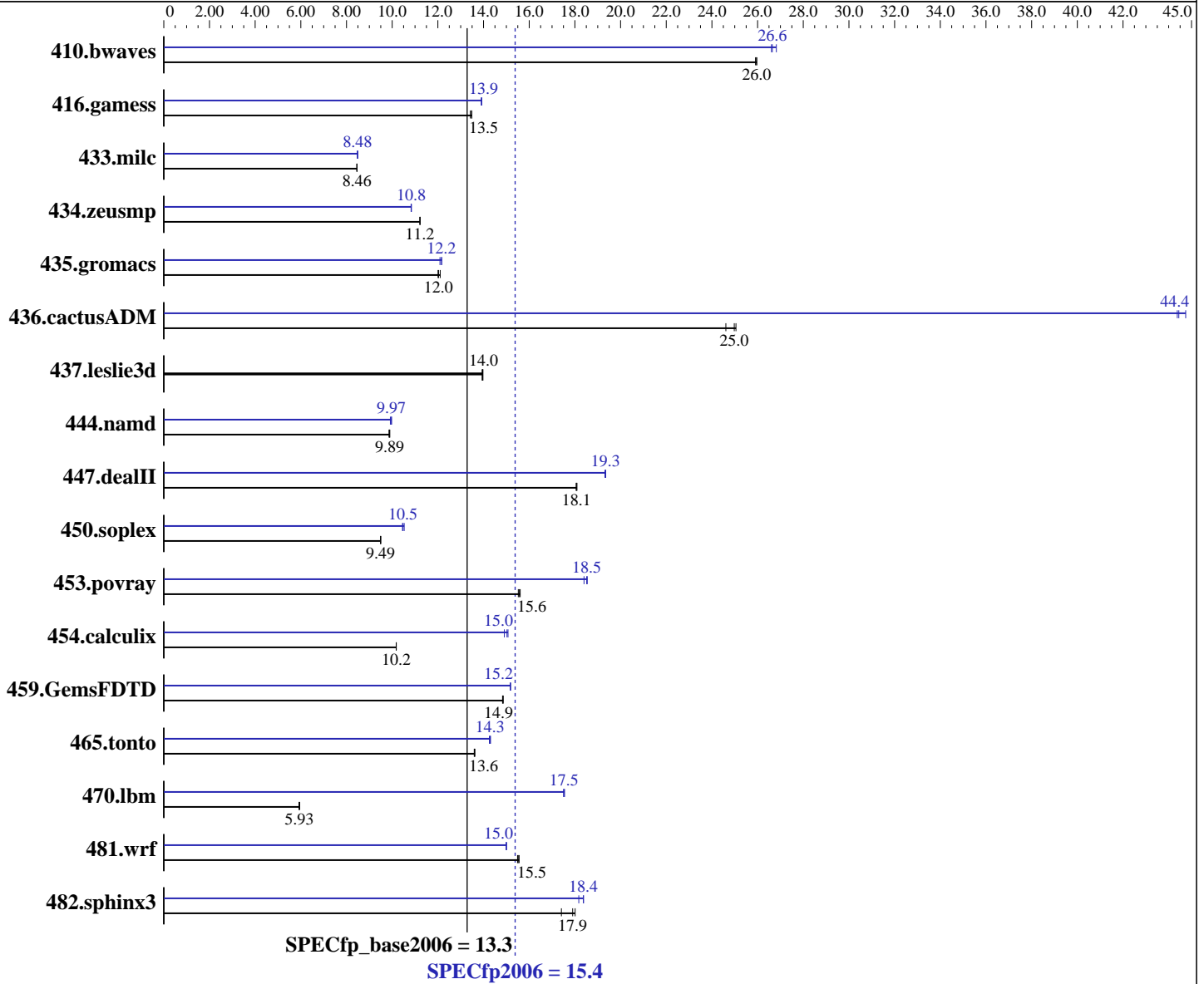
NovaScale T860 E1  
(Intel Xeon E5205, 1.86GHz)

SPECfp®2006 = 15.4

SPECfp\_base2006 = 13.3

CPU2006 license: 20  
Test sponsor: Bull SAS  
Tested by: NEC Corporation

Test date: Feb-2008  
Hardware Availability: Feb-2008  
Software Availability: Nov-2007



**Hardware**

CPU Name: Intel Xeon E5205  
 CPU Characteristics: 1.86 GHz, 6 MB L2, 1066 MHz bus  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 6 MB I+D on chip per chip

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**Software**

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP1, Kernel 2.6.16.46-0.12-smp  
 Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l\_cc\_p\_10.1.008, l\_fc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ext2

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L3 Cache: None  
Other Cache: None  
Memory: 12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)  
Disk Subsystem: 1x73.2 GB SAS, 15000RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: binutils-2.17.tar.gz, Version 2.17

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<b>524</b>	<b>26.0</b>	523	26.0	525	25.9	507	26.8	<b>510</b>	<b>26.6</b>	511	26.6
416.gamess	<b>1453</b>	<b>13.5</b>	1452	13.5	1459	13.4	<b>1408</b>	<b>13.9</b>	1408	13.9	1406	13.9
433.milc	<b>1085</b>	<b>8.46</b>	1085	8.46	1086	8.46	1083	8.48	<b>1082</b>	<b>8.48</b>	1081	8.49
434.zeusmp	811	11.2	811	11.2	<b>811</b>	<b>11.2</b>	<b>840</b>	<b>10.8</b>	840	10.8	839	10.8
435.gromacs	<b>594</b>	<b>12.0</b>	590	12.1	595	12.0	590	12.1	<b>587</b>	<b>12.2</b>	587	12.2
436.cactusADM	477	25.1	486	24.6	<b>478</b>	<b>25.0</b>	269	44.4	<b>269</b>	<b>44.4</b>	267	44.7
437.leslie3d	<b>673</b>	<b>14.0</b>	673	14.0	674	13.9	<b>673</b>	<b>14.0</b>	673	14.0	674	13.9
444.namd	813	9.86	<b>811</b>	<b>9.89</b>	811	9.89	808	9.92	<b>805</b>	<b>9.97</b>	804	9.97
447.dealII	<b>633</b>	<b>18.1</b>	632	18.1	634	18.1	591	19.3	592	19.3	<b>591</b>	<b>19.3</b>
450.soplex	878	9.50	<b>878</b>	<b>9.49</b>	879	9.49	792	10.5	<b>796</b>	<b>10.5</b>	798	10.5
453.povray	341	15.6	343	15.5	<b>342</b>	<b>15.6</b>	<b>287</b>	<b>18.5</b>	289	18.4	287	18.5
454.calculix	811	10.2	811	10.2	<b>811</b>	<b>10.2</b>	553	14.9	<b>549</b>	<b>15.0</b>	547	15.1
459.GemsFDTD	<b>714</b>	<b>14.9</b>	715	14.8	714	14.9	<b>699</b>	<b>15.2</b>	699	15.2	699	15.2
465.tonto	724	13.6	722	13.6	<b>723</b>	<b>13.6</b>	688	14.3	<b>689</b>	<b>14.3</b>	690	14.3
470.lbm	2309	5.95	<b>2317</b>	<b>5.93</b>	2321	5.92	<b>784</b>	<b>17.5</b>	785	17.5	783	17.5
481.wrf	718	15.6	<b>720</b>	<b>15.5</b>	721	15.5	745	15.0	<b>744</b>	<b>15.0</b>	744	15.0
482.sphinx3	1082	18.0	1119	17.4	<b>1089</b>	<b>17.9</b>	1061	18.4	<b>1061</b>	<b>18.4</b>	1073	18.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of cores

## Platform Notes

Bios settings:  
Intel SpeedStep Technology: Disabled

## General Notes

All benchmarks compiled in 64-bit mode except 450.soplex,  
470.lbm and 482.sphinx3, for peak, are compiled in 32-bit mode

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### General Notes (Continued)

The NEC Express5800/120Lj(Intel Xeon E5205) and the Bull NovaScale T860 E1(Intel Xeon E5205,1.86GHz) models are electronically equivalent. The results have been measured on a NEC Express5800/120Lj(Intel Xeon E5205) model.

### Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc  
  
Fortran benchmarks:  
ifort  
  
Benchmarks using both Fortran and C:  
icc ifort

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

### Base Optimization Flags

C benchmarks:  
-fast -parallel  
  
C++ benchmarks:  
-fast -parallel

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## Base Optimization Flags (Continued)

Fortran benchmarks:  
-fast -parallel

Benchmarks using both Fortran and C:  
-fast -parallel

## Peak Compiler Invocation

C benchmarks (except as noted below):  
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include

433.milc: icc

C++ benchmarks (except as noted below):  
icpc

450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

### C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-scalar-rep- -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast  
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

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## Peak Optimization Flags (Continued)

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-FP-intel64-linux-flags.20090713.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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