SPEC® CFP2006 Result

Fujitsu Siemens Computers

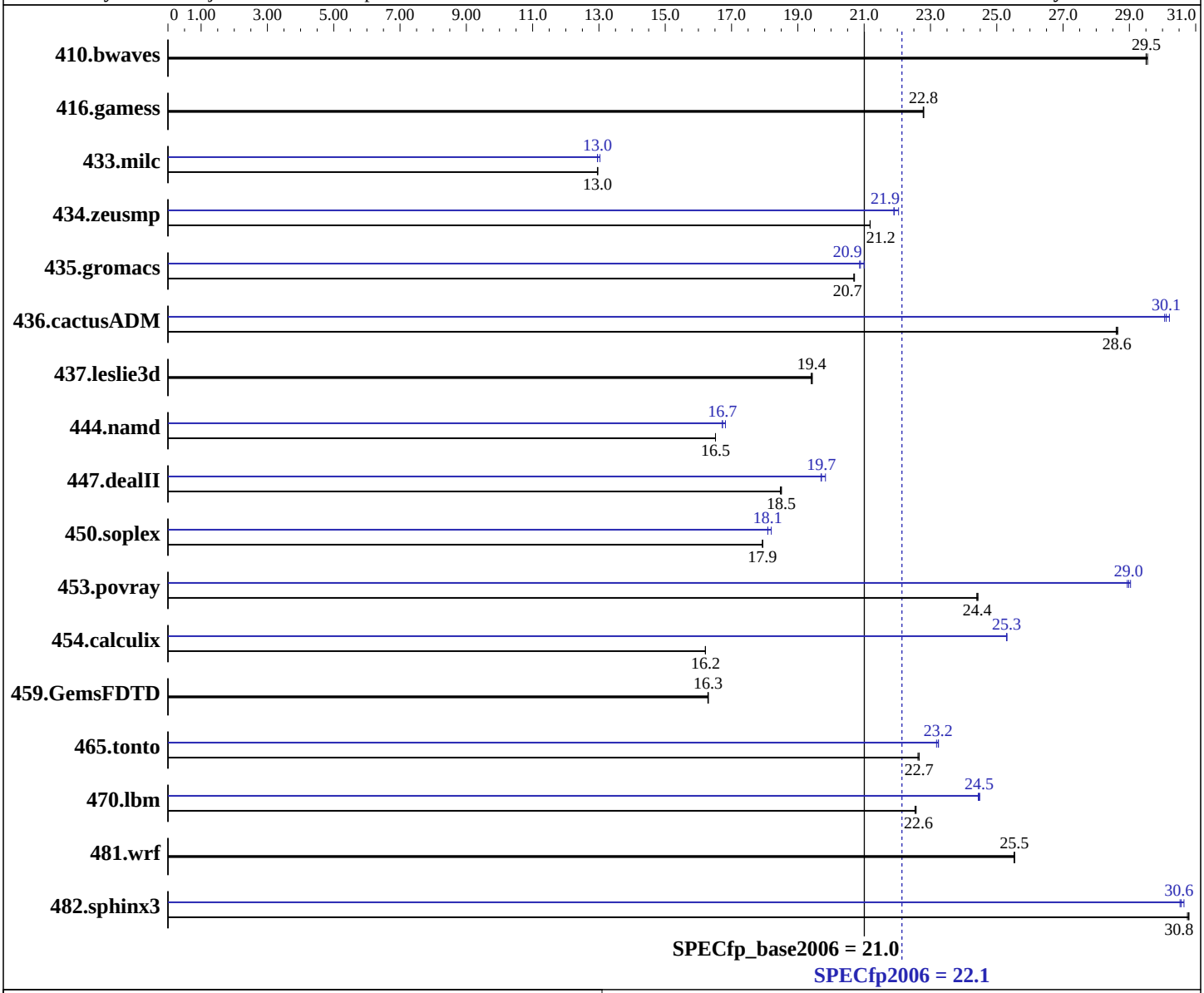
CELSIUS M460, Intel Core 2 Duo E8500

SPECfp®2006 = 22.1

SPECfp_base2006 = 21.0

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Feb-2008
Hardware Availability: Feb-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Core 2 Duo E8500
CPU Characteristics:	
CPU MHz:	3167
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	Windows Vista Ultimate, 64 bit Version

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L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem: 1 x 400 GB SATA 7200 RPM
Other Hardware: None

Compiler: Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20070913
Intel Visual Fortran Compiler for applications running on Intel 64, Version 10.1, Build 20070913
Microsoft Visual Studio 2005 with SP1 (for libraries)
Auto Parallel: Yes
File System: NTFS
System State: Default
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: MicroQuill SmartHeap Library 8.0 (64 bit)

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**461**	**29.5**	460	29.5	461	29.5	**461**	**29.5**	460	29.5	461	29.5
416.gamess	**859**	**22.8**	859	22.8	860	22.8	**859**	**22.8**	859	22.8	860	22.8
433.milc	708	13.0	**708**	**13.0**	709	13.0	704	13.0	709	13.0	**708**	**13.0**
434.zeusmp	430	21.2	430	21.2	**430**	**21.2**	413	22.0	416	21.9	**415**	**21.9**
435.gromacs	345	20.7	345	20.7	**345**	**20.7**	340	21.0	**342**	**20.9**	342	20.9
436.cactusADM	417	28.7	**417**	**28.6**	418	28.6	396	30.2	**397**	**30.1**	397	30.1
437.leslie3d	484	19.4	484	19.4	**484**	**19.4**	484	19.4	484	19.4	**484**	**19.4**
444.namd	**486**	**16.5**	486	16.5	486	16.5	477	16.8	480	16.7	**479**	**16.7**
447.dealII	619	18.5	**618**	**18.5**	618	18.5	577	19.8	581	19.7	**580**	**19.7**
450.soplex	465	17.9	465	17.9	**465**	**17.9**	458	18.2	**461**	**18.1**	461	18.1
453.povray	218	24.4	218	24.4	**218**	**24.4**	184	28.9	183	29.0	**184**	**29.0**
454.calculix	509	16.2	509	16.2	**509**	**16.2**	326	25.3	**326**	**25.3**	326	25.3
459.GemsFDTD	651	16.3	**651**	**16.3**	652	16.3	651	16.3	**651**	**16.3**	652	16.3
465.tonto	435	22.6	**434**	**22.7**	434	22.7	**423**	**23.2**	424	23.2	423	23.2
470.lbm	609	22.6	**609**	**22.6**	610	22.5	**562**	**24.5**	561	24.5	562	24.4
481.wrf	**437**	**25.5**	437	25.5	438	25.5	**437**	**25.5**	437	25.5	438	25.5
482.sphinx3	634	30.8	**633**	**30.8**	633	30.8	**638**	**30.6**	636	30.6	638	30.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
BIOS default settings have been used.
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com/
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc8 -Qc99 ifort
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
```
-fast -Qparallel -F1000000000 libguide40.lib
```

C++ benchmarks:
```
-fast -Qparallel -Qcxx-features -F1000000000 libguide40.lib
shlW64M.lib -link -FORCE:MULTIPLE
```

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-fast -Qparallel -F1000000000 libguide40.lib`

Benchmarks using both Fortran and C:
`-fast -Qparallel -F1000000000 libguide40.lib`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc8 -Qc99`

C++ benchmarks:
`icl -Qvc8`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icl -Qvc8 -Qc99 ifort`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F1000000000 libguide40.lib`

470.lbm: `-fast -Qunroll2 -Qscalar-rep- -Qprefetch -F1000000000 libguide40.lib`

482.sphinx3: `-fast -Qunroll2 -F1000000000 libguide40.lib`

C++ benchmarks:

444.namd: `-fast -Qcxx-features -Oa -F1000000000 libguide40.lib shlW64M.lib -link -FORCE:MULTIPLE`

447.dealII: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx-features -F1000000000 libguide40.lib shlW64M.lib -link -FORCE:MULTIPLE`

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Peak Optimization Flags (Continued)

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: -O2 -Qunroll0 -QxT -Qscalar-rep- -Qprec-div- -F1000000000 libguide40.lib

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -fast -Qunroll4 -Qauto -F1000000000 libguide40.lib

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F1000000000 libguide40.lib

436.cactusADM: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel -Qprefetch -Qunroll2 -F1000000000 libguide40.lib

454.calculix: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll-aggressive -F1000000000 libguide40.lib

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.03.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.03.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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