SPEC® CINT2006 Result
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Dell Inc.

Dell Precision T7400 (Intel Xeon X5482, 3.20 GHz)

$SPECint^{®}2006 =$ 26.1

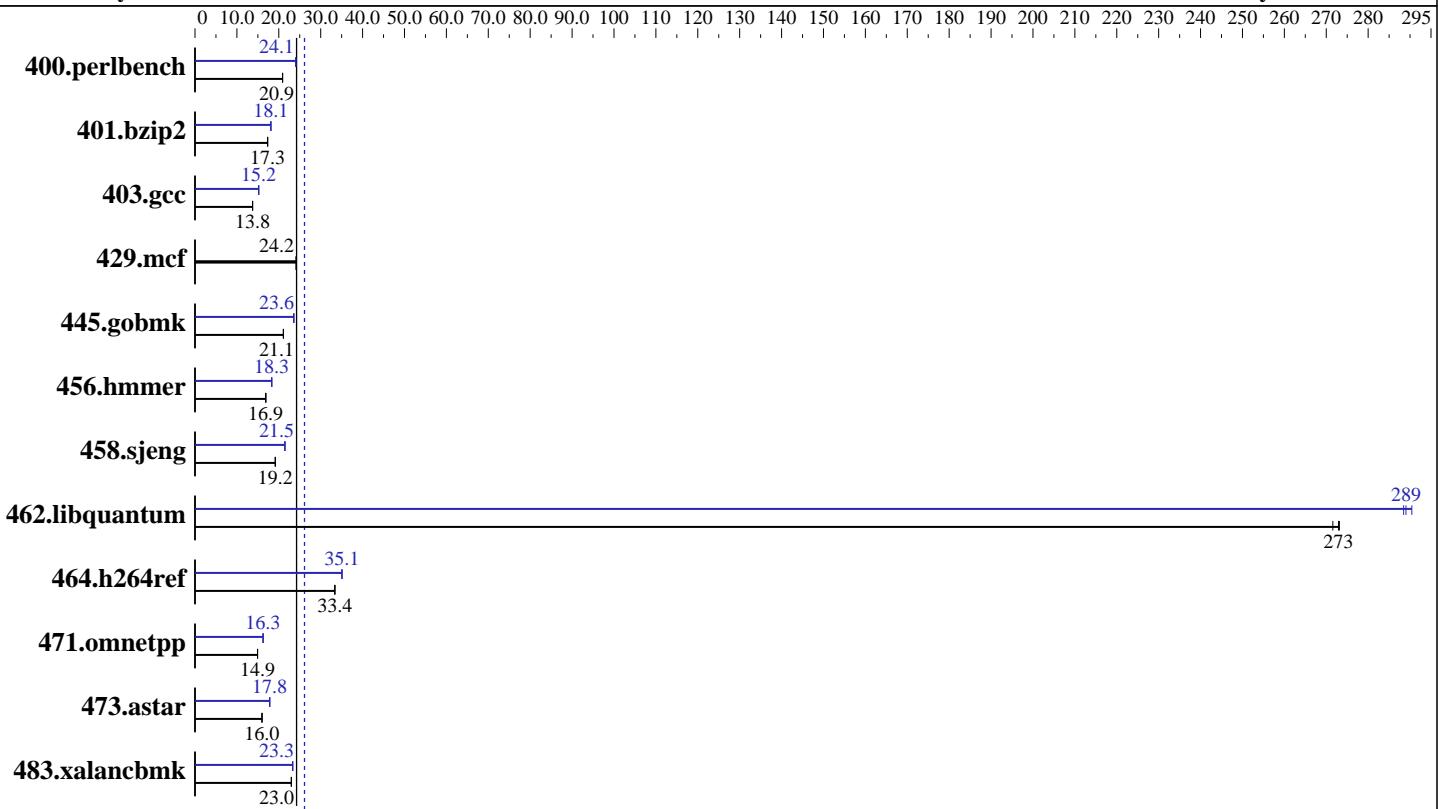$SPECint\_base2006 =$ 24.2

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Dec-2007
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Benchmark results chart (scale 0 to 295):

- **400.perlbench** 24.1 / 20.9
- **401.bzip2** 18.1 / 17.3
- **403.gcc** 15.2 / 13.8
- **429.mcf** 24.2
- **445.gobmk** 23.6 / 21.1
- **456.hmmer** 18.3 / 16.9
- **458.sjeng** 21.5 / 19.2
- **462.libquantum** 289 / 273
- **464.h264ref** 35.1 / 33.4
- **471.omnetpp** 16.3 / 14.9
- **473.astar** 17.8 / 16.0
- **483.xalancbmk** 23.3 / 23.0

SPECint_base2006 = 24.2
SPECint2006 = 26.1

Hardware

CPU Name:	Intel Xeon X5482
CPU Characteristics:	1600 MHz Bus Speed
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB 800 MHz FB-DIMM CL5)
Disk Subsystem:	1 x 73 GB SAS 10K RPM
Other Hardware:	None

Software

Operating System:	Windows XP Professional x64 Edition SP2
Compiler:	Intel C++ Compiler for IA-32, Version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	467	20.9	467	20.9	**467**	**20.9**	**406**	**24.1**	406	24.1	406	24.1
401.bzip2	557	17.3	557	17.3	**557**	**17.3**	533	18.1	**533**	**18.1**	533	18.1
403.gcc	**585**	**13.8**	585	13.8	586	13.7	529	15.2	530	15.2	**529**	**15.2**
429.mcf	377	24.2	**377**	**24.2**	378	24.2	377	24.2	**377**	**24.2**	378	24.2
445.gobmk	**497**	**21.1**	497	21.1	497	21.1	**445**	**23.6**	445	23.6	445	23.6
456.hmmer	**552**	**16.9**	552	16.9	552	16.9	**509**	**18.3**	509	18.3	509	18.3
458.sjeng	**631**	**19.2**	631	19.2	631	19.2	**564**	**21.5**	564	21.5	564	21.5
462.libquantum	75.9	273	76.3	272	**75.9**	**273**	**71.7**	**289**	71.8	288	71.3	290
464.h264ref	663	33.4	**663**	**33.4**	663	33.4	631	35.1	**631**	**35.1**	631	35.1
471.omnetpp	**419**	**14.9**	418	14.9	419	14.9	385	16.3	385	16.2	**385**	**16.3**
473.astar	439	16.0	439	16.0	**439**	**16.0**	394	17.8	**394**	**17.8**	394	17.8
483.xalancbmk	300	23.0	300	23.0	**300**	**23.0**	**296**	**23.3**	296	23.3	296	23.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
   Binaries were built on Windows Vista Ultimate (64-bit)

   BIOS Settings
     Adjacent Cache Line Prefetch set to ON
```

Base Compiler Invocation

C benchmarks:
```
    icl -Qstd=c99
```

C++ benchmarks:
```
    icl
```

Base Portability Flags

```
      403.gcc: -DSPEC_CPU_WIN32
   464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

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Base Optimization Flags

C benchmarks:
```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
shlw32mt.lib libguide40.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32mt.lib libguide40.lib
          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

     403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qstd=c99
```

C++ benchmarks:
```
icl
```

Peak Portability Flags

     403.gcc: `-DSPEC_CPU_WIN32`
   464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
 483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias`
                `-Qprefetch -Qparallel -Qpar-runtime-control /F512000000`
                 `shlw32mt.lib libguide40.lib`
                              `-link /FORCE:MULTIPLE`

       401.bzip2: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch`
                 `/F512000000 shlw32mt.lib libguide40.lib`
                              `-link /FORCE:MULTIPLE`

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Peak Optimization Flags (Continued)

403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
-Qprec-div- -Qansi-alias /F512000000
-link /FORCE:MULTIPLE

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias -Qopt-multi-version-aggressive /F512000000
shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
/F512000000 shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
-Qopt-streaming-stores:always -Qparallel
-Qpar-runtime-control /F512000000 shlw32mt.lib
libguide40.lib   -link /FORCE:MULTIPLE

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qansi-alias /F512000000 shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=block -Qcxx_features /F512000000
shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F512000000 shlw32mt.lib libguide40.lib
-link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.20090714.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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