



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®_rate2006 = Not Run

IBM System x3650 (Intel Xeon E5405)

SPECfp_rate_base2006 = 58.2

CPU2006 license: 11

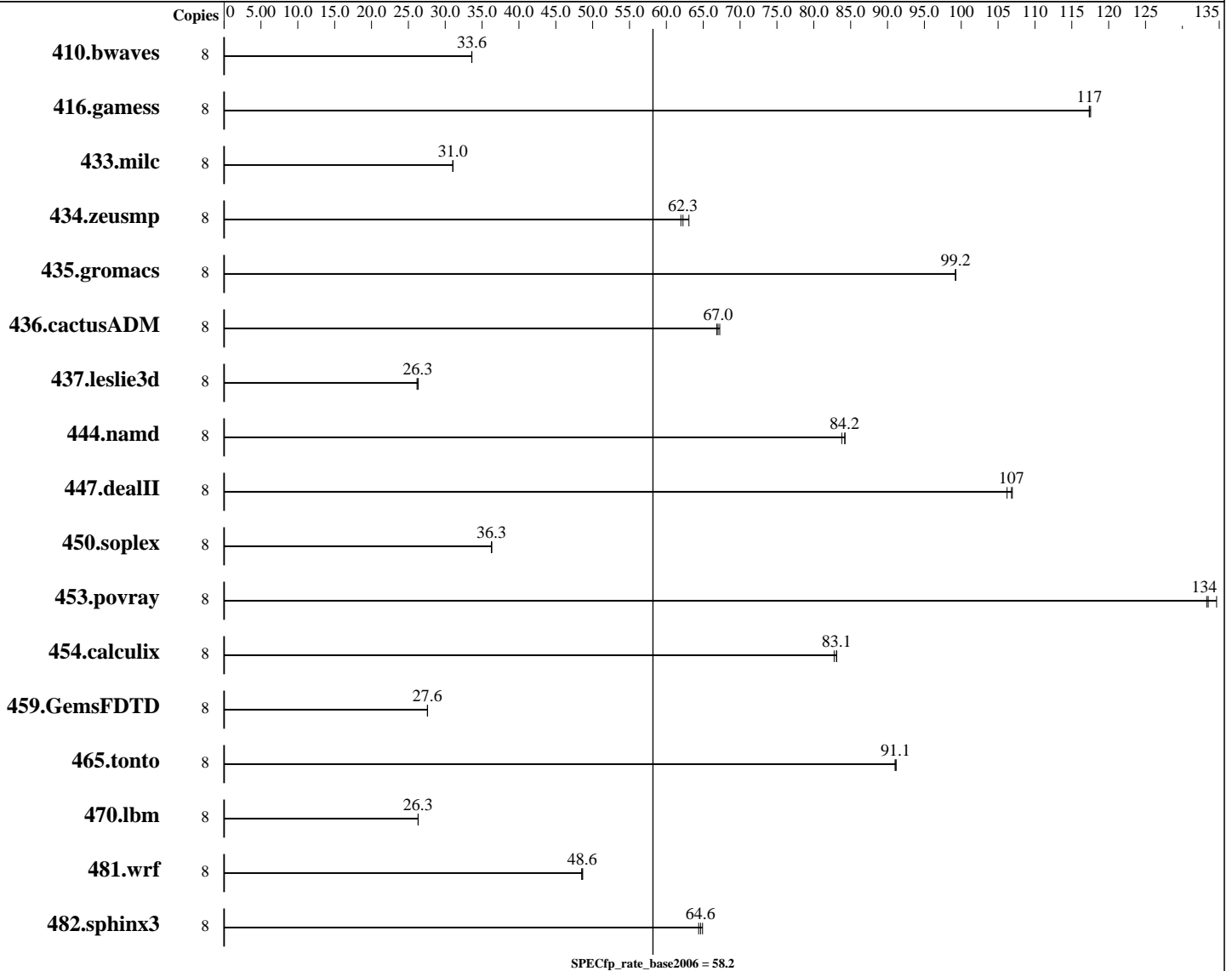
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Nov-2007

Hardware Availability: Jan-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5405
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 1995
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64), kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
 Auto Parallel: No
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 36 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: Not Applicable
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	<u>3236</u>	<u>33.6</u>	3237	33.6	3233	33.6							
416.gamess	8	1332	118	<u>1334</u>	<u>117</u>	1335	117							
433.milc	8	<u>2366</u>	<u>31.0</u>	2365	31.1	2367	31.0							
434.zeusmp	8	1175	62.0	1154	63.1	<u>1169</u>	<u>62.3</u>							
435.gromacs	8	<u>576</u>	<u>99.2</u>	576	99.2	575	99.3							
436.cactusADM	8	<u>1427</u>	<u>67.0</u>	1430	66.8	1422	67.2							
437.leslie3d	8	2874	26.2	<u>2860</u>	<u>26.3</u>	2857	26.3							
444.namd	8	<u>762</u>	<u>84.2</u>	766	83.8	762	84.2							
447.dealII	8	856	107	862	106	<u>857</u>	<u>107</u>							
450.soplex	8	<u>1839</u>	<u>36.3</u>	1840	36.3	1838	36.3							
453.povray	8	319	133	<u>319</u>	<u>134</u>	316	135							
454.calculix	8	<u>794</u>	<u>83.1</u>	798	82.8	794	83.1							
459.GemsFDTD	8	3077	27.6	3075	27.6	<u>3077</u>	<u>27.6</u>							
465.tonto	8	863	91.2	865	91.0	<u>864</u>	<u>91.1</u>							
470.lbm	8	4177	26.3	<u>4175</u>	<u>26.3</u>	4175	26.3							
481.wrf	8	1837	48.6	<u>1840</u>	<u>48.6</u>	1842	48.5							
482.sphinx3	8	2402	64.9	2422	64.4	<u>2412</u>	<u>64.6</u>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

All benchmarks compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
Powersaved dameon was disabled in OS
taskset utility used to bind CPU(s) to processes

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 15:08:27 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 26 December 2007.