



# SPEC® CFP2006 Result

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## Acer Incorporated

### SPECfp®\_rate2006 = 65.5

## Acer Altos R520 (Intel Xeon X5365)

### SPECfp\_rate\_base2006 = 62.7

CPU2006 license: 97

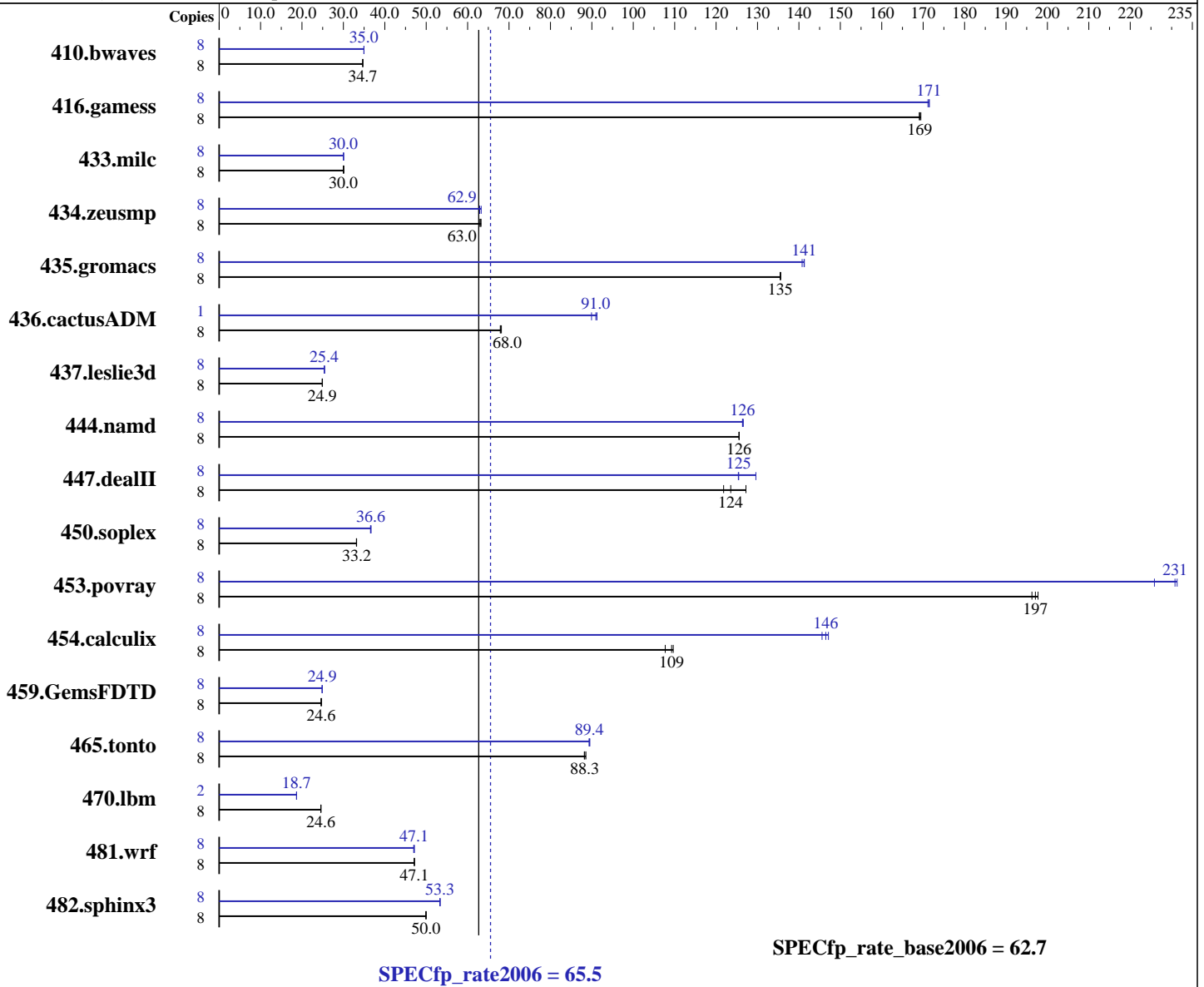
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Nov-2007

Hardware Availability: Nov-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5365  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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### Software

Operating System: SuSE Linux Enterprise Server 10 SP1 EM64T (kernel 2.6.16.46-0.12-smp)  
 Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008, l\_fc\_p\_10.1.008  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, level 3  
 Base Pointers: 64-bit

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L3 Cache: None  
Other Cache: None  
Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)  
Disk Subsystem: 1 x 73GB 10000PRM SAS HDD  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Binutils 2.17.50.0.15

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3136	34.7	3135	34.7	<b>3135</b>	<b>34.7</b>	8	3108	35.0	3111	35.0	<b>3109</b>	<b>35.0</b>
416.gamess	8	924	169	927	169	<b>926</b>	<b>169</b>	8	915	171	913	171	<b>914</b>	<b>171</b>
433.milc	8	2439	30.1	<b>2446</b>	<b>30.0</b>	2450	30.0	8	<b>2446</b>	<b>30.0</b>	2447	30.0	2443	30.1
434.zeusmp	8	<b>1155</b>	<b>63.0</b>	1155	63.0	1151	63.3	8	1150	63.3	<b>1157</b>	<b>62.9</b>	1157	62.9
435.gromacs	8	<b>422</b>	<b>135</b>	421	136	422	135	8	404	141	406	141	<b>404</b>	<b>141</b>
436.cactusADM	8	1408	67.9	<b>1406</b>	<b>68.0</b>	1403	68.2	1	133	89.9	<b>131</b>	<b>91.0</b>	131	91.2
437.leslie3d	8	3015	24.9	3023	24.9	<b>3017</b>	<b>24.9</b>	8	2964	25.4	<b>2961</b>	<b>25.4</b>	2952	25.5
444.namd	8	<b>511</b>	<b>126</b>	511	126	511	126	8	507	127	508	126	<b>508</b>	<b>126</b>
447.dealII	8	751	122	<b>741</b>	<b>124</b>	719	127	8	<b>729</b>	<b>125</b>	706	130	730	125
450.soplex	8	<b>2011</b>	<b>33.2</b>	2013	33.1	2010	33.2	8	1826	36.5	1819	36.7	<b>1822</b>	<b>36.6</b>
453.povray	8	217	196	<b>216</b>	<b>197</b>	215	198	8	188	226	184	231	<b>184</b>	<b>231</b>
454.calculix	8	613	108	<b>604</b>	<b>109</b>	602	110	8	449	147	453	146	<b>451</b>	<b>146</b>
459.GemsFDTD	8	3451	24.6	3430	24.7	<b>3446</b>	<b>24.6</b>	8	3412	24.9	<b>3411</b>	<b>24.9</b>	3410	24.9
465.tonto	8	889	88.6	<b>892</b>	<b>88.3</b>	893	88.2	8	881	89.3	879	89.5	<b>880</b>	<b>89.4</b>
470.lbm	8	4475	24.6	4474	24.6	<b>4474</b>	<b>24.6</b>	2	<b>1470</b>	<b>18.7</b>	1470	18.7	1470	18.7
481.wrf	8	<b>1896</b>	<b>47.1</b>	1895	47.2	1897	47.1	8	<b>1898</b>	<b>47.1</b>	1896	47.1	1902	47.0
482.sphinx3	8	<b>3121</b>	<b>50.0</b>	3118	50.0	3125	49.9	8	<b>2925</b>	<b>53.3</b>	2925	53.3	2921	53.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

This result was measured on the Acer Altos R520.  
The Altos R720 and Altos R520 are electronically equivalent.

All benchmarks compiled in 64-bit mode except 437.leslie3d, 450.soplex  
470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode

### BIOS Setting

Hardware Prefetcher set to OFF  
Adjacent Cache Line Prefetch set to OFF  
Optimization for high-frequency FSB applications set to ON

All results used taskset to bind processes to cores.



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## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast



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## Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

433.milc: icc

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib
-I/opt/intel/cc/10.1.008/include
```

Fortran benchmarks (except as noted below):

ifort

```
437.leslie3d: /opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib
-I/opt/intel/fc/10.1.008/include
```

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.deall: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32
```

```
470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep- -prefetch -opt-malloc-options=3
```

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## Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll2

### C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32

447.dealIII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast  
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0  
-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.06.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.06.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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