



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-m6
(Intel Xeon processor E5345)

SPECint®2006 = 16.9

SPECint_base2006 = 15.4

CPU2006 license: 9006

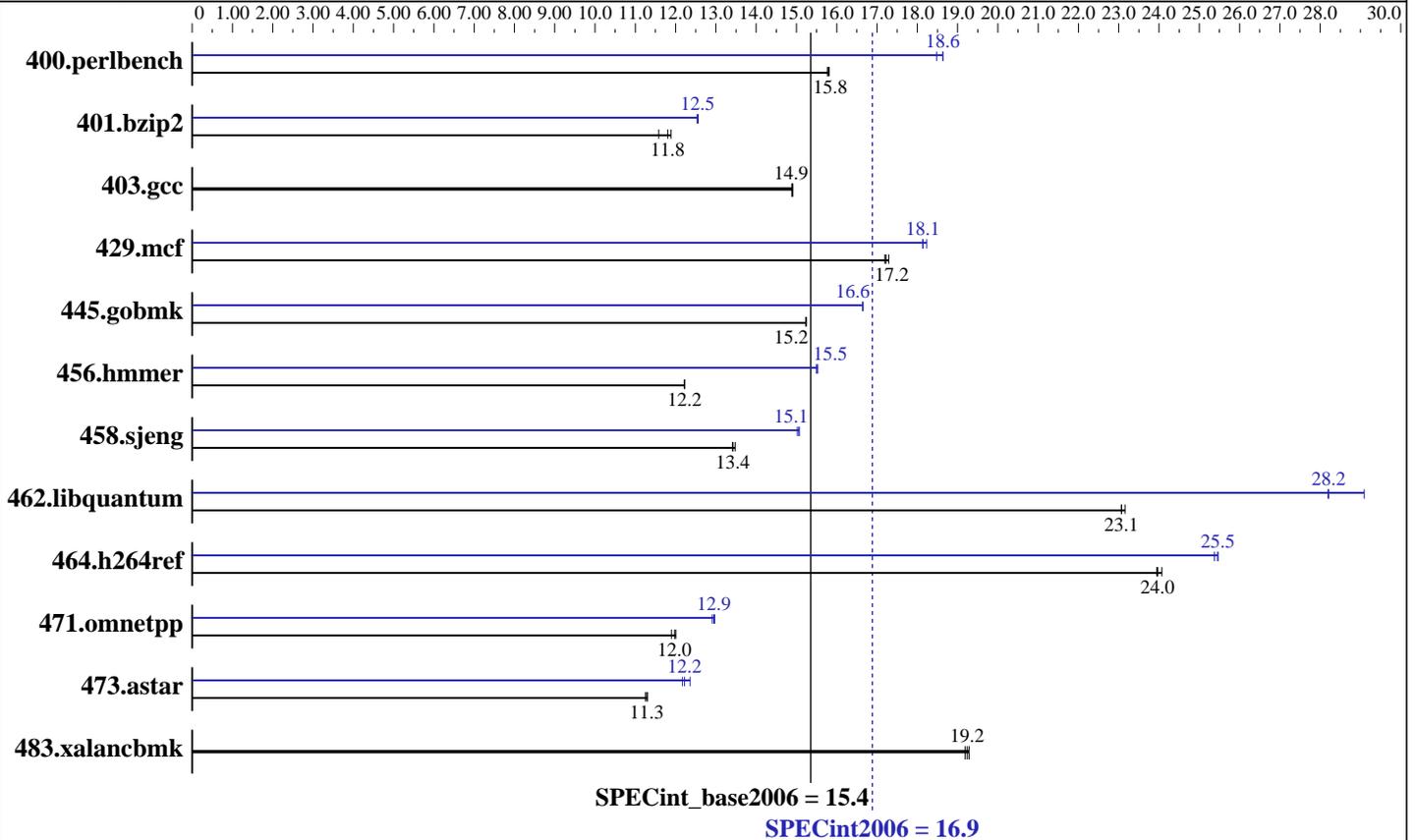
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Nov-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon E5345
 CPU Characteristics: 2.33 GHz, 2x4 MB L2 shared, 1333 MHz bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x73.2 GB SAS, 10000RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070426 Package ID: I_cc_p_10.0.023
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-m6
(Intel Xeon processor E5345)

SPECint2006 = 16.9

SPECint_base2006 = 15.4

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Nov-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	<u>619</u>	<u>15.8</u>	619	15.8	618	15.8	524	18.6	529	18.5	<u>524</u>	<u>18.6</u>
401.bzip2	812	11.9	833	11.6	<u>817</u>	<u>11.8</u>	770	12.5	<u>769</u>	<u>12.5</u>	768	12.6
403.gcc	541	14.9	<u>541</u>	<u>14.9</u>	540	14.9	541	14.9	<u>541</u>	<u>14.9</u>	540	14.9
429.mcf	527	17.3	530	17.2	<u>529</u>	<u>17.2</u>	<u>503</u>	<u>18.1</u>	500	18.2	503	18.1
445.gobmk	<u>688</u>	<u>15.2</u>	689	15.2	688	15.2	630	16.7	<u>630</u>	<u>16.6</u>	630	16.6
456.hmmer	764	12.2	763	12.2	<u>763</u>	<u>12.2</u>	<u>602</u>	<u>15.5</u>	601	15.5	602	15.5
458.sjeng	898	13.5	902	13.4	<u>902</u>	<u>13.4</u>	805	15.0	803	15.1	<u>804</u>	<u>15.1</u>
462.libquantum	898	23.1	895	23.2	<u>898</u>	<u>23.1</u>	<u>734</u>	<u>28.2</u>	735	28.2	712	29.1
464.h264ref	924	23.9	<u>923</u>	<u>24.0</u>	919	24.1	<u>869</u>	<u>25.5</u>	869	25.5	872	25.4
471.omnetpp	525	11.9	<u>522</u>	<u>12.0</u>	520	12.0	482	13.0	<u>483</u>	<u>12.9</u>	484	12.9
473.astar	624	11.3	<u>622</u>	<u>11.3</u>	621	11.3	577	12.2	<u>574</u>	<u>12.2</u>	568	12.4
483.xalancbmk	358	19.3	<u>359</u>	<u>19.2</u>	360	19.2	358	19.3	<u>359</u>	<u>19.2</u>	360	19.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-m6
(Intel Xeon processor E5345)

SPECint2006 = 16.9

SPECint_base2006 = 15.4

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Nov-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-m6
(Intel Xeon processor E5345)

SPECint2006 = 16.9

SPECint_base2006 = 15.4

CPU2006 license: 9006

Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Nov-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007

Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-ic10-INT-ia32-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-ic10-INT-ia32-intel64-linux-flags.xml>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/120Bb-m6
(Intel Xeon processor E5345)

SPECint2006 = 16.9

SPECint_base2006 = 15.4

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Nov-2007
Hardware Availability: Jan-2007
Software Availability: Jun-2007

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 13:46:57 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 11 December 2007.