



# SPEC® CINT2006 Result

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## NEC Corporation

Express5800/i110Rh-1  
(Intel Xeon processor 3040)

**SPECint®2006 = 14.3**

**SPECint\_base2006 = 13.1**

CPU2006 license: 9006

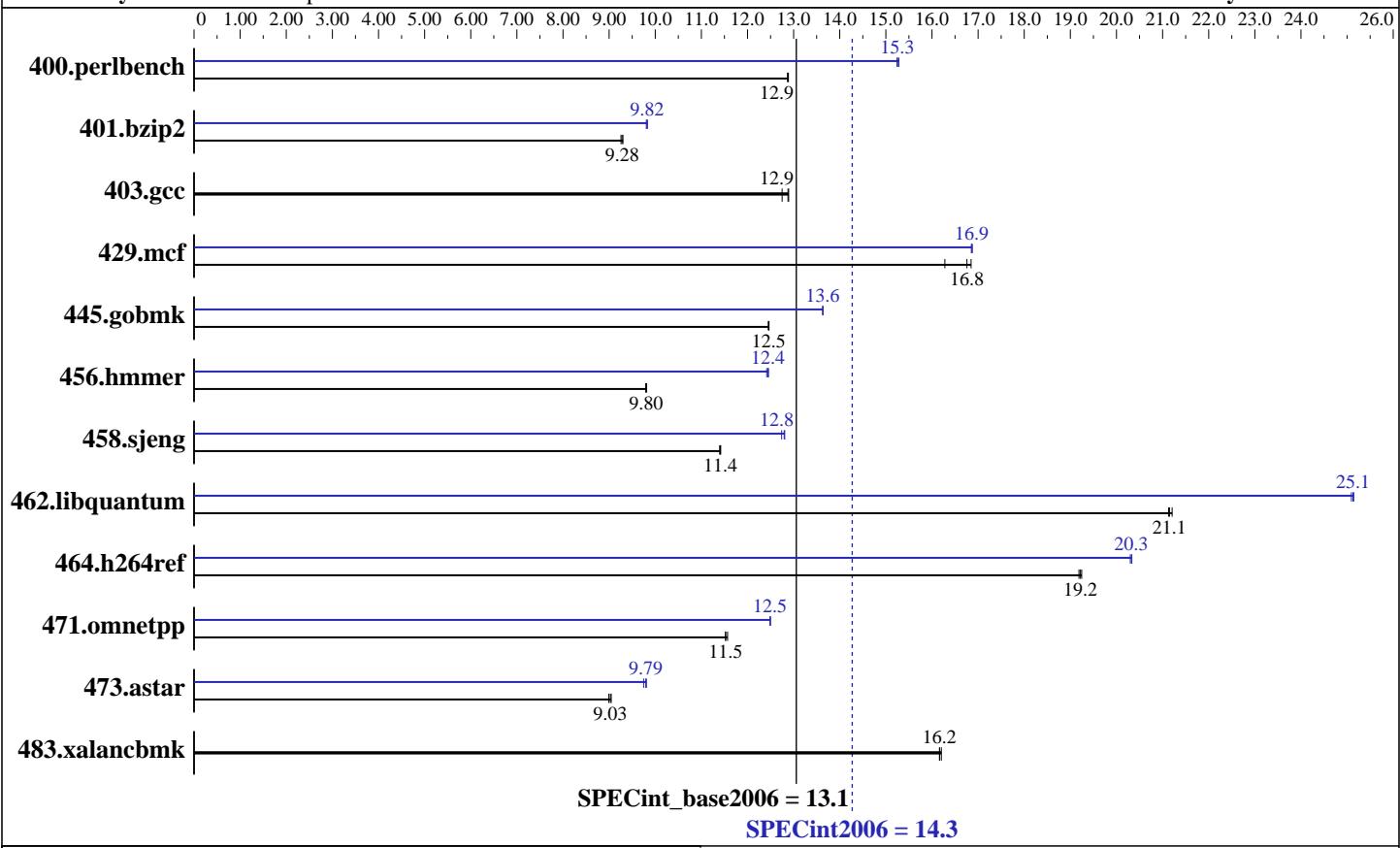
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Nov-2007

Hardware Availability: May-2007

Software Availability: Jun-2007



### Hardware

CPU Name: Intel Xeon 3040  
CPU Characteristics: 1.86 GHz, 2 MB L2, 1066 MHz bus  
CPU MHz: 1866  
FPU: Integrated  
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 2 MB I+D on chip per chip  
L3 Cache: None  
Other Cache: None  
Memory: 4 GB (4x1 GB PC2-5300E, 2 rank, CL5-5-5, ECC)  
Disk Subsystem: 1x80 GB SATAII, 7200RPM  
Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64), Kernel 2.6.16.21-0.8-smp  
Compiler: Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070426 Package ID: l\_cc\_p\_10.0.023  
Auto Parallel: No  
File System: ext2  
System State: Multiuser, Runlevel 3  
Base Pointers: 32-bit  
Peak Pointers: 32/64-bit  
Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17





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## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

456.hmmr: /opt/intel/cce/10.0.023/bin/icc  
-L/opt/intel/cce/10.0.023/lib  
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:  
icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmr: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Obo  
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-ic10-INT-ia32-intel64-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-ic10-INT-ia32-intel64-linux-flags.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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