



SPEC[®] CFP2006 Result

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Fujitsu Siemens Computers

PRIMERGY RX200 S4, Intel Xeon processor E5420,
2.50 GHz

SPECfp[®]_rate2006 = 64.1

SPECfp_rate_base2006 = 64.1

CPU2006 license: 22

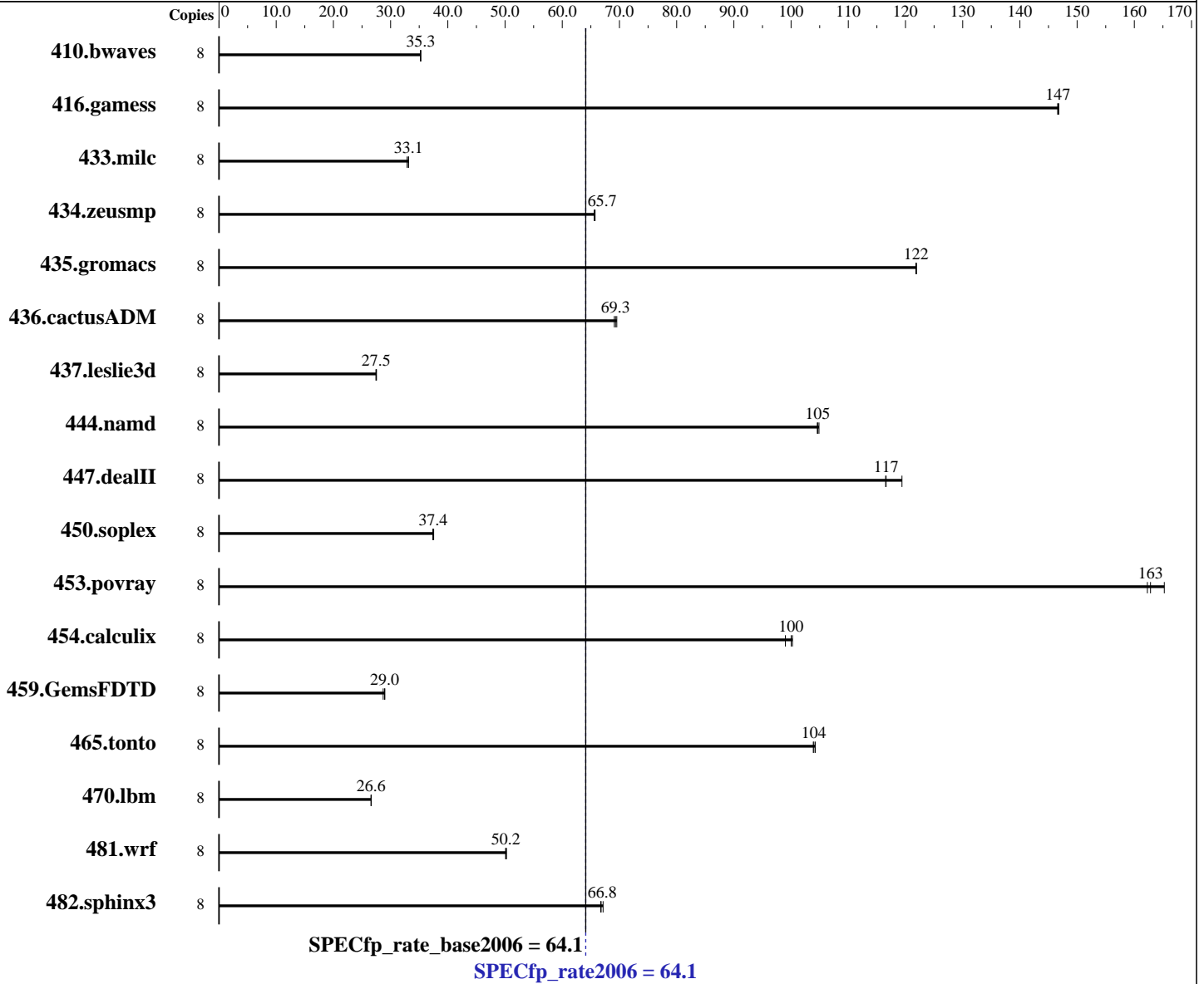
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Nov-2007

Hardware Availability: Dec-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5420
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1,
 Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ and Fortran Compiler
 for Linux32 and Linux64
 Version 10.1 - Build 20070725
 Auto Parallel: No
 File System: ext2
 System State: Multiuser, Runlevel 3
 Base Pointers: 64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: Seagate ST973451SS (SAS, 73GB, 15000rpm)
Other Hardware: None

Peak Pointers: 64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3085	35.2	3084	35.3	3084	35.3	8	3085	35.2	3084	35.3	3084	35.3
416.gamess	8	1068	147	1067	147	1068	147	8	1068	147	1067	147	1068	147
433.milc	8	2234	32.9	2217	33.1	2217	33.1	8	2234	32.9	2217	33.1	2217	33.1
434.zeusmp	8	1110	65.6	1108	65.7	1107	65.7	8	1110	65.6	1108	65.7	1107	65.7
435.gromacs	8	469	122	469	122	469	122	8	469	122	469	122	469	122
436.cactusADM	8	1384	69.1	1379	69.3	1375	69.5	8	1384	69.1	1379	69.3	1375	69.5
437.leslie3d	8	2737	27.5	2741	27.4	2731	27.5	8	2737	27.5	2741	27.4	2731	27.5
444.namd	8	613	105	613	105	612	105	8	613	105	613	105	612	105
447.dealII	8	767	119	785	117	785	117	8	767	119	785	117	785	117
450.soplex	8	1778	37.5	1783	37.4	1784	37.4	8	1778	37.5	1783	37.4	1784	37.4
453.povray	8	262	162	261	163	258	165	8	262	162	261	163	258	165
454.calculix	8	658	100	660	100	667	99.0	8	658	100	660	100	667	99.0
459.GemsFDTD	8	2929	29.0	2957	28.7	2931	29.0	8	2929	29.0	2957	28.7	2931	29.0
465.tonto	8	757	104	755	104	758	104	8	757	104	755	104	758	104
470.lbm	8	4135	26.6	4133	26.6	4131	26.6	8	4135	26.6	4133	26.6	4131	26.6
481.wrf	8	1784	50.1	1782	50.2	1778	50.3	8	1784	50.1	1782	50.2	1778	50.3
482.sphinx3	8	2334	66.8	2336	66.7	2322	67.1	8	2334	66.8	2336	66.7	2322	67.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

This result has been produced with binaries provided and compiled by Intel.

BIOS configuration:

Hardware Prefetch = Disable, Adjacent Sector Prefetch = Disable

For information about Fujitsu Siemens Computers please see:

<http://www.fujitsu-siemens.com>



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: basepeak = yes

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.05.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.05.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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