



# SPEC® CINT2006 Result

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**Intel Corporation**

**SPECint®2006 = 25.5**

Asus P5E3 Deluxe (Intel Core 2 Extreme QX9650)

**SPECint\_base2006 = 22.3**

CPU2006 license: 13

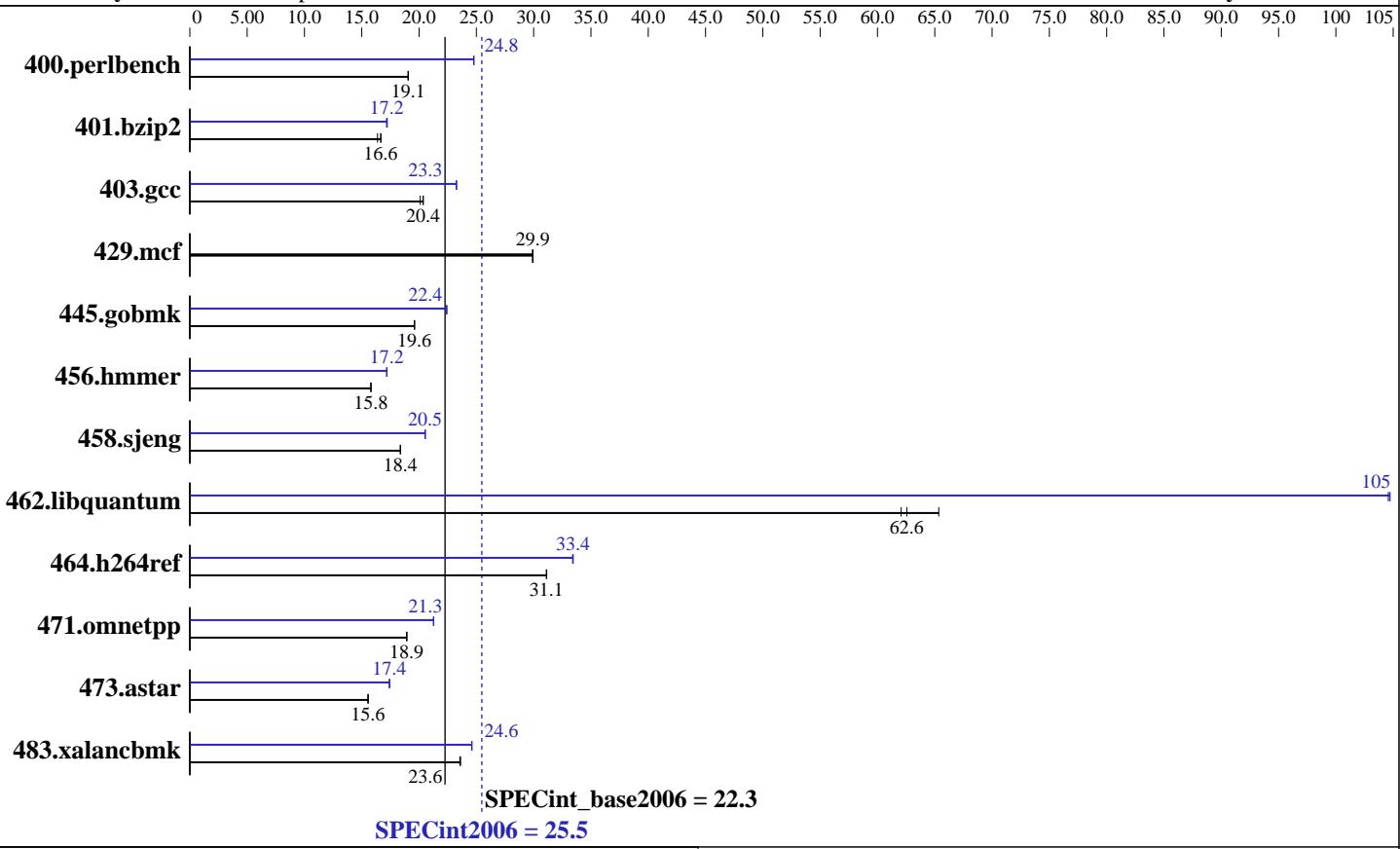
Test sponsor: Intel Corporation

Tested by: Intel Corporation

**Test date:** Nov-2007

**Hardware Availability:** Nov-2007

**Software Availability:** Nov-2007



## Hardware

CPU Name:	Intel Core 2 Extreme QX9650
CPU Characteristics:	3.00 GHz 1333 MHz FSB
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (4x1GB Corsair TWIN3X2048-1333C9DHX DDR3-1333 CL9)
Disk Subsystem:	Seagate 320GB NCQ SATA, 16MB cache, 7200 RPM
Other Hardware:	None

## Software

Operating System:	Windows Vista64 Ultimate
Compiler:	Intel C++ Compiler for IA32 version 10.1 Build 20070913 Package ID: w_cc_p_10.1.011 Microsoft Visual Studio 2005 SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	SmartHeap Library Version 8.1 from <a href="http://www.microquill.com/">http://www.microquill.com/</a>



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	513	19.0	512	19.1	<b>513</b>	<b>19.1</b>	395	24.8	394	24.8	<b>394</b>	<b>24.8</b>
401.bzip2	<b>580</b>	<b>16.6</b>	589	16.4	579	16.7	<b>562</b>	<b>17.2</b>	<b>561</b>	<b>17.2</b>	560	17.2
403.gcc	400	20.1	<b>396</b>	<b>20.4</b>	395	20.4	346	23.2	<b>346</b>	<b>23.3</b>	346	23.3
429.mcf	<b>305</b>	<b>29.9</b>	305	29.9	305	29.9	<b>305</b>	<b>29.9</b>	305	29.9	305	29.9
445.gobmk	535	19.6	535	19.6	<b>535</b>	<b>19.6</b>	468	22.4	<b>468</b>	<b>22.4</b>	468	22.4
456.hmmer	591	15.8	590	15.8	<b>591</b>	<b>15.8</b>	<b>543</b>	<b>17.2</b>	543	17.2	543	17.2
458.sjeng	<b>659</b>	<b>18.4</b>	659	18.4	659	18.4	589	20.5	<b>589</b>	<b>20.5</b>	589	20.5
462.libquantum	<b>331</b>	<b>62.6</b>	334	62.1	317	65.3	<b>198</b>	<b>105</b>	198	105	198	105
464.h264ref	711	31.1	711	31.1	<b>711</b>	<b>31.1</b>	663	33.4	<b>662</b>	<b>33.4</b>	662	33.4
471.omnetpp	<b>330</b>	<b>18.9</b>	330	18.9	330	18.9	294	21.3	294	21.3	<b>294</b>	<b>21.3</b>
473.astar	451	15.6	451	15.6	<b>451</b>	<b>15.6</b>	<b>403</b>	<b>17.4</b>	403	17.4	403	17.4
483.xalancbmk	292	23.6	<b>292</b>	<b>23.6</b>	293	23.6	281	24.6	280	24.6	<b>281</b>	<b>24.6</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply Product description located as of 11/2007:

<http://www.asus.com/products.aspx?l1=3&l2=11&l3=572&l4=0&model=1872&modelmenu=1>

The system bus runs at 1333 MHz

System was configured with Asus EN8800GTX discrete graphics card

Binaries were built on Windows Vista32

The following VS 2005 SP1 updates were applied: KB926601 and KB932232

## Base Compiler Invocation

C benchmarks:

icl -Qvc8 -Qc99

C++ benchmarks:

icl -Qvc8

## Base Portability Flags

403.gcc: -DSPEC\_CPU\_WIN32

464.h264ref: -DSPEC\_CPU\_NO\_INTTYPES -DWIN32

483.xalancbmk: -Qoption,cpp,--no\_wchar\_t\_keyword



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## Base Optimization Flags

C benchmarks:

```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000  
libguide40.lib
```

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib  
-link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks:

```
icl -Qvc8 -QC99
```

C++ benchmarks:

```
icl -Qvc8
```

## Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

```
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias  
-Qprefetch -Qparallel -Qpar-runtime-control /F512000000  
shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch  
/F512000000 libguide40.lib
```

```
403.gcc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000  
libguide40.lib
```

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## Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

```
445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo
           -Qprec-div- -Qansi-alias /F512000000
456.hmmr: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
           -Qansi-alias -Qopt-multi-version-aggressive /F512000000
           libguide40.lib
```

```
458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4
           /F512000000 libguide40.lib
```

```
462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch
                -Qopt-streaming-stores:always -Qparallel
                -Qpar-runtime-control /F512000000 libguide40.lib
```

```
464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
              -Qansi-alias /F512000000 libguide40.lib
```

C++ benchmarks:

```
471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
              -Qopt-ra-region-strategy=block -Qcxx_features /F512000000
              shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE
```

```
473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
            -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000
            shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE
```

```
483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
                 -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
                 -link /FORCE:MULTIPLE
```

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.09.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 14:31:50 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 27 November 2007.