



SPEC® CINT2006 Result

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Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor X5355, 2.66 GHz)

SPECint®2006 = 22.4

SPECint_base2006 = 19.4

CPU2006 license: 13

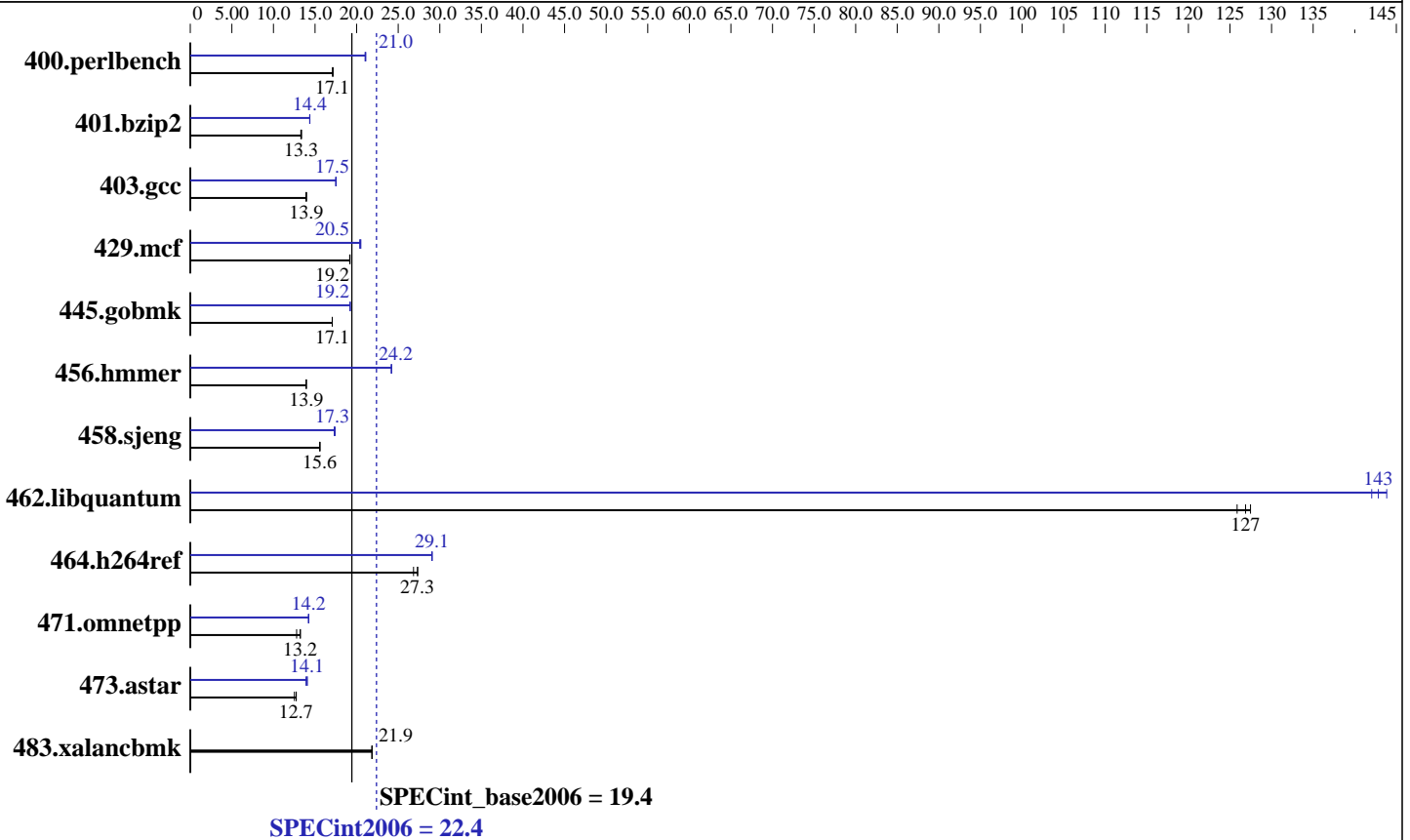
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2007

Hardware Availability: Jul-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: Quad Core, 2.66 GHz
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 * 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10 SP1 RC1, Kernel linux-cbqm 2.6.16.43-0.5-smp for x86_64
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>571</u>	<u>17.1</u>	569	17.2	571	17.1	463	21.1	465	21.0	<u>464</u>	<u>21.0</u>
401.bzip2	<u>724</u>	<u>13.3</u>	721	13.4	726	13.3	672	14.4	<u>671</u>	<u>14.4</u>	671	14.4
403.gcc	<u>577</u>	<u>13.9</u>	576	14.0	579	13.9	460	17.5	459	17.6	<u>460</u>	<u>17.5</u>
429.mcf	<u>475</u>	<u>19.2</u>	475	19.2	476	19.2	445	20.5	<u>446</u>	<u>20.5</u>	448	20.4
445.gobmk	<u>615</u>	<u>17.1</u>	614	17.1	615	17.1	546	19.2	546	19.2	<u>546</u>	<u>19.2</u>
456.hmmer	<u>669</u>	<u>13.9</u>	669	13.9	669	13.9	386	24.2	385	24.2	<u>386</u>	<u>24.2</u>
458.sjeng	<u>777</u>	<u>15.6</u>	776	15.6	778	15.6	<u>698</u>	<u>17.3</u>	695	17.4	699	17.3
462.libquantum	165	126	<u>163</u>	<u>127</u>	163	127	146	142	<u>145</u>	<u>143</u>	144	144
464.h264ref	824	26.8	809	27.3	<u>810</u>	<u>27.3</u>	<u>761</u>	<u>29.1</u>	762	29.1	761	29.1
471.omnetpp	<u>473</u>	<u>13.2</u>	489	12.8	472	13.2	440	14.2	<u>439</u>	<u>14.2</u>	439	14.2
473.astar	<u>552</u>	<u>12.7</u>	551	12.7	561	12.5	499	14.1	<u>499</u>	<u>14.1</u>	505	13.9
483.xalanbmk	316	21.8	<u>316</u>	<u>21.9</u>	316	21.9	316	21.8	<u>316</u>	<u>21.9</u>	316	21.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:

Hardware Prefetcher: Enabled

Adjacent Sector Prefetch: Enabled

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.xml>

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For other inquiries, please contact webmaster@spec.org.

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