



# SPEC® CINT2006 Result

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## Supermicro

(Test Sponsor: Intel Corporation)

Supermicro X7DB8+ (Intel Xeon processor X5365, 3.00 GHz)

SPECint®2006 = 24.5

SPECint\_base2006 = 21.1

CPU2006 license: 13

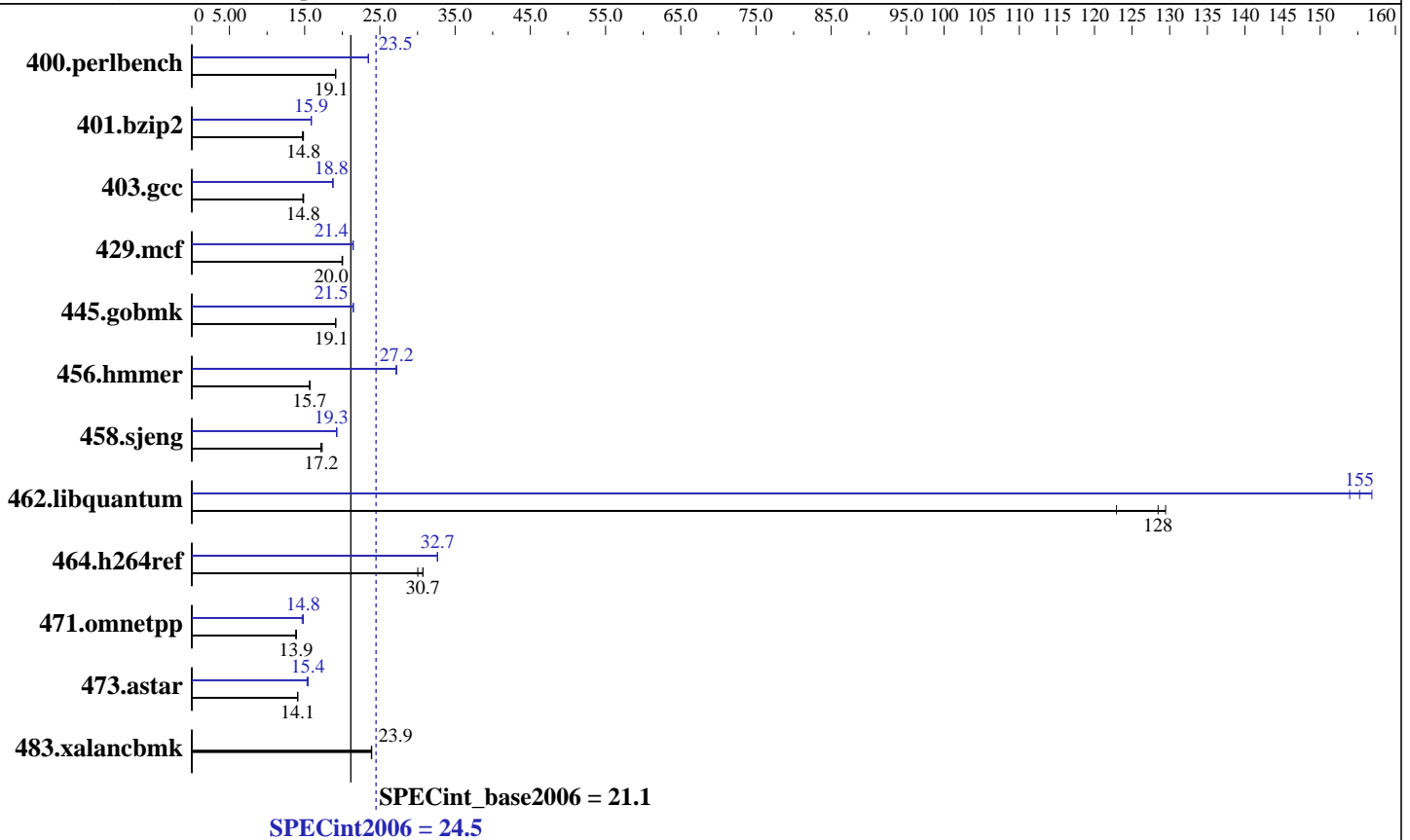
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5365  
 CPU Characteristics: Quad Core, 3.0 GHz  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 \* 2GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)  
 Disk Subsystem: Seagate, SCSI, 73GB, 10Krpm, 1 disk only  
 Other Hardware: None

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10 SP1 RC1, Kernel  
 linux-cbmg 2.6.16.43-0.5-smp for x86\_64  
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 Build 20070725  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap library V8.1  
 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>511</u>	<u>19.1</u>	512	19.1	511	19.1	<u>416</u>	<u>23.5</u>	416	23.5	417	23.4
401.bzip2	<u>651</u>	<u>14.8</u>	650	14.8	657	14.7	609	15.9	606	15.9	<u>608</u>	<u>15.9</u>
403.gcc	541	14.9	545	14.8	<u>544</u>	<u>14.8</u>	<u>429</u>	<u>18.8</u>	429	18.8	429	18.8
429.mcf	457	20.0	<u>456</u>	<u>20.0</u>	455	20.1	425	21.5	<u>426</u>	<u>21.4</u>	426	21.4
445.gobmk	549	19.1	<u>549</u>	<u>19.1</u>	548	19.1	489	21.5	489	21.5	<u>489</u>	<u>21.5</u>
456.hmmer	<u>595</u>	<u>15.7</u>	594	15.7	596	15.7	343	27.2	344	27.2	<u>343</u>	<u>27.2</u>
458.sjeng	<u>703</u>	<u>17.2</u>	698	17.3	705	17.2	629	19.2	<u>628</u>	<u>19.3</u>	628	19.3
462.libquantum	169	123	<u>161</u>	<u>128</u>	160	129	135	154	132	157	<u>133</u>	<u>155</u>
464.h264ref	737	30.0	720	30.7	<u>721</u>	<u>30.7</u>	678	32.6	677	32.7	<u>678</u>	<u>32.7</u>
471.omnetpp	450	13.9	<u>451</u>	<u>13.9</u>	452	13.8	422	14.8	426	14.7	<u>423</u>	<u>14.8</u>
473.astar	499	14.1	<u>499</u>	<u>14.1</u>	498	14.1	<u>455</u>	<u>15.4</u>	454	15.4	458	15.3
483.xalanbmk	289	23.9	289	23.9	<u>289</u>	<u>23.9</u>	289	23.9	289	23.9	<u>289</u>	<u>23.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Bios settings:

Hardware Prefetcher: Enabled

Adjacent Sector Prefetch: Enabled

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalanbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

-fast -vec-guard-write -parallel -par-runtime-control

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include

456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc  
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib  
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

Continued on next page



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## Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch  
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive  
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch  
-opt-streaming-stores always -vec-guard-write  
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

### C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

### C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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