



SPEC® CINT2006 Result

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Lenovo Group Limited

(Test Sponsor: Intel Corporation)

Lenovo R630 G7 (Intel Xeon processor E7220, 2.93 GHz)

SPECint®2006 = 23.9

SPECint_base2006 = 20.9

CPU2006 license: 13

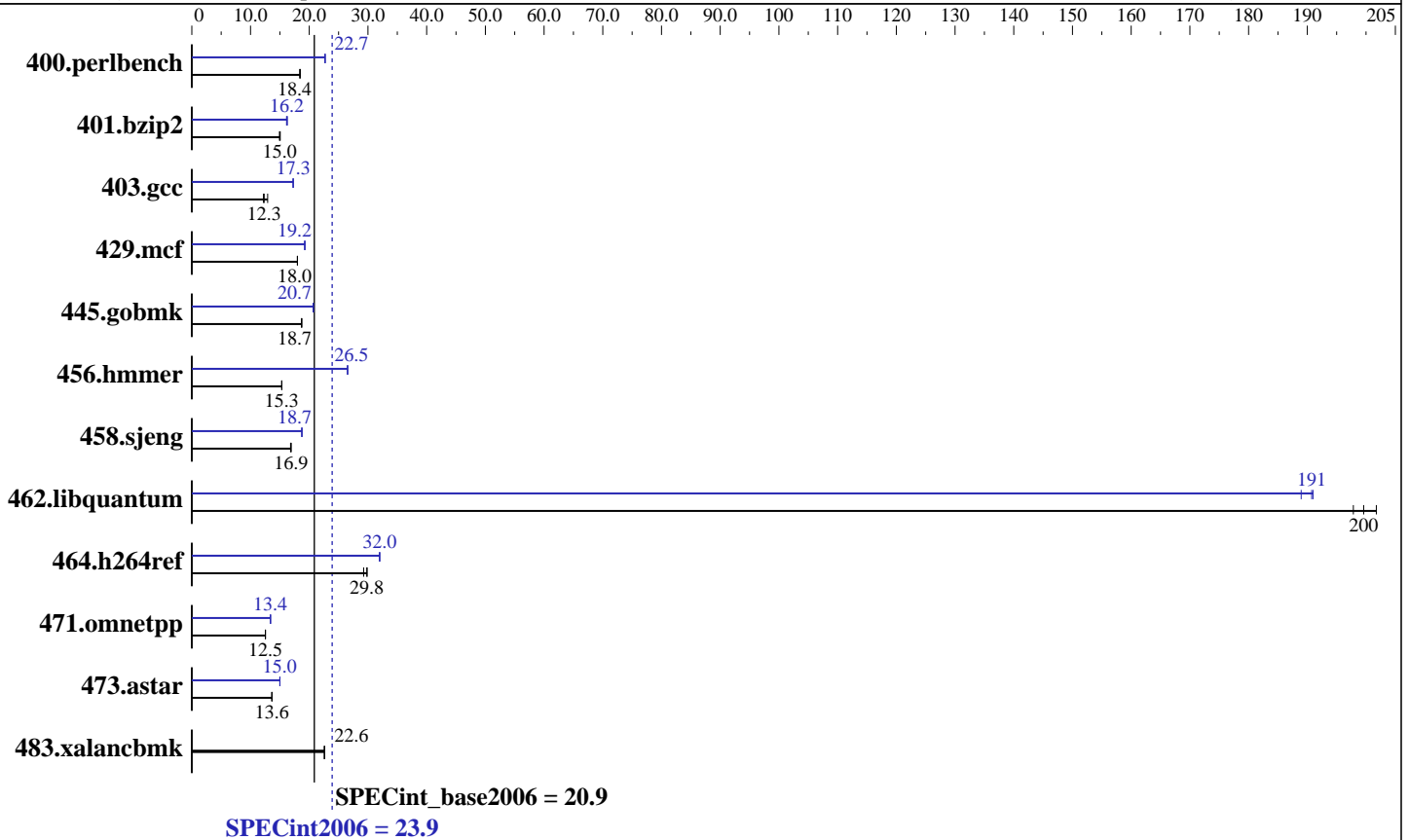
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2007

Hardware Availability: Oct-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E7220
 CPU Characteristics: Dual Core, 2.93 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip
 CPU(s) orderable: 1,2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per core
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (16 * 1GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: Seagate, SAS, 73GB, 10Krpm, 1 disk only
 Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10 SP1 RC1, Kernel
 linux-cbgn 2.6.16.43-0.5-smp for x86_64
 Compiler: Intel C++ Compiler for Linux32 and Linux64 Version 10.1 Build 20070725
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, run level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap library V8.1
 Binutils 2.17.50.0.15



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	531	18.4	<u>531</u>	<u>18.4</u>	529	18.5	<u>430</u>	<u>22.7</u>	431	22.7	430	22.7
401.bzip2	645	15.0	641	15.1	<u>645</u>	<u>15.0</u>	598	16.1	<u>595</u>	<u>16.2</u>	593	16.3
403.gcc	623	12.9	660	12.2	<u>653</u>	<u>12.3</u>	<u>466</u>	<u>17.3</u>	466	17.3	467	17.2
429.mcf	<u>507</u>	<u>18.0</u>	507	18.0	507	18.0	<u>474</u>	<u>19.2</u>	473	19.3	474	19.2
445.gobmk	561	18.7	561	18.7	<u>561</u>	<u>18.7</u>	507	20.7	<u>507</u>	<u>20.7</u>	507	20.7
456.hmmer	609	15.3	609	15.3	<u>609</u>	<u>15.3</u>	352	26.5	352	26.5	<u>352</u>	<u>26.5</u>
458.sjeng	716	16.9	719	16.8	<u>716</u>	<u>16.9</u>	645	18.8	<u>646</u>	<u>18.7</u>	646	18.7
462.libquantum	103	202	105	198	<u>104</u>	<u>200</u>	108	191	<u>109</u>	<u>191</u>	110	189
464.h264ref	<u>743</u>	<u>29.8</u>	741	29.9	756	29.3	<u>691</u>	<u>32.0</u>	693	32.0	691	32.0
471.omnetpp	<u>499</u>	<u>12.5</u>	499	12.5	498	12.5	<u>466</u>	<u>13.4</u>	467	13.4	466	13.4
473.astar	515	13.6	514	13.7	<u>515</u>	<u>13.6</u>	468	15.0	468	15.0	<u>468</u>	<u>15.0</u>
483.xalancbmk	305	22.6	306	22.6	<u>305</u>	<u>22.6</u>	305	22.6	306	22.6	<u>305</u>	<u>22.6</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Bios settings:

Hardware Prefetcher: Disabled

Adjacent Sector Prefetch: Disabled

High Bandwidth Option: Disabled

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

`-fast -vec-guard-write -parallel -par-runtime-control`

C++ benchmarks:

`-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc`

`401.bzip2: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include`

`456.hmmer: /home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc
-L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib
-I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include`

C++ benchmarks:

`icpc`

Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive
-auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.xml>

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For other inquiries, please contact webmaster@spec.org.

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