SPEC® CINT2006 Result

Lenovo Group Limited

(Test Sponsor: Intel Corporation)

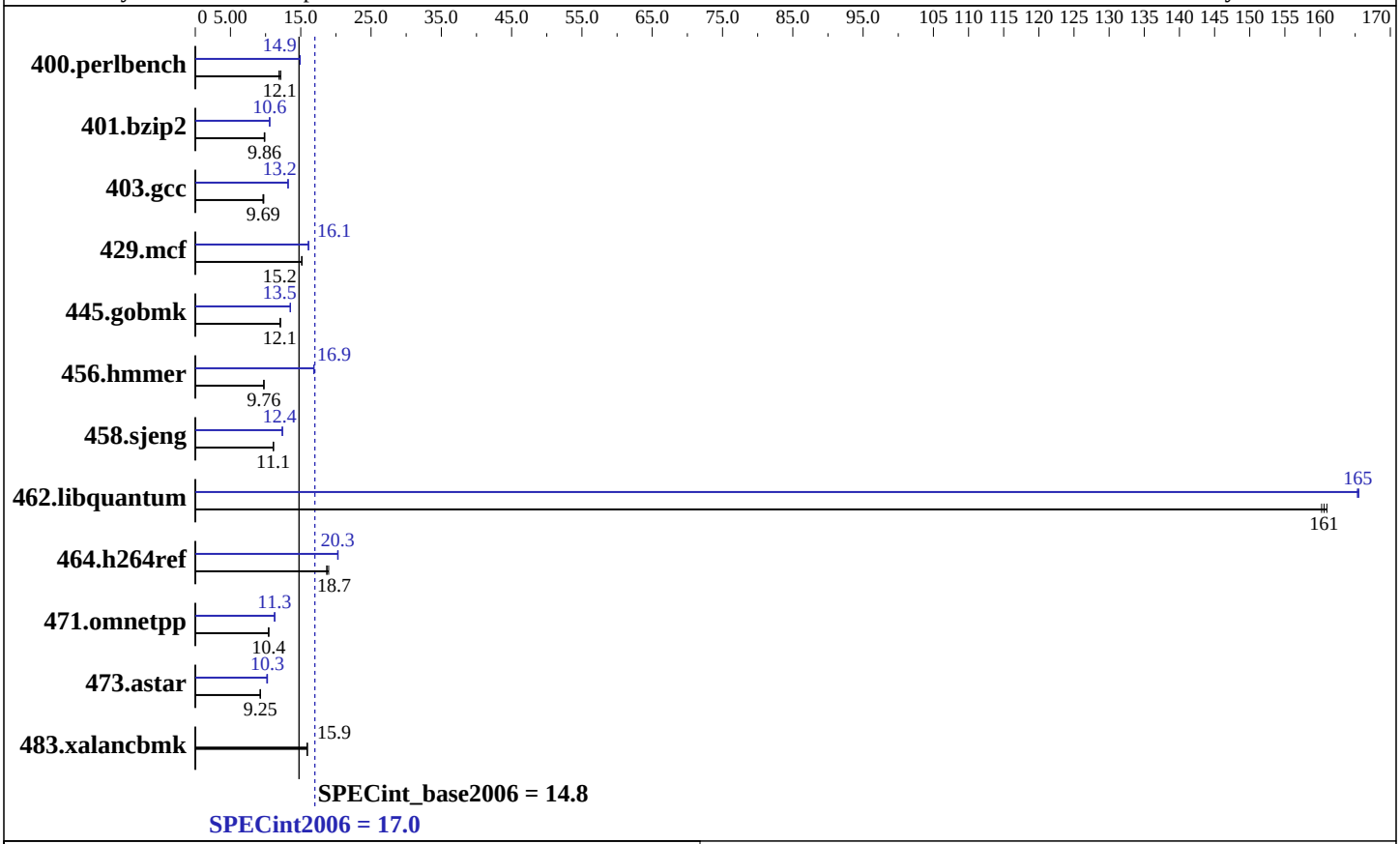
Lenovo R630 G7 (Intel Xeon processor L7345, 1.86 GHz)

SPECint®2006 = 17.0

SPECint_base2006 = 14.8

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2007
Hardware Availability: Oct-2007
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon L7345
CPU Characteristics:	Quad Core, 1.86 GHz
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	1,2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (16 * 1GB Samsung DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	Seagate, SAS, 73GB, 10Krpm, 1 disk only
Other Hardware:	None

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 SP1 RC1, Kernel linux-cbgm 2.6.16.43-0.5-smp for x86_64
Compiler:	Intel C++ Compiler for Linux32 and Linux64 Version 10.1 Build 20070725
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1 Binutils 2.17.50.0.15

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	805	12.1	**806**	**12.1**	821	11.9	**655**	**14.9**	655	14.9	654	14.9
401.bzip2	**979**	**9.86**	984	9.81	976	9.88	914	10.6	911	10.6	**912**	**10.6**
403.gcc	**830**	**9.69**	836	9.62	826	9.75	610	13.2	**610**	**13.2**	610	13.2
429.mcf	**602**	**15.2**	601	15.2	602	15.1	**567**	**16.1**	566	16.1	567	16.1
445.gobmk	867	12.1	**867**	**12.1**	868	12.1	776	13.5	**776**	**13.5**	776	13.5
456.hmmer	956	9.76	957	9.75	**956**	**9.76**	552	16.9	553	16.9	**553**	**16.9**
458.sjeng	1086	11.1	1092	11.1	**1089**	**11.1**	978	12.4	977	12.4	**977**	**12.4**
462.libquantum	129	161	129	160	**129**	**161**	125	166	**125**	**165**	125	165
464.h264ref	1183	18.7	1167	19.0	**1182**	**18.7**	**1091**	**20.3**	1093	20.3	1089	20.3
471.omnetpp	**599**	**10.4**	598	10.4	600	10.4	**554**	**11.3**	555	11.3	553	11.3
473.astar	**759**	**9.25**	759	9.24	759	9.26	691	10.2	**685**	**10.3**	684	10.3
483.xalancbmk	433	15.9	**433**	**15.9**	434	15.9	433	15.9	**433**	**15.9**	434	15.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios settings:
Hardware Prefetcher:      Disabled
Adjacent Sector Prefetch: Disabled
High Bandwidth Option:    Disabled
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -vec-guard-write -parallel -par-runtime-control
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include`

456.hmmer: `/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/bin/icc -L/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/lib -I/home/cmplr/usr3/alrahate/compilers/ic10.1mainline/20070824/Linux64/include`

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive -auto-ilp32`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.28.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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