



# SPEC<sup>®</sup> CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

NovaScale R440  
(Intel Xeon processor E5335, 2.00GHz)

SPECfp<sup>®</sup>\_rate2006 = 52.1

SPECfp\_rate\_base2006 = 51.4

CPU2006 license: 20

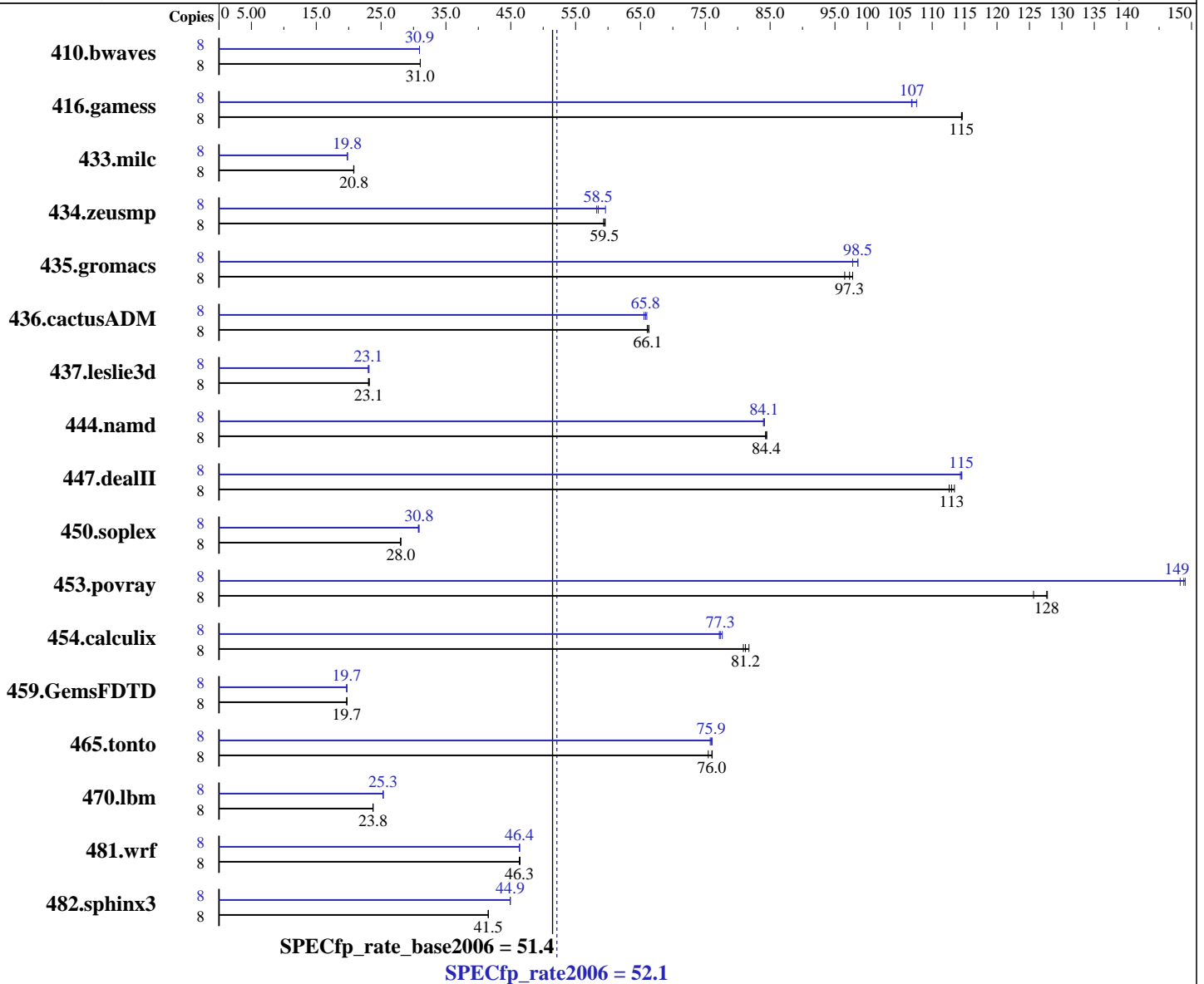
Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Aug-2007

Hardware Availability: Mar-2007

Software Availability: May-2007



### Hardware

CPU Name: Intel Xeon E5335  
 CPU Characteristics: 2.00 GHz, 8 MB L2, 1333 MHz system bus  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1 to 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

### Software

Operating System: SUSE LINUX Enterprise Server 10  
 Kernel 2.6.16.21-0.8-smp for x86\_64  
 Compiler: Intel C++ Compiler for IA32/EM64T application version 10.0  
 Build 20070426 Package ID: l\_cc\_p\_10.0.023  
 Intel Fortran Compiler for IA32/EM64T application version 10.0  
 Build 20070426 Package ID: l\_fc\_p\_10.0.023  
 Auto Parallel: No  
 File System: ext2

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

NovaScale R440  
(Intel Xeon processor E5335, 2.00GHz)

SPECfp\_rate2006 = 52.1

SPECfp\_rate\_base2006 = 51.4

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: Bull SAS

Test date: Aug-2007

Hardware Availability: Mar-2007

Software Availability: May-2007

L3 Cache: None  
Other Cache: None  
Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5  
Disk Subsystem: 1x73 GB SAS, 15000 RPM  
Other Hardware: None

System State: Multi-user run level 3  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: Binutils 2.17.50.0.15

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3502	31.0	3502	31.0	<b>3502</b>	<b>31.0</b>	8	<b>3516</b>	<b>30.9</b>	3516	30.9	3516	30.9
416.gamess	8	1368	115	<b>1368</b>	<b>115</b>	1366	115	8	1456	108	1466	107	<b>1465</b>	<b>107</b>
433.milc	8	<b>3531</b>	<b>20.8</b>	3530	20.8	3536	20.8	8	<b>3709</b>	<b>19.8</b>	3706	19.8	3710	19.8
434.zeusmp	8	1227	59.3	1222	59.6	<b>1224</b>	<b>59.5</b>	8	1250	58.3	1221	59.6	<b>1245</b>	<b>58.5</b>
435.gromacs	8	584	97.7	592	96.5	<b>587</b>	<b>97.3</b>	8	580	98.5	584	97.7	<b>580</b>	<b>98.5</b>
436.cactusADM	8	1447	66.0	<b>1446</b>	<b>66.1</b>	1441	66.3	8	1449	66.0	<b>1453</b>	<b>65.8</b>	1458	65.6
437.leslie3d	8	<b>3257</b>	<b>23.1</b>	3262	23.1	3241	23.2	8	<b>3262</b>	<b>23.1</b>	3265	23.0	3249	23.1
444.namd	8	<b>760</b>	<b>84.4</b>	761	84.3	759	84.5	8	<b>763</b>	<b>84.1</b>	763	84.1	764	84.0
447.dealII	8	<b>810</b>	<b>113</b>	807	113	813	113	8	800	114	799	115	<b>799</b>	<b>115</b>
450.soplex	8	<b>2381</b>	<b>28.0</b>	2379	28.0	2383	28.0	8	2170	30.7	2163	30.8	<b>2163</b>	<b>30.8</b>
453.povray	8	339	126	<b>333</b>	<b>128</b>	333	128	8	287	148	<b>286</b>	<b>149</b>	286	149
454.calculix	8	816	80.9	807	81.7	<b>813</b>	<b>81.2</b>	8	850	77.6	855	77.2	<b>853</b>	<b>77.3</b>
459.GemsFDTD	8	4306	19.7	<b>4306</b>	<b>19.7</b>	4306	19.7	8	4312	19.7	4302	19.7	<b>4307</b>	<b>19.7</b>
465.tonto	8	<b>1036</b>	<b>76.0</b>	1043	75.5	1035	76.1	8	<b>1037</b>	<b>75.9</b>	1039	75.8	1035	76.1
470.lbm	8	<b>4626</b>	<b>23.8</b>	4629	23.7	4624	23.8	8	4341	25.3	4341	25.3	<b>4341</b>	<b>25.3</b>
481.wrf	8	1924	46.4	1929	46.3	<b>1929</b>	<b>46.3</b>	8	<b>1928</b>	<b>46.4</b>	1928	46.4	1929	46.3
482.sphinx3	8	<b>3754</b>	<b>41.5</b>	3753	41.5	3754	41.5	8	<b>3471</b>	<b>44.9</b>	3471	44.9	3470	44.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
'/usr/bin/taskset' used to bind processes to CPUs

## General Notes

All binaries were built with 64-bit Intel compiler except:  
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with  
32-bit Intel compiler by changing the path for include and library files.

The NovaScale R440 and the NovaScale R460 models are  
electronically equivalent.  
The results have been measured on a NovaScale R460 model.



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

NovaScale R440  
(Intel Xeon processor E5335, 2.00GHz)

SPECfp\_rate2006 = 52.1

SPECfp\_rate\_base2006 = 51.4

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** Bull SAS

**Test date:** Aug-2007  
**Hardware Availability:** Mar-2007  
**Software Availability:** May-2007

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

Benchmarks using both Fortran and C:  
icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-fast

Fortran benchmarks:  
-fast

Benchmarks using both Fortran and C:  
-fast



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

NovaScale R440  
(Intel Xeon processor E5335, 2.00GHz)

SPECfp\_rate2006 = 52.1

SPECfp\_rate\_base2006 = 51.4

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** Bull SAS

**Test date:** Aug-2007  
**Hardware Availability:** Mar-2007  
**Software Availability:** May-2007

## Peak Compiler Invocation

### C benchmarks:

```
/opt/intel/cc/10.0.023/bin/icc -L/opt/intel/cc/10.0.023/lib  
-I/opt/intel/cc/10.0.023/include
```

### C++ benchmarks (except as noted below):

icpc

```
450.soplex: /opt/intel/cc/10.0.023/bin/icpc -L/opt/intel/cc/10.0.023/lib  
-I/opt/intel/cc/10.0.023/include
```

### Fortran benchmarks (except as noted below):

ifort

```
434.zeusmp: /opt/intel/fc/10.0.023/bin/ifort -L/opt/intel/fc/10.0.023/lib  
-I/opt/intel/fc/10.0.023/include
```

### Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.deallI: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

### C benchmarks:

```
433.milc: -prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32
```

```
470.lbm: Same as 433.milc
```

```
482.sphinx3: -fast -auto_ilp32
```

### C++ benchmarks:

```
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Bull SAS

NovaScale R440  
(Intel Xeon processor E5335, 2.00GHz)

SPECfp\_rate2006 = 52.1

SPECfp\_rate\_base2006 = 51.4

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** Bull SAS

**Test date:** Aug-2007  
**Hardware Availability:** Mar-2007  
**Software Availability:** May-2007

## Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: `-prof_gen(pass 1) -prof_use(pass 2) -fast`

416.gamess: Same as 410.bwaves

434.zeusmp: `-fast`

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: Same as 410.bwaves

Benchmarks using both Fortran and C:

`-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32`

The flags file that was used to format this result can be browsed at

[http://www.spec.org/cpu2006/flags/EM64T\\_Intel100\\_flags.20090714.html](http://www.spec.org/cpu2006/flags/EM64T_Intel100_flags.20090714.html)

You can also download the XML flags source by saving the following link:

[http://www.spec.org/cpu2006/flags/EM64T\\_Intel100\\_flags.20090714.xml](http://www.spec.org/cpu2006/flags/EM64T_Intel100_flags.20090714.xml)

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.0.  
Report generated on Tue Jul 22 14:58:02 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 16 October 2007.